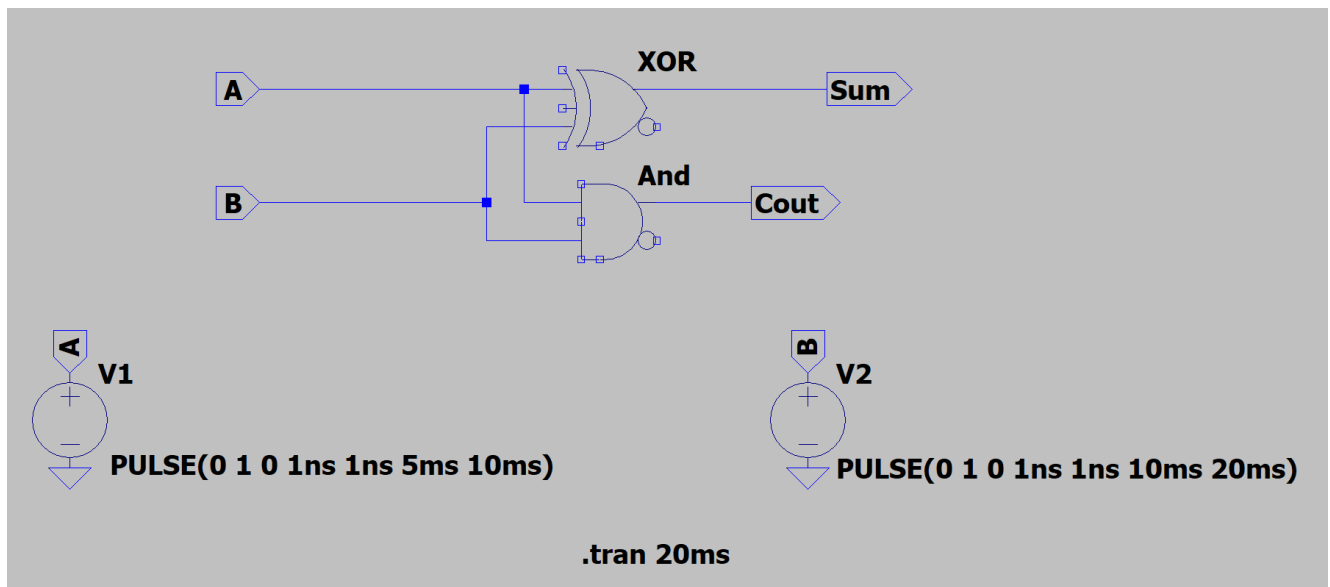# Lab – 5

ECN – 252

19114001 | Abhinav Saini
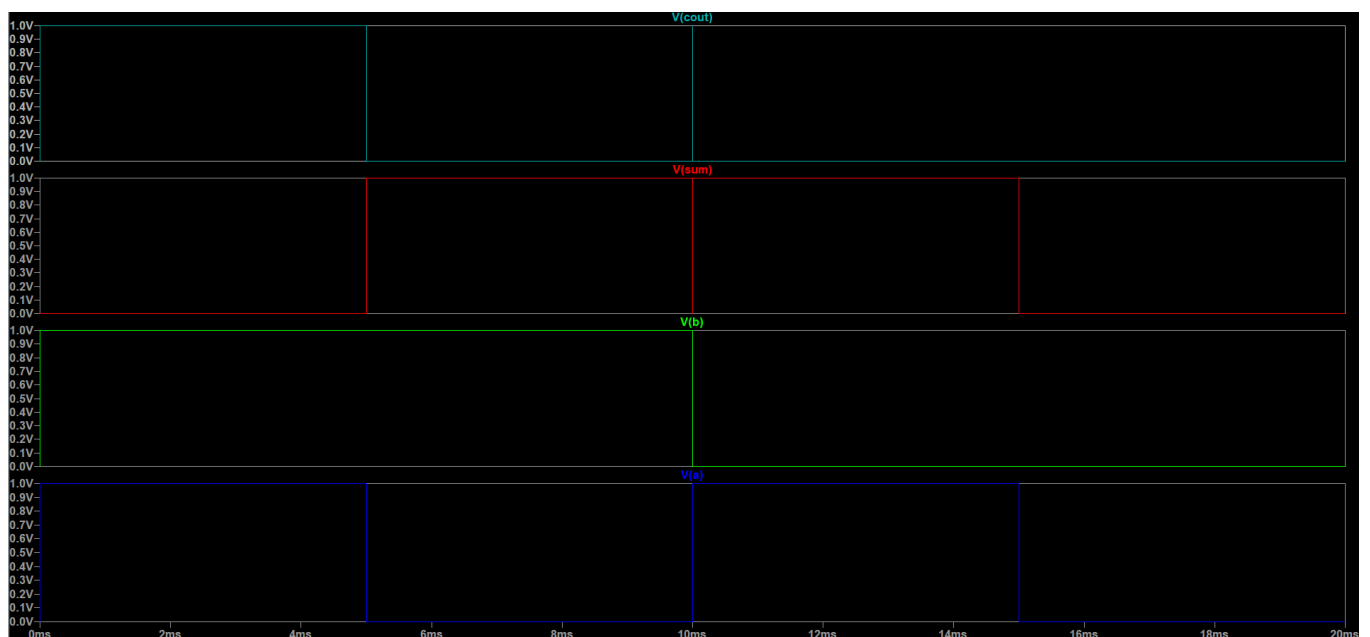
Part 1 :

Half – Adder
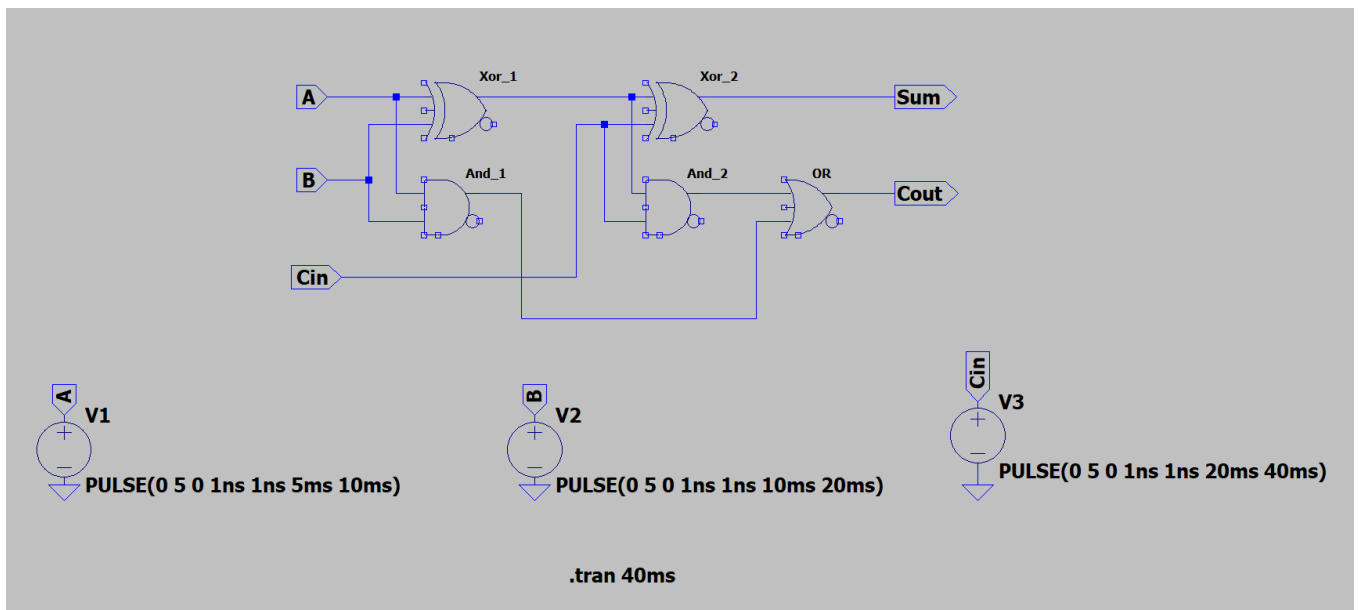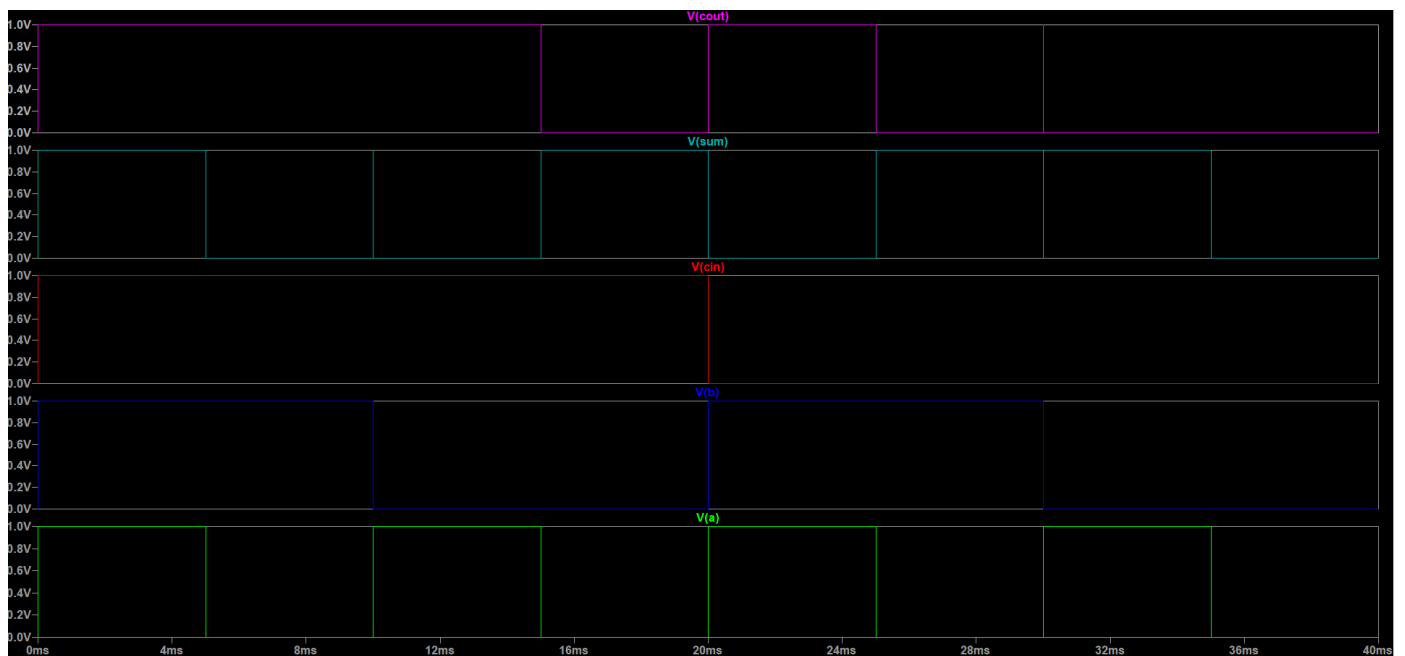
Schematic

Simulation

Full – Adder

Schematic

Simulation

Part – 2 : 4-bit Multiplier

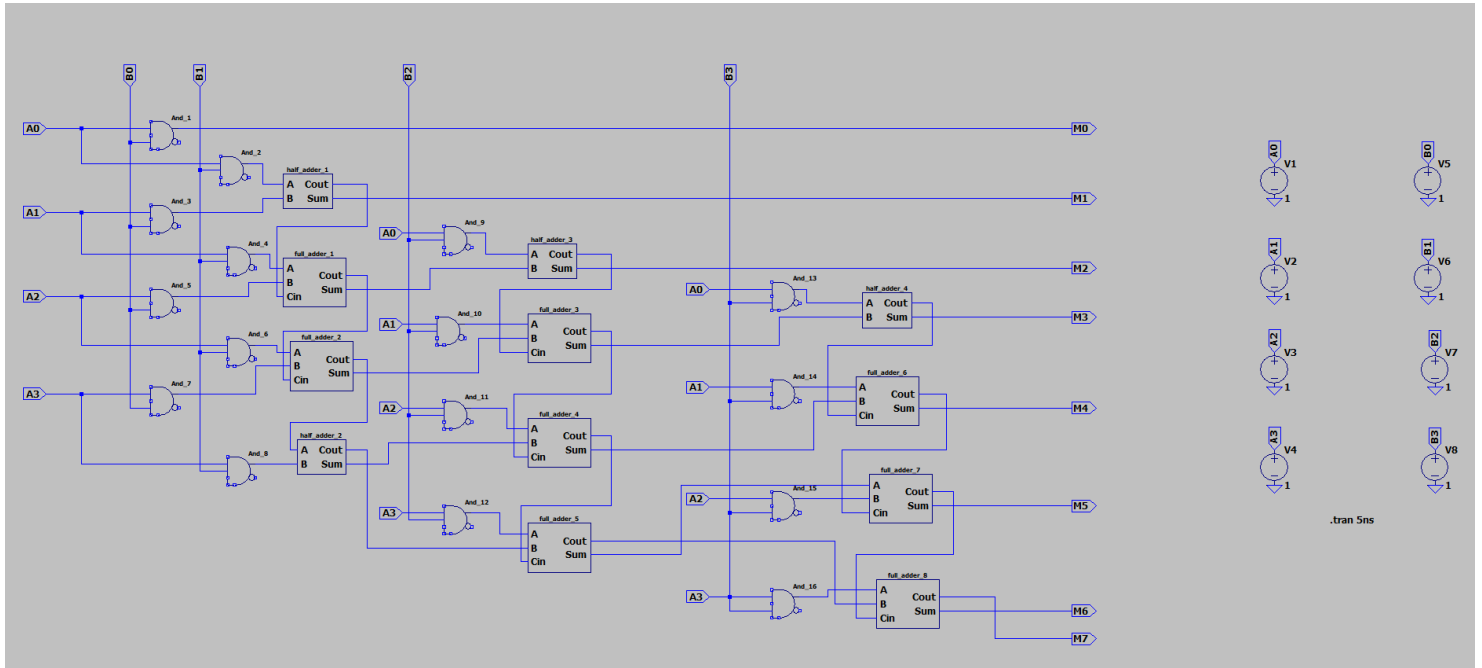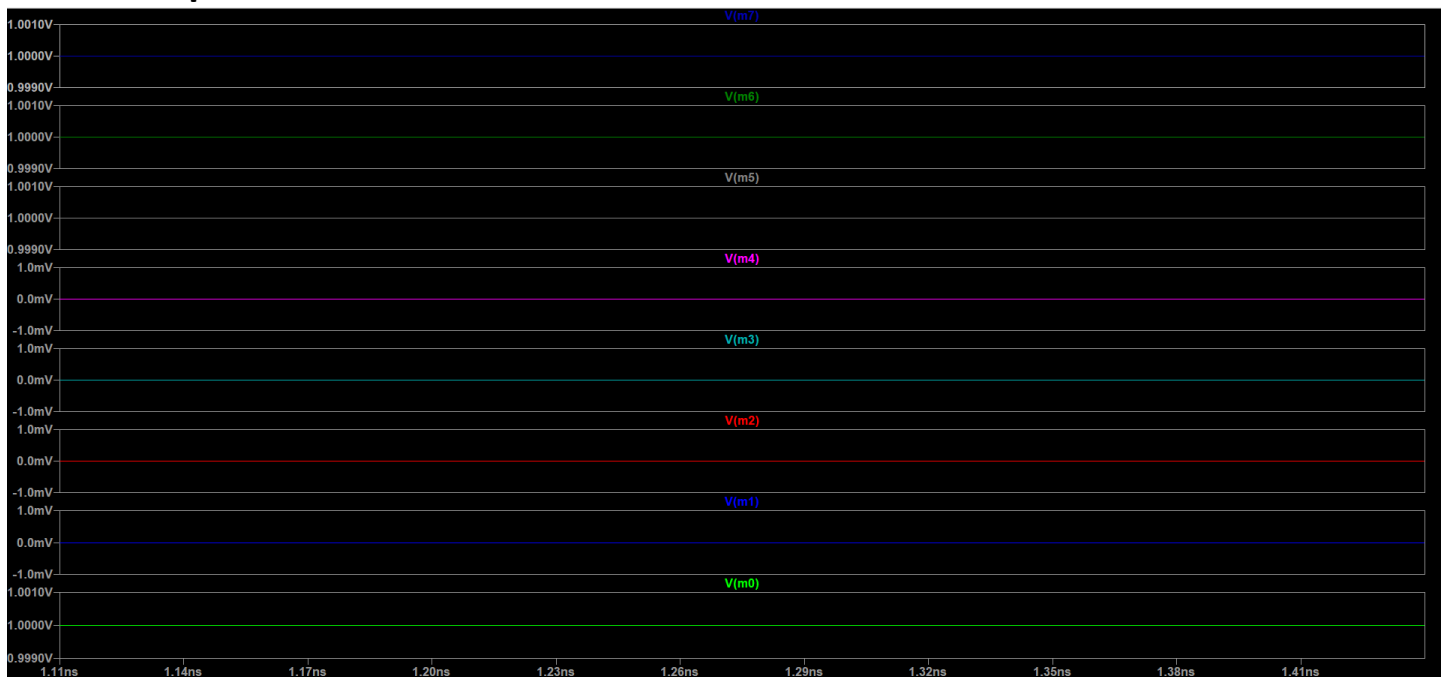### a) 1111 x 1111

Schematic

Simulation

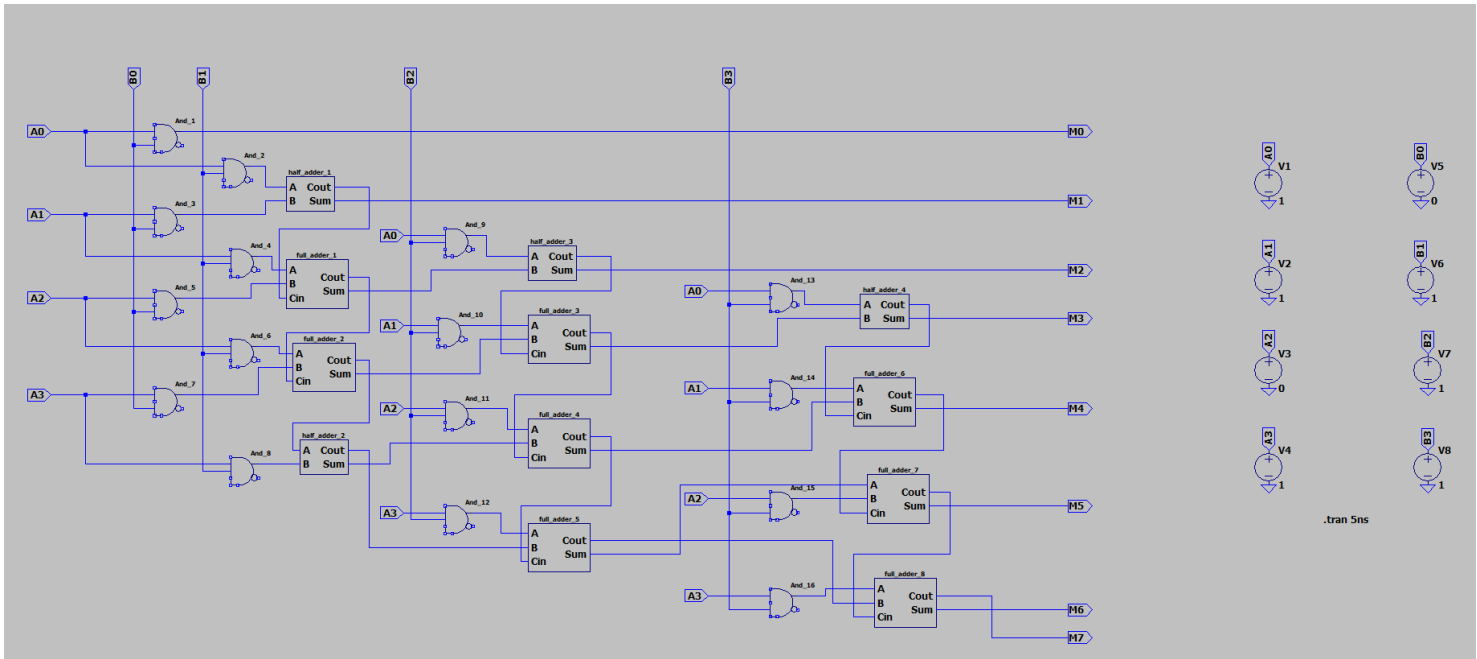### Output - 11100001

b) 1011 x 1110

Schematic

Simulation
Output - 10011010