



NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

EC732

RF CIRCUIT DESIGN TERM PROJECT
REPORT

**WIRELESS POWER TRANSFER
SIMULATION**

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENTS FOR THE DEGREE
OF MASTER OF TECHNOLOGY

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Abstract

A rectenna is a type of antenna that is designed to convert radio frequency waves into direct current (DC) power. This process is known as rectification. Rectennas are commonly used in microwave wireless power transmission systems. In this project, the design of a novel 2.45 GHz patch antenna and a rectifier with an impedance matching circuit was undertaken using MATLAB and Advanced Design System software. The results indicate that the antenna has a gain of 8.01 dB, while the rectenna produces a power output of 0.075 watts with a current of 27 mA for a load resistance of 100 ohms.

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Chapter 1

Introduction

Microwave wireless power transmission has a long history, dating back to the development of high-power microwave emitters known as cavity magnetrons during World War II. Since then, there have been many advances in the field of microwave power transmission. In 1964, William C. Brown built a miniature helicopter equipped with a rectenna, which is a combination of an antenna and a rectifier device. A rectenna typically consists of an antenna, a diode, an impedance matching circuit, a bypass capacitor, and a load. There are many types of antennas that can be used in wireless applications, but patch antennas are a popular choice due to their simplicity and effectiveness.

In this project report, the design of a patch antenna and a rectification unit with an impedance matching circuit and an RF bypass capacitor is presented. The complete design was carried out using Advanced Design System software.

Chapter 2

RECTENNA AND RECTIFIER DESIGN

PATCH ANTENNA

A patch antenna consists of a radiating patch on a substrate, with a ground plane on one side of the substrate. Substrates with low dielectric constants are generally preferred for maximum radiation. The patch can be in a rectangular or circular shape, as these configurations are easy to analyze. Other shapes are more complex and require extensive numerical calculations. A microstrip antenna is characterized by its length, width, input impedance, gain, and radiation patterns. The length of the antenna is usually around half a wavelength in the dielectric, which determines the resonant frequency of the antenna. Patch antennas are commonly used in spacecraft, aircraft, missiles, satellites, and other applications because they are lightweight, small, easy to install, and easy to fabricate, and they produce less aerodynamic drag.

FEED TECHNIQUE

There are several different techniques that can be used to connect a coaxial cable to a microstrip patch antenna. These techniques can be divided into two categories: contacting and non-contacting. Coaxial feed and microstrip feed are examples of contacting techniques, while aperture coupling and proximity coupling are examples of non-contacting techniques. In a contacting technique, the RF power is fed directly to the radiating patch using a connecting element such as a microstrip line. In a non-contacting technique, electromagnetic field coupling is used to transfer power between the microstrip line and the radiating patch. In this design, the microstrip feed technique is used, which involves directly connecting a microstrip line to the radiating patch

MICRO STRIP FEED

The microstrip feed line consists of a conducting strip that is narrower than the radiating patch. It is easy to fabricate and can be easily matched by adjusting the inset position. The microstrip feed line can be etched onto the same substrate as the patch antenna, resulting in a planar structure. The inset cut in the patch is used to match the impedance of the feed line to the patch without the need for additional matching elements. This is achieved by carefully controlling the position of the insert.

DESIGN OF PATCH ANTENNA

The micro strip patch antenna is operating at 2.45GHz using the substrate material RT5880. The dielectric constant of the substrate is 2.2 and the thickness of the material is 1.6mm.

Width of the patch antenna is

$$W = \frac{c}{2f\sqrt{\frac{\epsilon_r+1}{2}}} \quad (2.1)$$

where

c = speed of light

f = operating frequency

ϵ_r = dielectric constant

Effective Length of the patch antenna is

$$L_{\text{eff}} = \frac{c}{2f\sqrt{\epsilon_{\text{eref}}}} \quad (2.2)$$

where

ϵ_{eref} = effective dielectric constant

$$\epsilon_{\text{ereff}} = \frac{(\epsilon_r + 1)}{2} + \frac{(\epsilon_r - 1)}{2} \left(1 + \frac{10H}{W} \right)^{-1/2}$$

Open circuit end correction length of patch antenna is

$$\Delta L = 0.412H \frac{\epsilon_{\text{ereff}} + 0.3 \left(\frac{W}{H} + 0.264 \right)}{\epsilon_{\text{ereff}} - 0.258 \left(\frac{W}{H} + 0.8 \right)}$$

The resonant length is

$$L = L_{\text{eff}} - 2\Delta L$$

Resonant input impedance is

$$Z_{\text{in}} = 90 \frac{\varepsilon_r^2}{(\varepsilon_r - 1)} \left(\frac{L}{W} \right)^2$$

The input conductance of the patch fed on the edge will be twice the conductance of one of the edge slots

$$G = \frac{\pi W}{n\lambda} \left(1 - \frac{(kH)^2}{24} \right)$$

where

$$k = \frac{2\pi}{\lambda}$$

$$R_{\text{in}} = \frac{1}{2G}$$

The inset feed point distance Y_o is obtained using

$$R_p = R_e * \cos^2 \left(\frac{\pi}{L} Y_o \right)$$

where R_e = the input resistance of the patch fed on the edge, R_p = Probe impedance.

Design of patch antenna is implemented in MATLAB, It shows the following parameters

The wavelength (λ) = 122.4490 mm

The width of the patch (W) = 48.4022 mm

The value of Effective dielectric constant (E_{eff}) = 2.1077

The Effective length of the Patch (L_{eff}) = 42.1717 mm

The extension length (ΔL) = 0.8433 mm

The actual length of patch (L) = 40.4852 mm The value of the ground plane dimensions length and width = 50.0852 mm, 58.0022 mm

The Input Impedance of the patch is (Z_{in}) = 253.9622 mm

The value of Patch edge impedance (R_{in}) = 151.8320 mm

The inset feed point distance is (Y_o) = 12.3661 mm

DESIGN ,SIMULATION AND RESULTS

Figure 1 shows the design of a 2.45 GHz patch antenna in MATLAB. The antenna consists of a radiating patch and a quarter-wave transformer feed line with a 50 ohm impedance. The patch antenna was designed using the specified dimensions, and the return losses were improved through trial and error. The edge of the patch antenna is matched to the 50 ohm feed line, as all cables have a 50 ohm impedance. The quarter-wave transformer line is used to connect the radiating patch to the microstrip line.

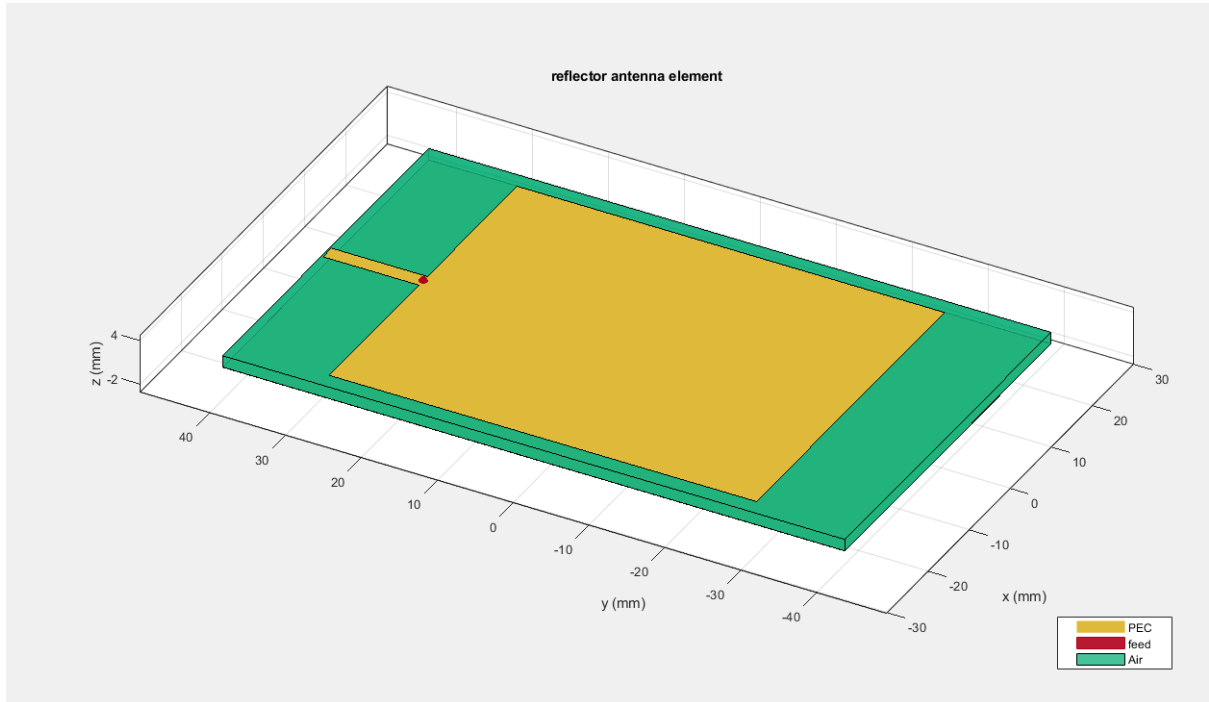


Figure 2.1: Matlab antenna designl

The results of the 2.45GHz edge feed patch antenna graph is shown in Figure below, represent s11 of antenna

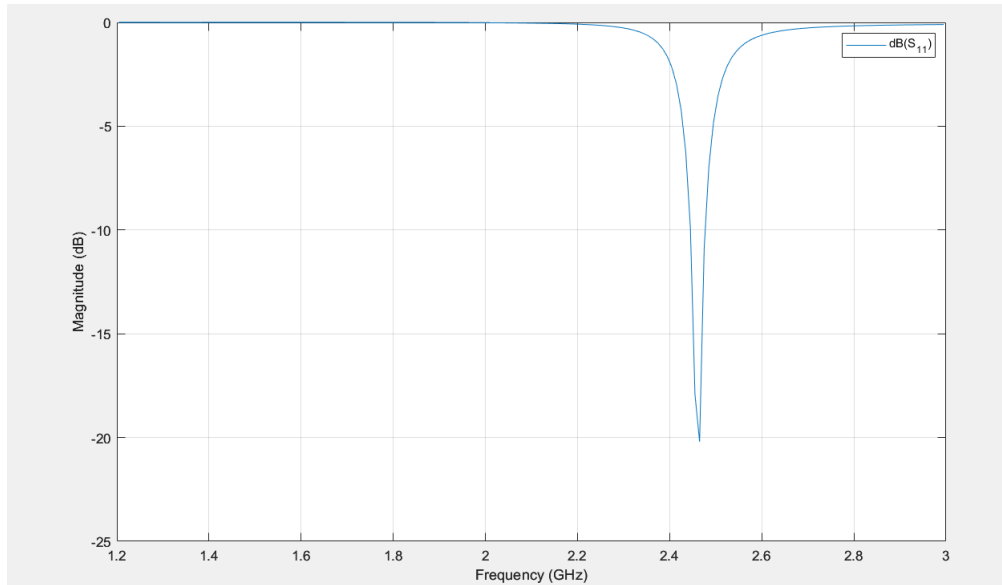


Figure 2.2: s11

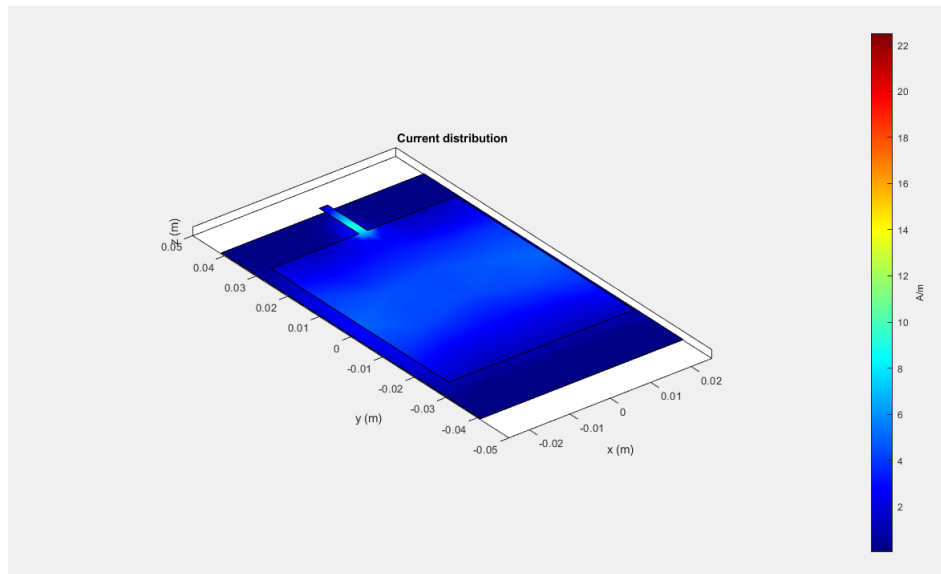


Figure 2.3: current flow

here is the radiation pattern for the antenna from Matlab.

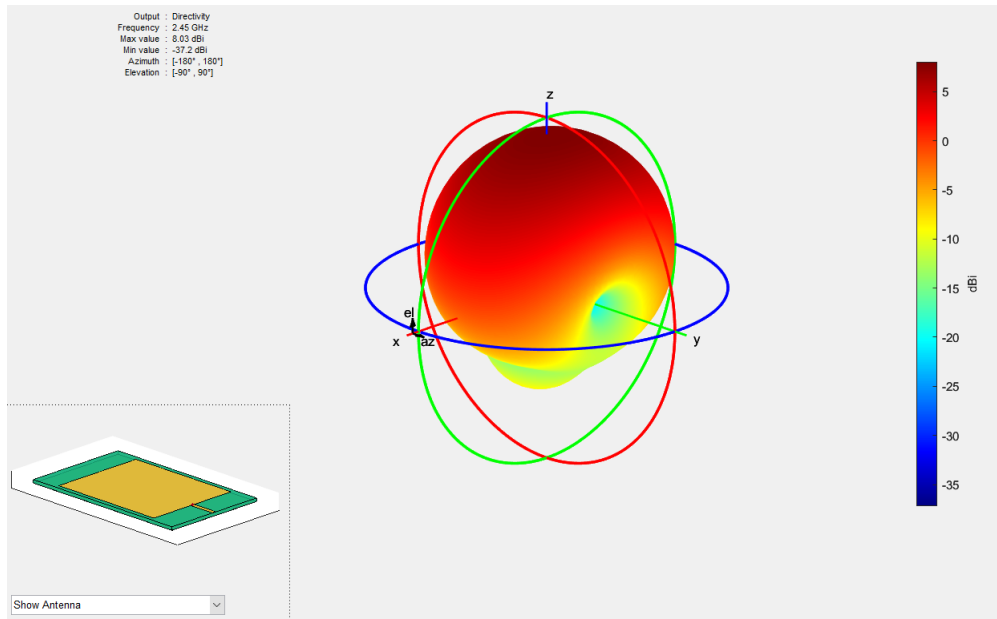


Figure 2.4: radiation pattern

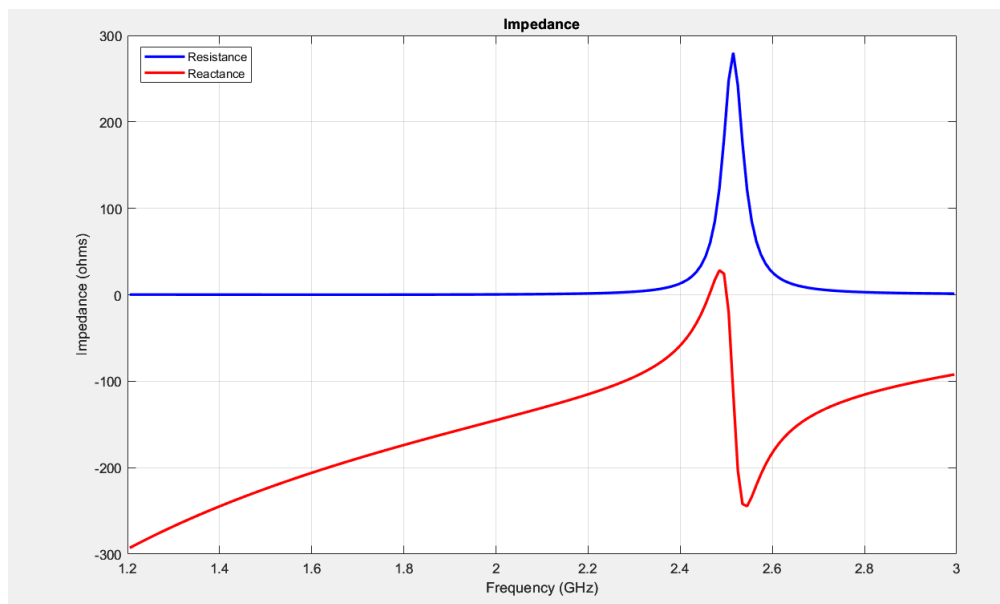


Figure 2.5: impedance

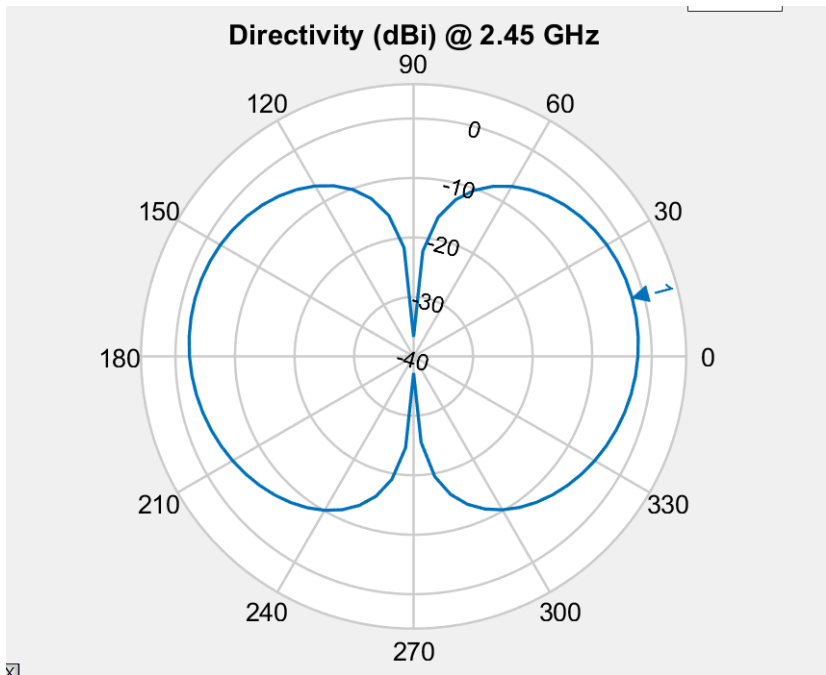


Figure 2.6: AZ plot

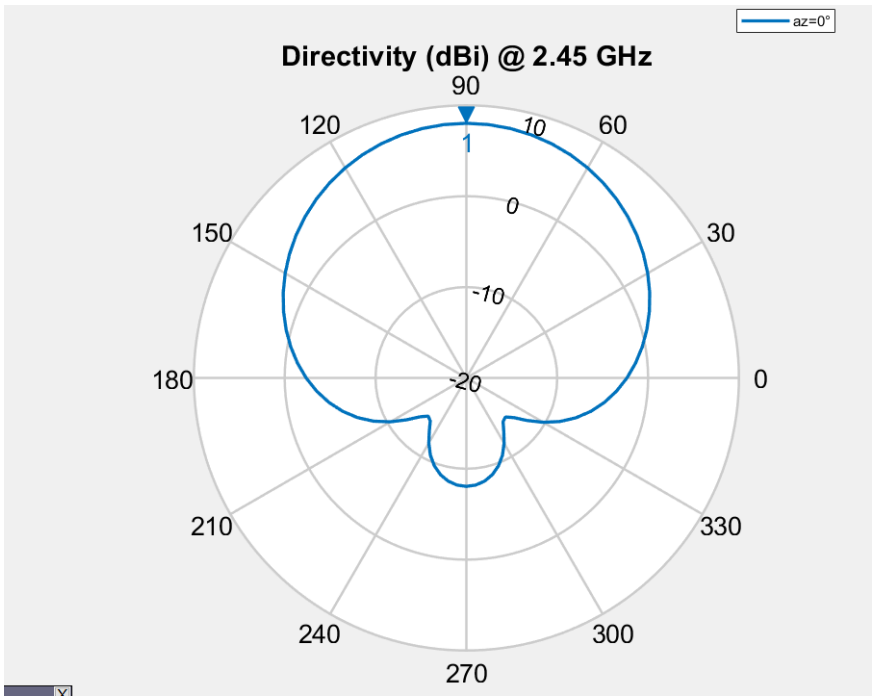


Figure 2.7: elevation plot

The Antenna parameter for Edge feed patch antenna, Peak frequency: 2.449GHz

Gain: 8.01 dB

Return loss: -20.31 dB

Bandwidth: 120MHz

RECTIFIER

The RF to DC conversion circuit consists of HSMS 2860 Schottky diode, Capacitor and impedance matching element. Schottky diode converts the RF into DC, Capacitor act as DC power storage and RF by-pass capacitor and impedance match will improve the efficiency of the Rectenna. It is developed in ADS software with the reference of HSMS 2860 Schottky diode datasheet, which is shown below figure.

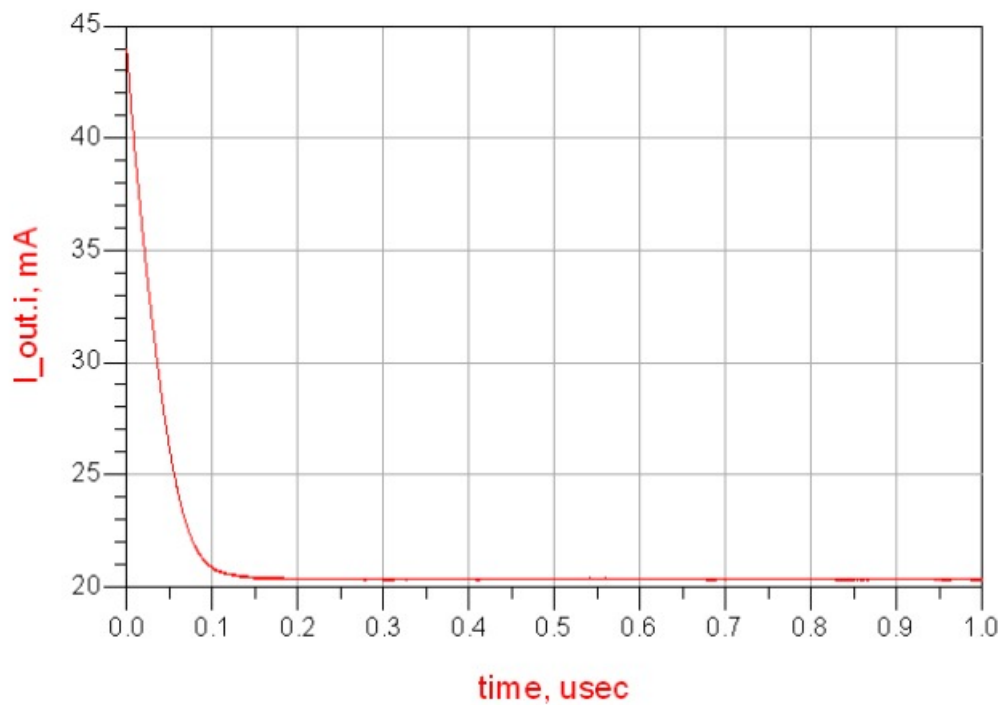


Figure 2.8: current out vs time

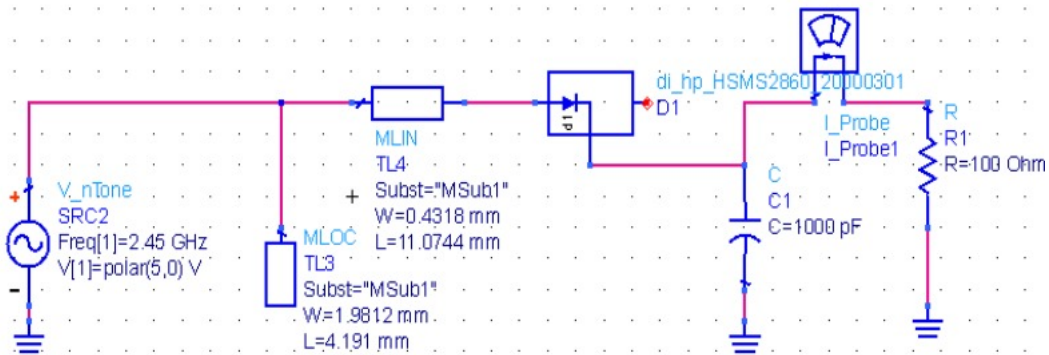


Figure 2.9: rectifier circuit

The Final ADS simulation circuit of the half-wave Rectenna design is shown in Figure below, in this, simulation produces approximately 27mA at 100Ω load and this is shown in Figure below.

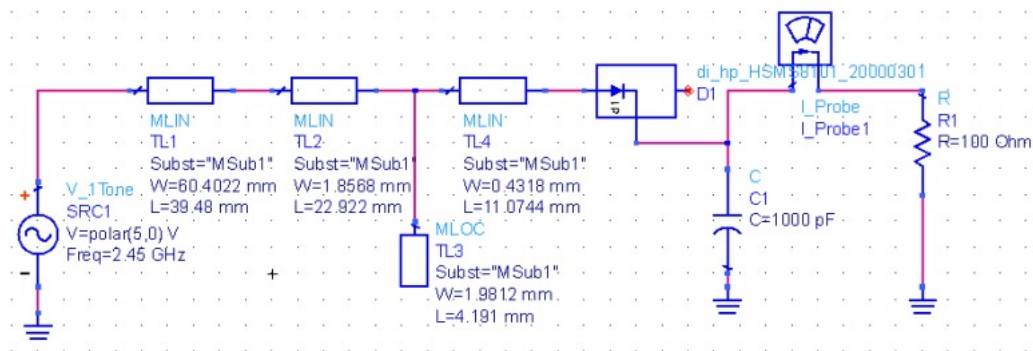


Figure 2.10: rectenna circuit

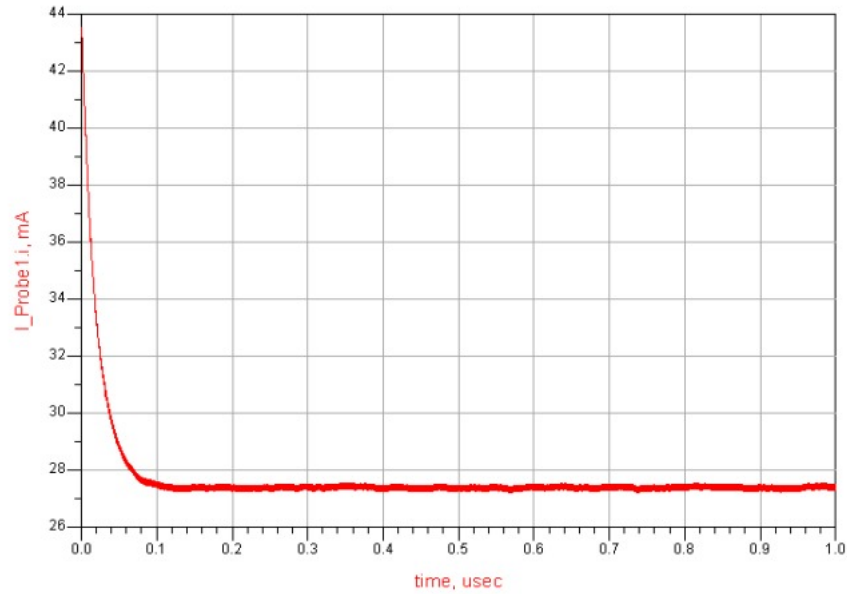


Figure 2.11: current vs time

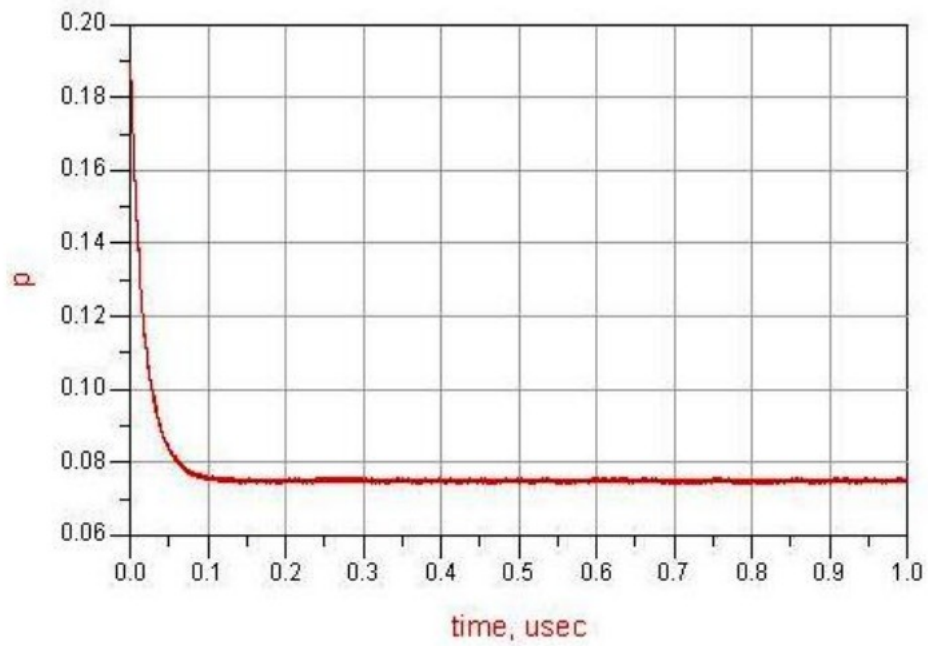


Figure 2.12: power vs time

Chapter 3

Conclusions

A complete analysis of Rectenna model for Microwave Wireless Power Transmission was made in ADS Software and matlab. It provides efficient Rectenna for Microwave Wireless Power Transmission. Novel micro strip patch antenna operating at 2.45GHz range has been designed which provide a return loss of -20.3dB. The overall Rectenna circuit current shows approximately 27mA and output power is 0.075Watts. With such kind of high performance Rectenna, the range of received power can be improved.

References

- [1] W. C. Brown, “The history of power transmission by radio waves,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 32, no. 9, pp. 1302–1310, 1984.
- [2] M. R. Moorthi and A. Saravanakumar, “Rectenna model for 2.45ghz microwave wireless power transmission,” in *2013 International Conference on Energy Efficient Technologies for Sustainability*, pp. 1360–1364, 2013.
- [3] T. A. Milligan, *Modern Antenna Design*. John Wiley Sons Inc., 2 ed., 2005.
- [4] A. Technologies, “Hsms 286x series, surface mount microwave schottky detector diode,” May 2009.
- [5] J. Zbitou and M. Latrach, “Hybrid rectenna and monolithic integrated zero-bias microwave rectifier,” *IEEE Transactions on Microwave Theory and Techniques*, vol. 54, no. 1, pp. 246–252, 2006.