

F/F characteristic equation :-

⇒ S-R F/F

S	R	Q_n	Q_{n+1}
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	?
1	1	1	?

Q_{n+1}	$\bar{R}\bar{Q}_n$	$\bar{R}Q_n$	RQ_n	$R\bar{Q}_n$
\bar{S}	0	1	0	0
S	1	1	X	X

$$Q_{n+1} = S + \bar{R}Q_n$$

$$SR = 0$$

Both S and R can not equal to 1 simultaneously

⇒ D F/F

D	Q_n	Q_{n+1}
0	0	0
0	1	0
1	0	1
1	1	1

$$Q_{n+1} = D$$

⇒ JK F/F

J	K	Q_n	Q_{n+1}
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

Q_{n+1}	$\bar{J}\bar{K}$	$\bar{J}K$	$J\bar{K}$	JK
\bar{J}	0	1	0	0
J	1	1	0	1

$$Q_{n+1} = J\bar{Q}_n + \bar{K}Q_n$$

⇒ T F/F

T	Q_n	Q_{n+1}
0	0	0
0	1	1
1	0	1
1	1	0

$$Q_{n+1} = \bar{T}Q_n + T\bar{Q}_n$$

Excitation table :- provide information to the inputs with Q_n and Q_{n+1}

⇒ SR FIF

S	R	Q _n	Q _{n+1}
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	?
1	1	1	?

Q _n	Q _{n+1}	S	R
0	0	0	X
0	1	1	0
1	0	0	1
1	1	X	0

⇒ JK FIF

J	K	Q _n	Q _{n+1}
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

Q _n	Q _{n+1}	J	K
0	0	0	X
0	1	1	X
1	0	X	1
1	1	X	0

⇒ D FIF

D	Q _n	Q _{n+1}
0	0	0
0	1	0
1	0	1
1	1	1

Q _n	Q _{n+1}	D
0	0	0
0	1	1
1	0	0
1	1	1

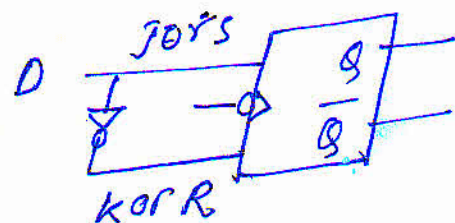
⇒ T FIF

T	Q _n	Q _{n+1}
0	0	0
0	1	1
1	0	1
1	1	0

Q _n	Q _{n+1}	T
0	0	0
0	1	1
1	0	1
1	1	0

Inter connection of FIFs. ∴

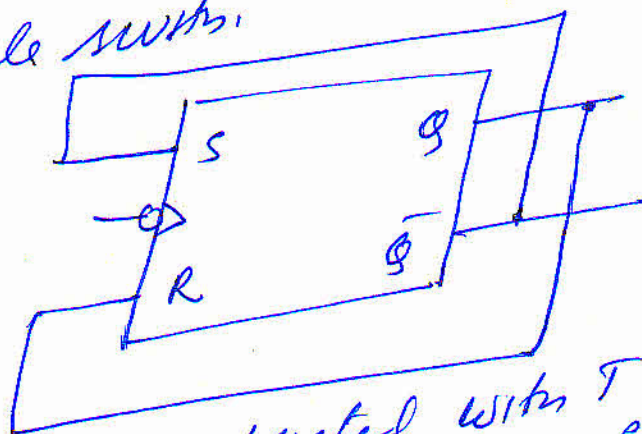
⇒ JK/RS into D



⇒ J.K into T



⇒ SR as Toggle switch.



SR F/F can't be constructed with T F/F since $S=R=1$ is forbidden, however circuit shown acts as a toggle switch, Q changes with each clock pulse.

Conversion of F/Fs :-

⇒ SR into D

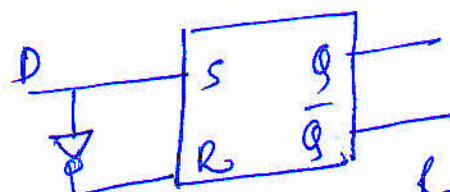
Inputs	D
Q	0
0	0
0	1
0	0
1	0
1	1

Transition	$Q_n \rightarrow Q_{n+1}$
0	0
0	1
1	0
1	1

O/Ps	S	R
0	X	0
1	0	1
X	0	0

	\bar{D}	D
\bar{Q}	0	1
Q	0	X

$S = D$ $R = \bar{D}$



⇒ SR into T

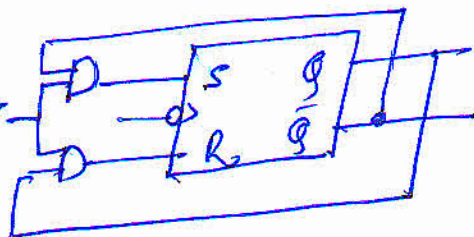
Inputs	T
Q	0
0	0
0	1
1	0
1	1

Transition	$Q_n \rightarrow Q_{n+1}$
0	0
0	1
1	1
1	0

O/Ps	S	R
0	X	0
1	0	1
X	0	0

	\bar{T}	T
\bar{Q}	0	1
Q	X	0

$S = \bar{Q}T$ $R = QT$

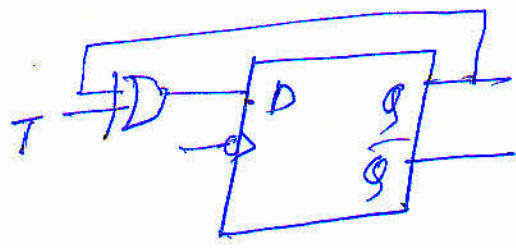


⇒ D into T

Q	T	$Q_n \rightarrow Q_{n+1}$	D
0	0	$0 \rightarrow 0$	0
0	1	$0 \rightarrow 1$	1
1	0	$1 \rightarrow 0$	1
1	1	$1 \rightarrow 1$	0

D	\bar{T}	T
\bar{Q}	0	1
Q	1	0

$$D = Q\bar{T} + \bar{Q}T = Q \oplus T$$



⇒ SR into JK

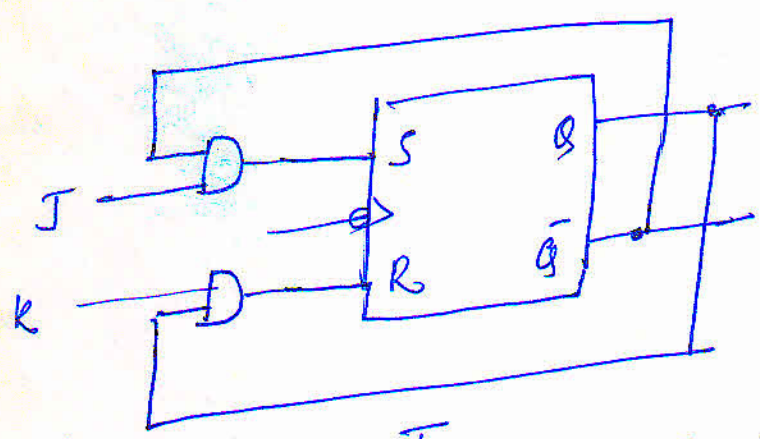
Q	J	K	$Q_n \rightarrow Q_{n+1}$	S	R
0	0	0	$0 \rightarrow 0$	0	X
0	0	1	$0 \rightarrow 0$	0	X
0	1	0	$0 \rightarrow 1$	1	0
0	1	1	$0 \rightarrow 1$	1	0
1	0	0	$1 \rightarrow 0$	X	0
1	0	1	$1 \rightarrow 0$	X	0
1	1	0	$1 \rightarrow 1$	0	1
1	1	1	$1 \rightarrow 1$	0	1

S	$\bar{J}\bar{K}$	$\bar{J}K$	$J\bar{K}$	JK
\bar{Q}	0	0	1	0
Q	X	0	0	X

$$S = \bar{Q}J$$

R	$\bar{J}\bar{K}$	$\bar{J}K$	$J\bar{K}$	JK
\bar{Q}	X	X	0	0
Q	0	1	1	0

$$R = QK$$



⇒ JK to T

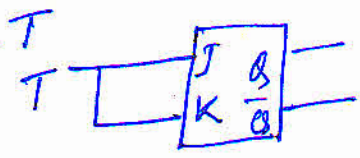
Q	T	$Q_n \rightarrow Q_{n+1}$	J	K
0	0	$0 \rightarrow 0$	0	X
0	1	$0 \rightarrow 1$	1	X
1	0	$1 \rightarrow 0$	X	0
1	1	$1 \rightarrow 1$	X	1

T	\bar{J}	J
\bar{Q}	0	1
Q	X	X

$$J = T$$

K	\bar{J}	J
\bar{Q}	X	X
Q	0	1

$$K = T$$



⇒ in XY F/F truth table is given below is to implement using JK F/F from J S R respectively

X	Y	Q_{n+1}
0	0	1
0	1	Q_n
1	0	$\overline{Q_n}$
1	1	0

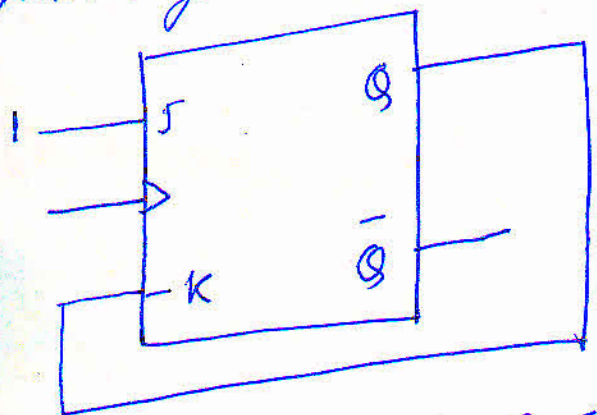
$\overline{Q_n}$	Q_n	$\overline{X}\overline{Y}$	$\overline{X}Y$	$X\overline{Y}$	XY
1	0	0	1	0	0
0	1	0	0	1	0
1	1	0	0	0	1

JK to XY F/F.

Q_n	X	Y	$Q_n \rightarrow Q_{n+1}$	J	K
0	0	0	0 → 1	1	X
0	0	1	0 → 0	0	X
0	1	0	0 → 1	1	X
0	1	1	0 → 0	0	X
1	0	0	1 → 1	X	0
1	0	1	1 → 0	X	1
1	1	0	1 → 0	X	1
1	1	1	1 → 1	X	0

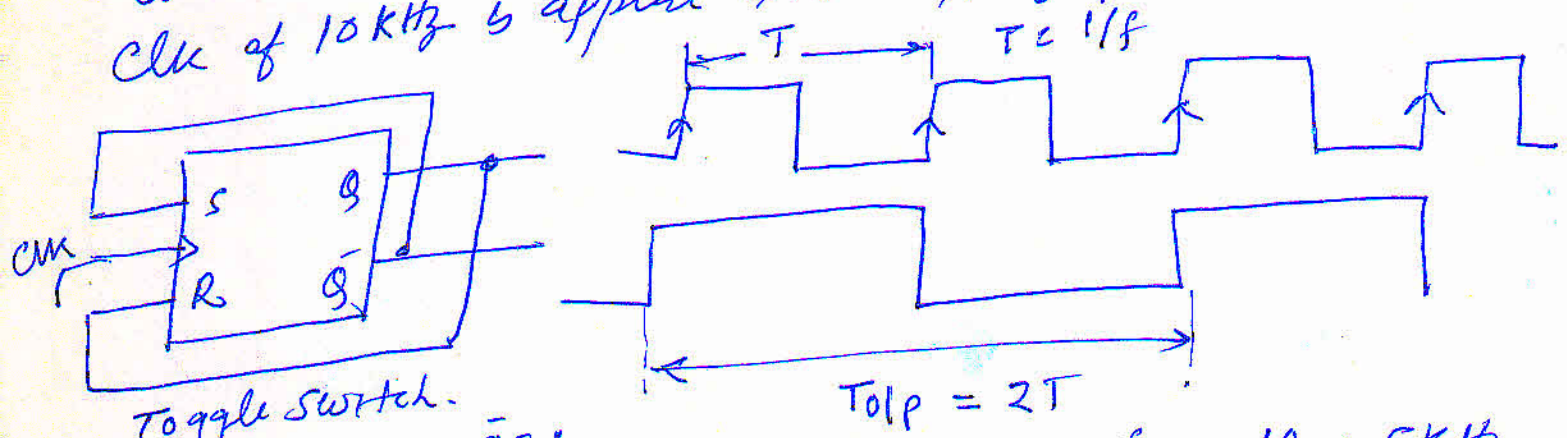
$J = \overline{Y}$ Similarly $K = X$

⇒ If initially Q o/p is zero after 6 clk pulses the o/p Q is



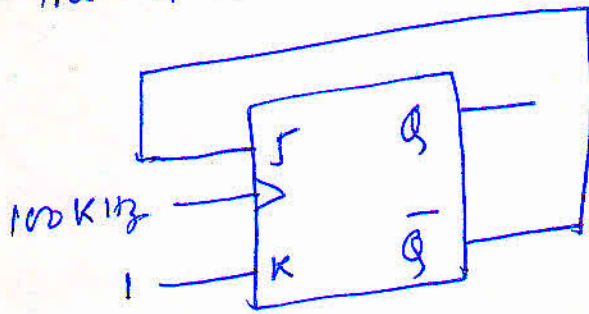
Q	J	K	Q_{n+1}
0	1	0	1
0	1	1	0
1	0	1	0
1	1	0	1
1	1	1	0

⇒ In +ve edge triggered SR F/F if \overline{Q} connected to S I/P, Q is connected to R and clk of 10kHz is applied then o/p freq Q is



Toggle Switch.
 If Initial $Q = 0, \overline{Q} = 1$
 $R = 0, S = 1$ F/F Set
 If $Q = 1, \overline{Q} = 0 \rightarrow R = 1, S = 0$ Reset
 $f = \frac{1}{T_{o/p}} = \frac{1}{2T} = \frac{f}{2} = \frac{10}{2} = 5\text{kHz}$

⇒ The o/p freq. is



If initially

$$Q = 0, \bar{Q} = 1$$

$J = 1, K = 1$ Toggle $Q \rightarrow 1$

$$\bar{Q} = 0$$

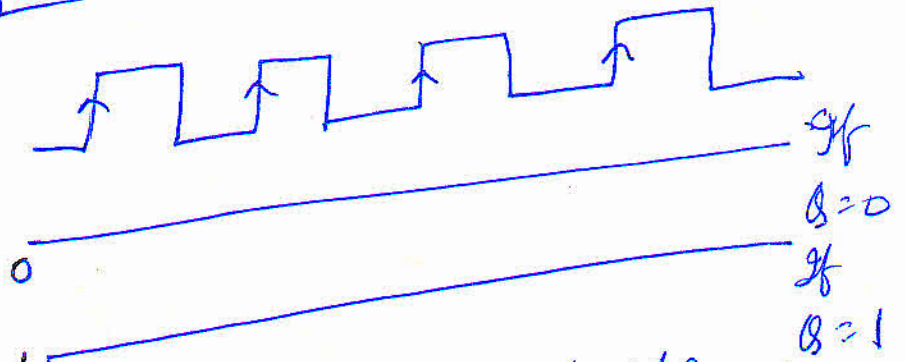
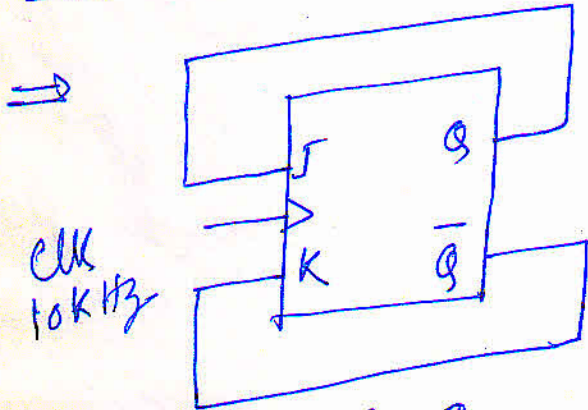
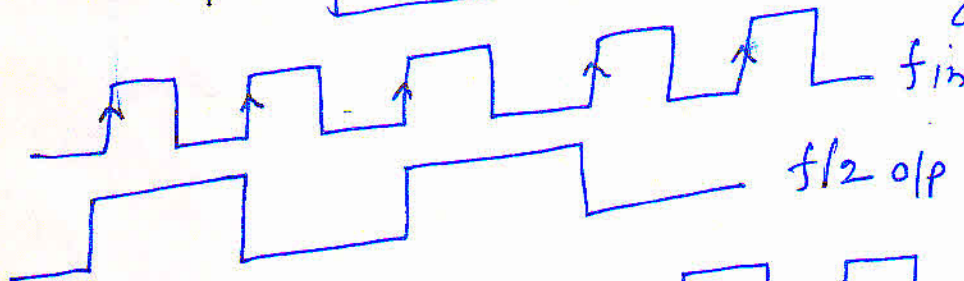
If $Q = 1$

$J = 0, K = 1$ Reset $Q = 0$

$$\bar{Q} = 1$$

and so on.

(Toggle switch).



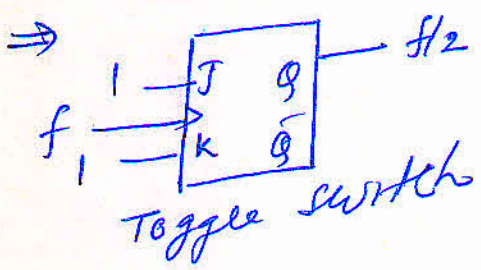
Hence Time period of o/p = ∞
 freq. = $\frac{1}{\infty} = \underline{\underline{0 \text{ Hz}}}$.

If initially $Q = 0$
 $\bar{Q} = 1$

$J = 0$ F/F is Rest $Q = 0$ always
 $K = 1$ $Q = 1$

If initially $Q = 1$
 $\bar{Q} = 0$

$J = 1$ F/F is Set $Q = 1$ always
 $K = 0$ $Q = 0$



Counters :- counter can be used as an instrument for measuring time and therefore period or freq. There are two types of counters.

- 1) Synchronous
- 2) Asynchronous.

⇒ Asynchronous / Ripple / Serial counter :- Simple and straight forward in operation

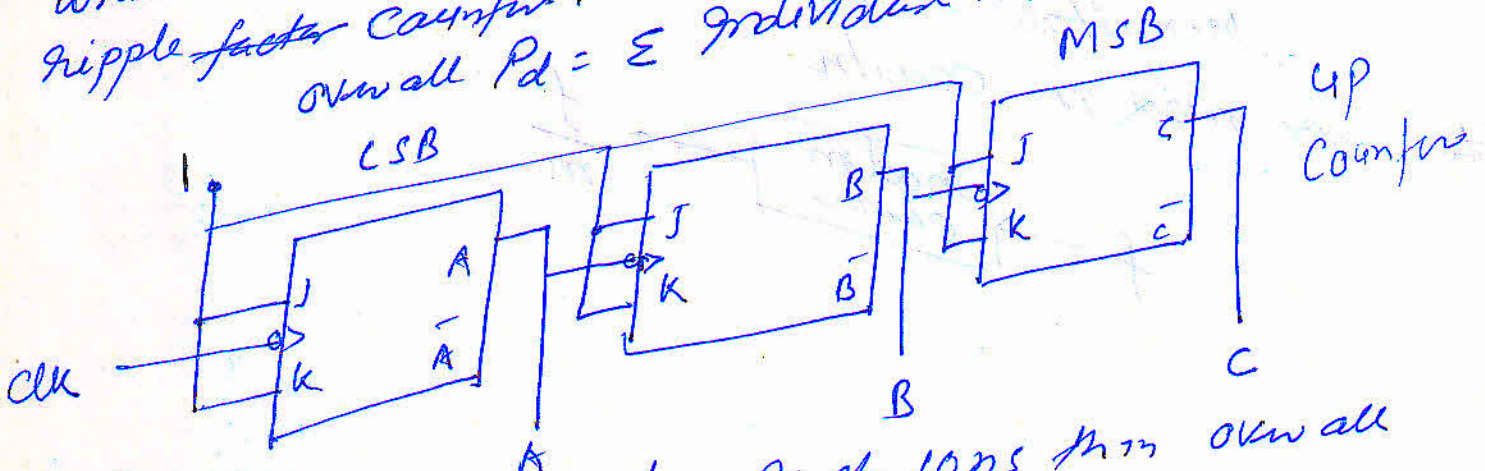
- * Require minimum hardware
- * Has speed limitation
- * Each F/F is triggered by the previous F/F and so the counter have cumulative settling time
- * Suffer from glitch

⇒ parallel / Synchronous counter :-

- * Inherent speed of operation
- * Since F/F is triggered by the clk in synchronous and so the settling time equals delay of the single F/F
- * Inherent hardware
- * Glitch do not occur.

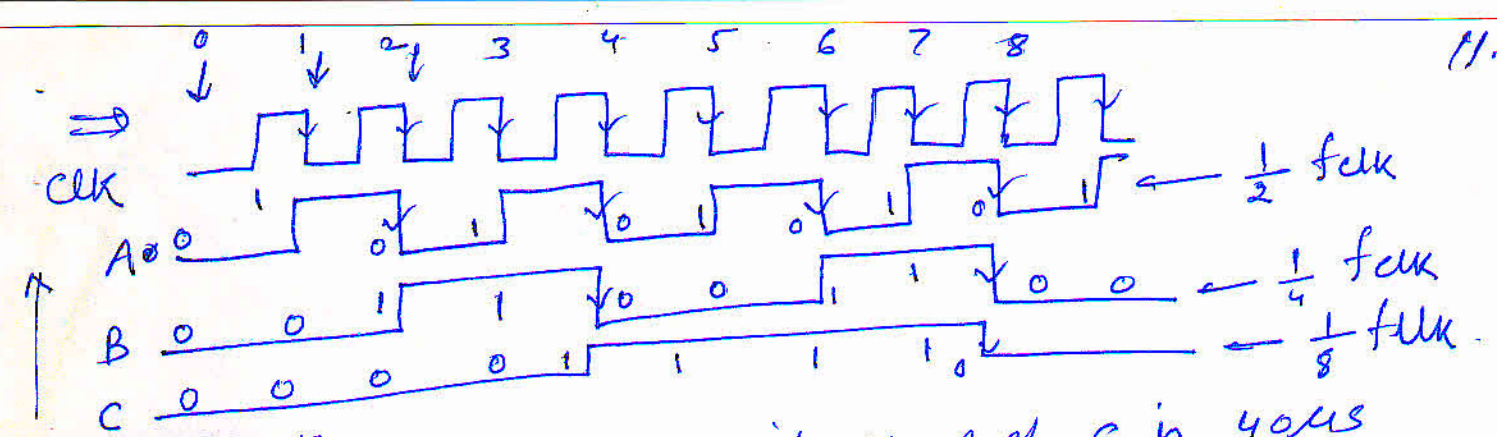
Ripple / Asynchronous counter :- When o/p of a F/F is used as a clock for next stage F/F, then it is called ripple factor counter.

Overall Pd = Σ individual Pd of F/Fs.



CBA = 000 initially

If each F/F has Pd of 10ns then overall Pd is 30ns. (LSB where external clk is applied).



Counting sequence

CLK transition

CLK transition	C	B	A
0	0	0	0
1	0	0	1
2	0	1	0
3	0	1	1
4	1	0	0
5	1	0	1
6	1	1	0
7	1	1	1
0	0	0	0

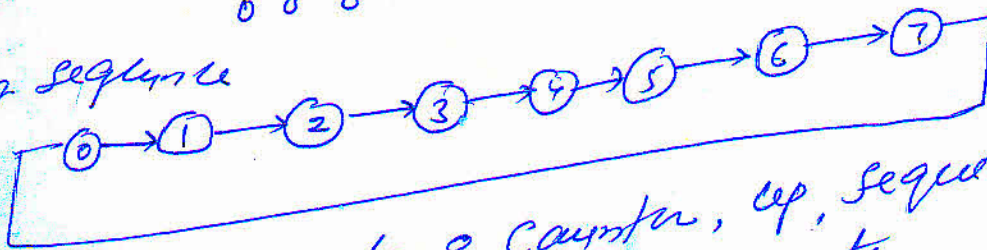
if period of C is 40 μ s $f_{clk} = ?$

$$f_c = \frac{1}{40 \times 10^{-6}} =$$

$$f_c = \frac{1}{8} f_{clk}$$

$$f_{clk} = 8 \times \frac{1}{40 \times 10^{-6}} = 200 \text{ KHz}$$

Counting sequence

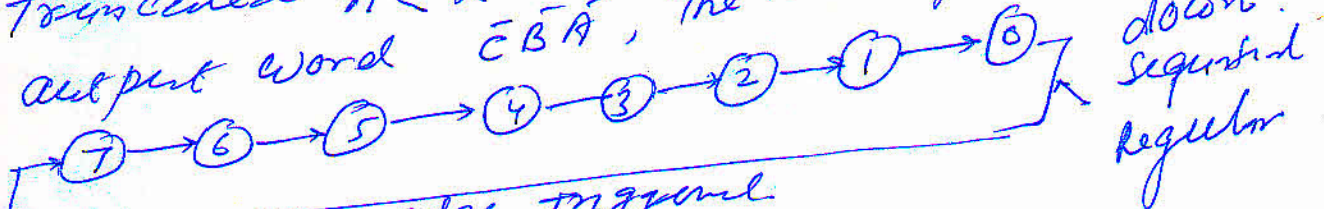


Count up mode

- * 8 states \rightarrow Mode 8 counter, up, sequential. Regular
- * Modular \rightarrow No. of distinct states
- * Sequential \rightarrow states one in sequence 0, 1, 2, 3, ...
- * Regular $\rightarrow n = 2^m$, $n = \text{no. of states} / \text{Mod of counter}$
- $m \rightarrow$ No. of F/Fs

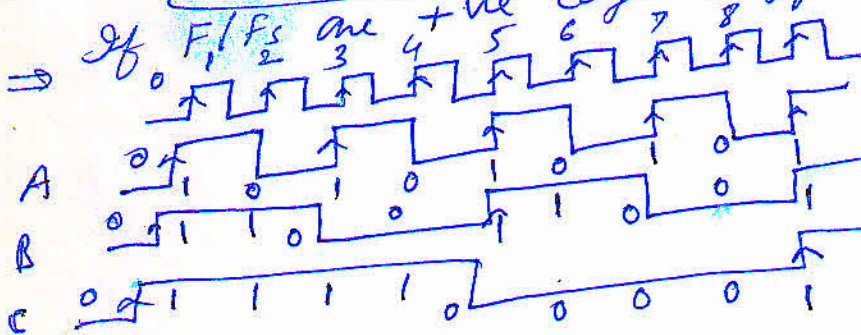
* Truncated $n < 2^m$

\Rightarrow If output word $\bar{C}\bar{B}\bar{A}$, The Resulting sequence is

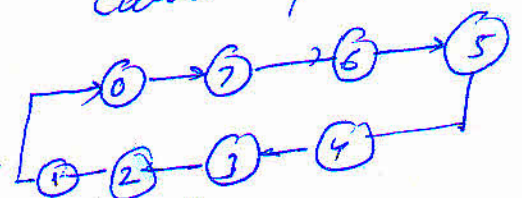


down sequential regular

\Rightarrow If F/Fs are +ve edge triggered

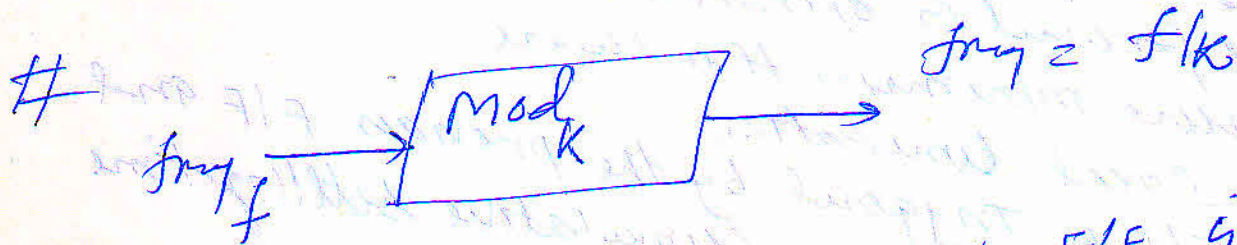
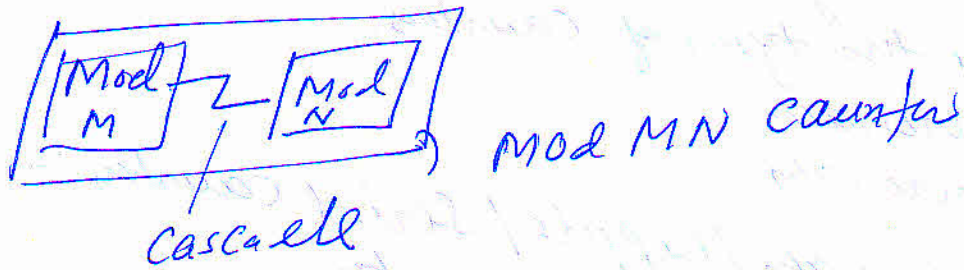


Count sequence



down sequence

in Asynchronous counter



in ripple counter P.d of each F/F is t_{pd} than time period of the clk

$$T_{clk} \geq n t_{pdFF}$$

$$f_{clk} \leq \frac{1}{n t_{pd}}$$

$$f_{max} = \frac{1}{n t_{pd}}$$



$$\text{Max state} = 2^n$$

for mod m counter

