

EXPERIMENT NUMBER-13 :-

TITLE:- Implement common Bus using MUX.

OBJECTIVE:- Implementing common Bus using MUX (4539 IC, 4:1MUX).

APPARATUS REQUIRED:-

Sr. No.	Component's Name	Specification	Qty
1	4:1MUX IC	4539 IC	2
2	Register	Using 4013 IC	16
3	Trainer Kit	—	1
4	Wires	—	1 bunch

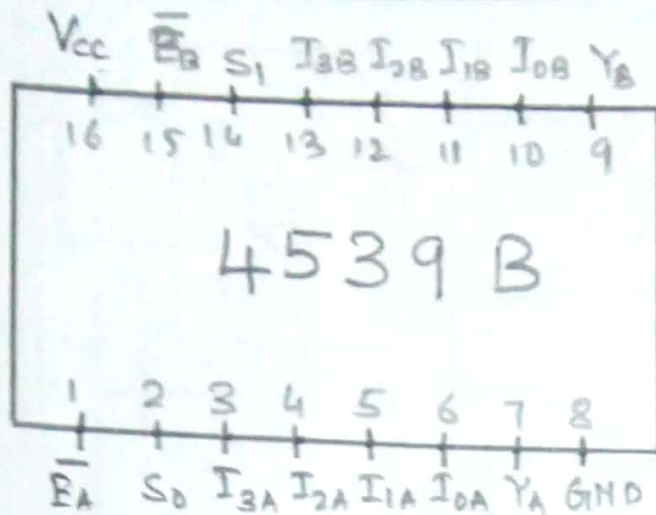
THEORY:-

A bus is a set of common wires that carries data between registers. A bus has a separate wire for every bit in the registers. There are a set of control signals which determines which register is selected by the bus at a particular time.

Name: Orunayan Bhattacharya Roll No.: 66

Section: CSE2C

Year: 2nd.

PIN CONFIGURATION:-FUNCTION TABLE for the BUS:-

S_1	S_0	Selected Register
0	0	A
0	1	B
1	0	C
1	1	D

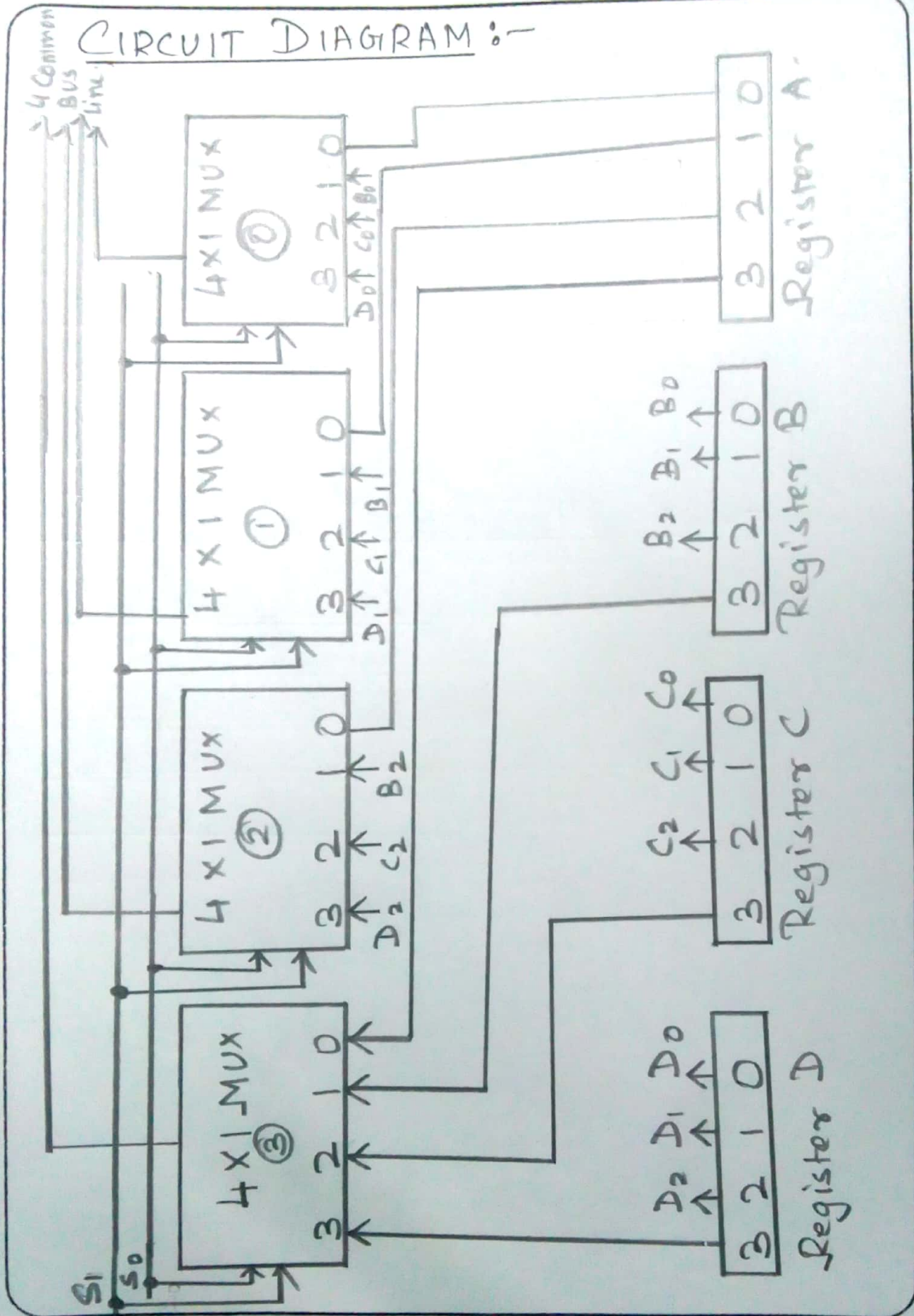
P.T.O.

Name: Orunayan

Section: CSF2C

Roll No.: 66

Year: 2nd (2017-18)

CIRCUIT DIAGRAM :-

Name : Drunayan Bhattacharya Roll No. : 66

Section : CSB2C

Year : 2nd

CONCLUSION:-

With the help of this experiment, we came to know about common bus designing by 4X1 MUX IC (4539).

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