

ABHISHEK SHARMA.

CS THIRD YEAR

SECTION : 'I'

ROLL NO. : 01.

ENROLLMENT NO. : 12019009001127

CLASS. ASSIGNMENT (2 Questions)

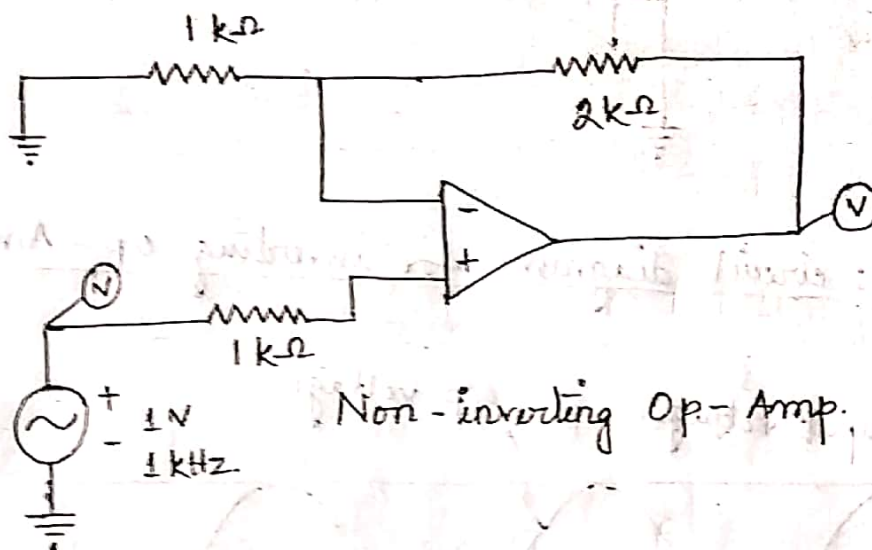
Date : 08.10.2021.

Analog Electronics Circuit Lab.

UEM, Kolkata.

R1. Study of Inverting and Non inverting amplifier using Op - Amp.

Circuit diagram :-



Graph :-

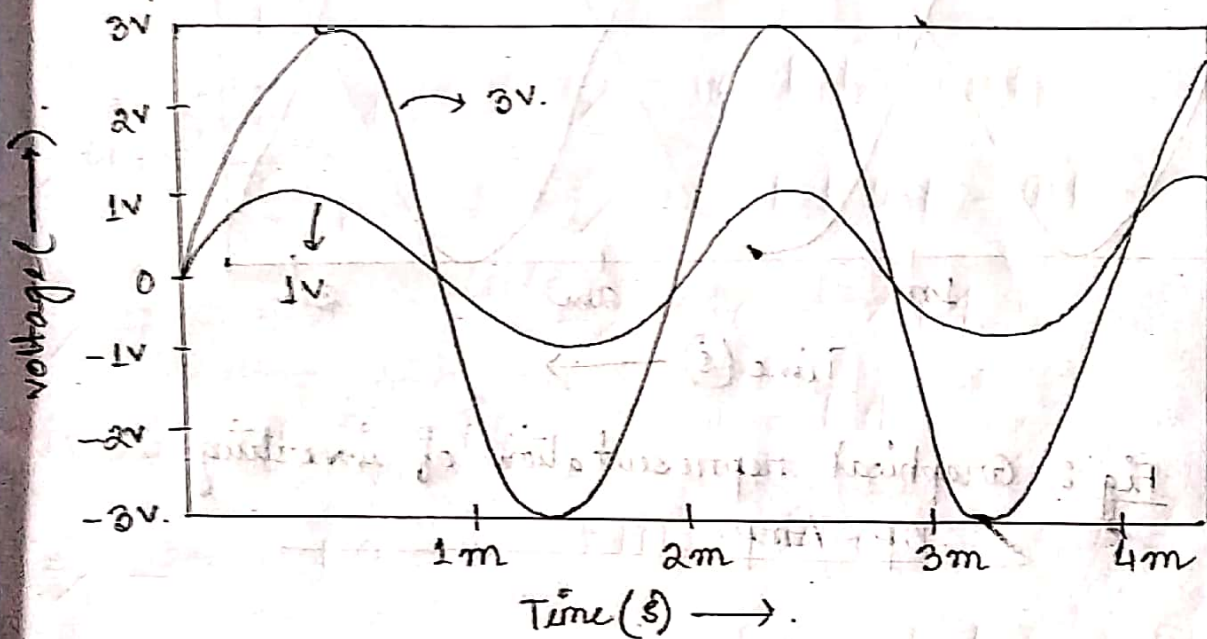


Fig : Graphical presentation of Non inverting Op - Amp.

Inverting Op - Amp circuit diagram :-

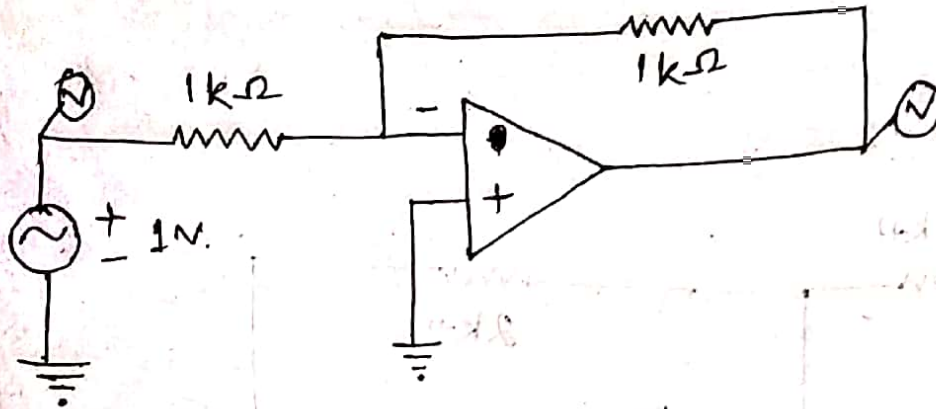


Fig: circuit diagram for inverting op - Amp.

Graph :-

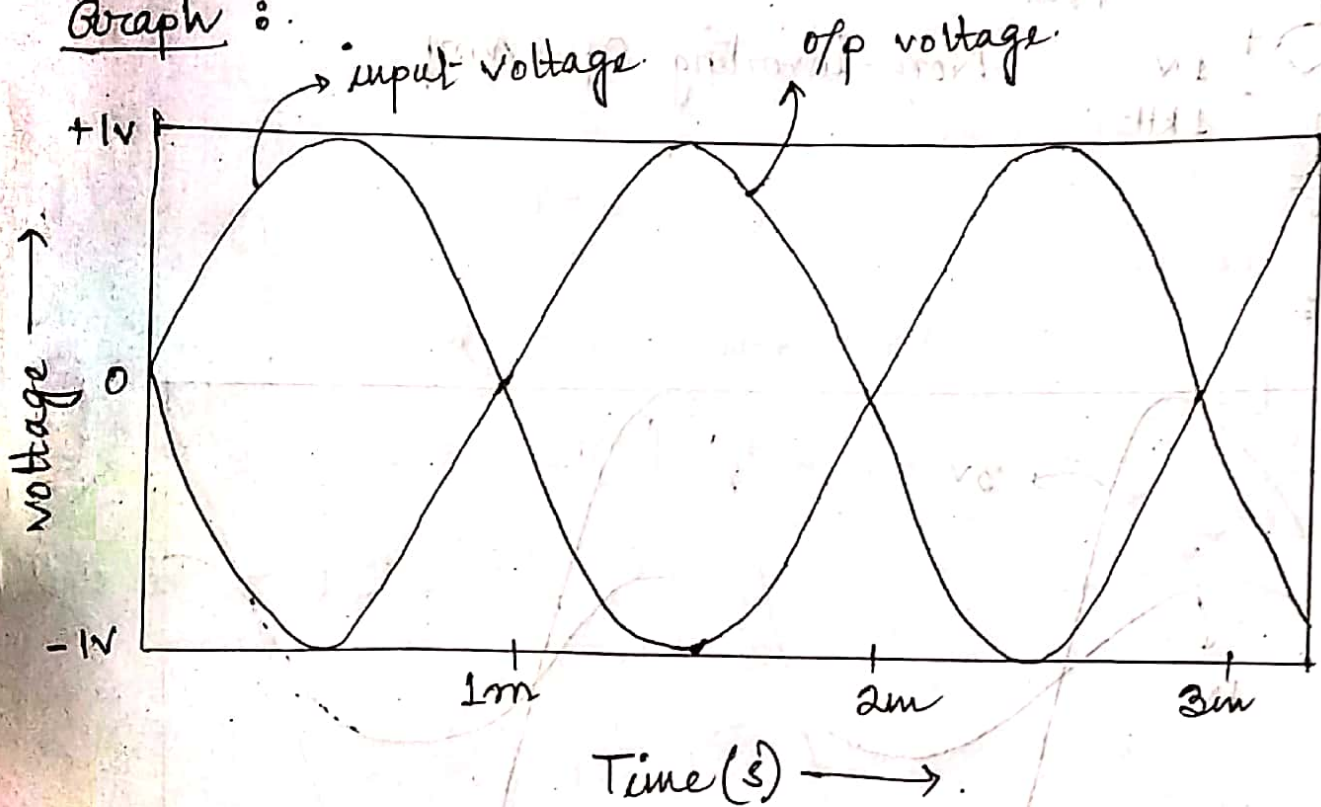


Fig: Graphical representation of inverting Op - Amp.

Q2. Voltage subtractor using Op-Amp ($V_1 - V_2$) where V_1 and V_2 both are AC voltages ranges from $3V$ to $-3V$ and $2V$ to $-2V$ respectively.

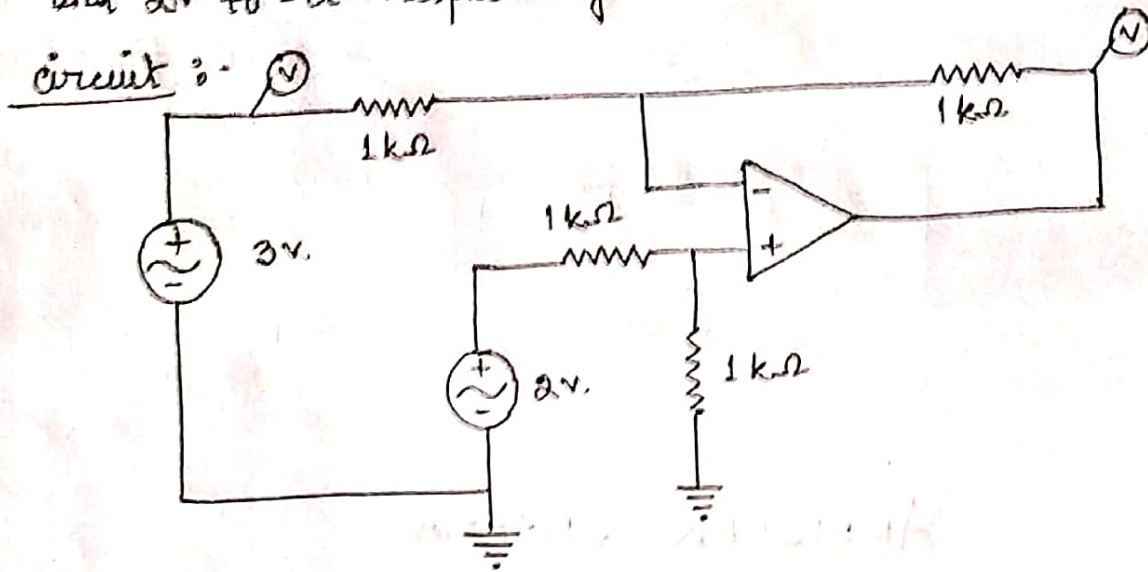


Fig : circuit diagram for voltage subtractor.

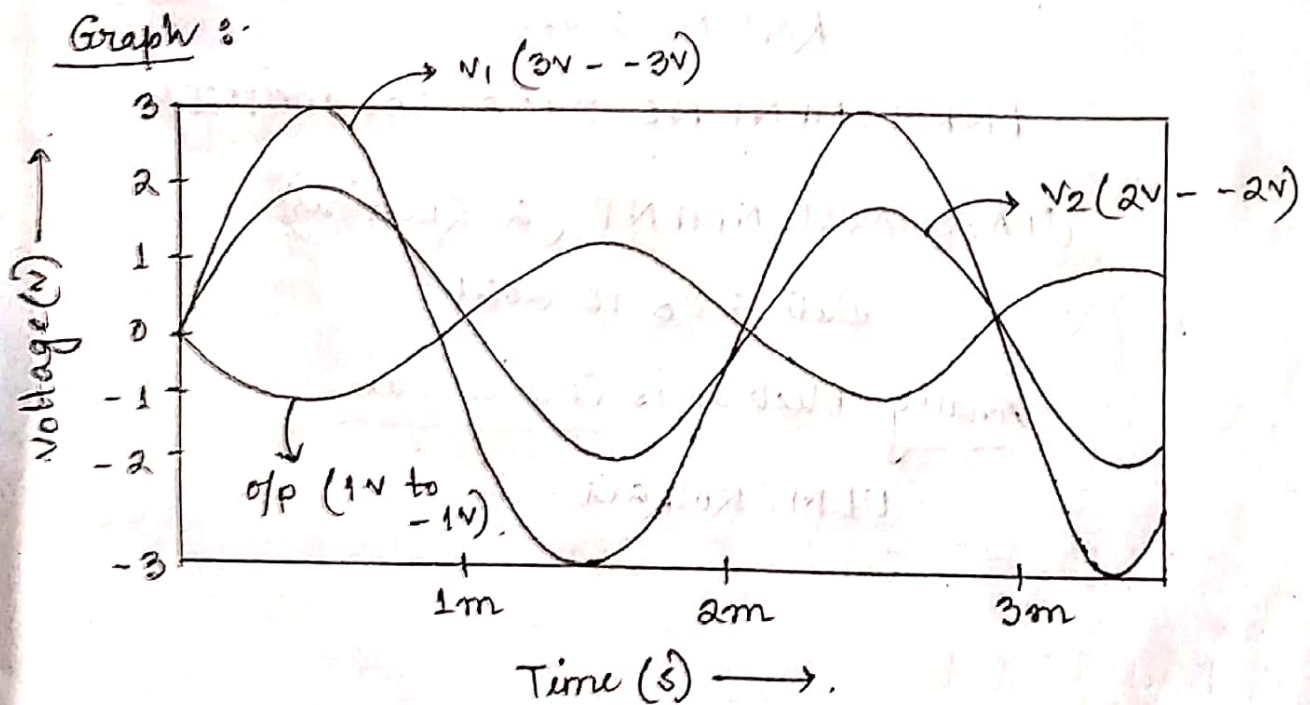


Fig : Graphical representation of Voltage Subtractor.