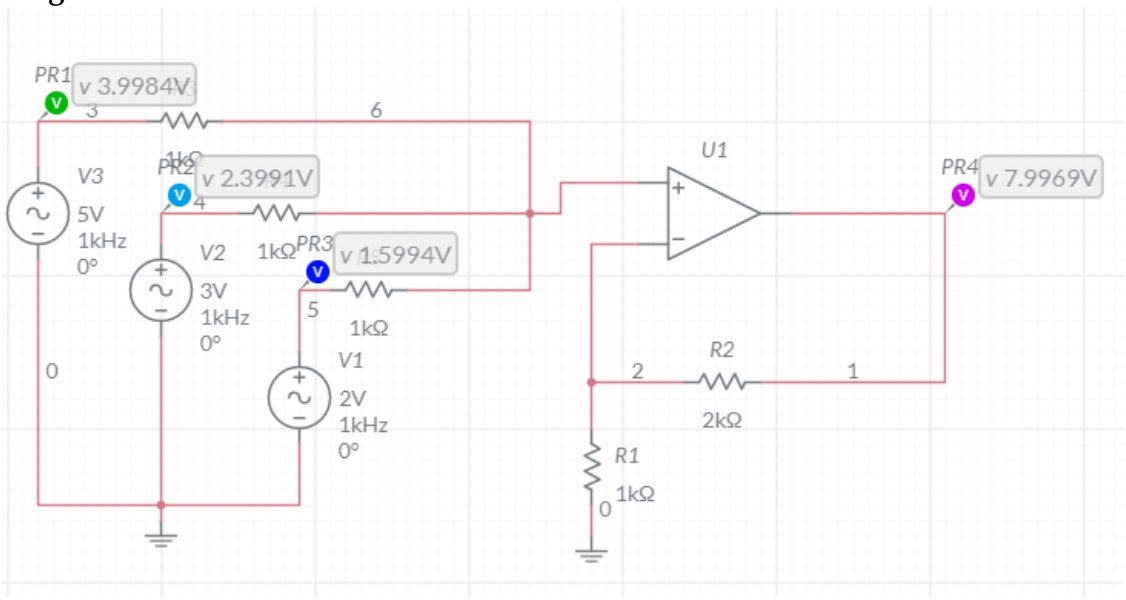
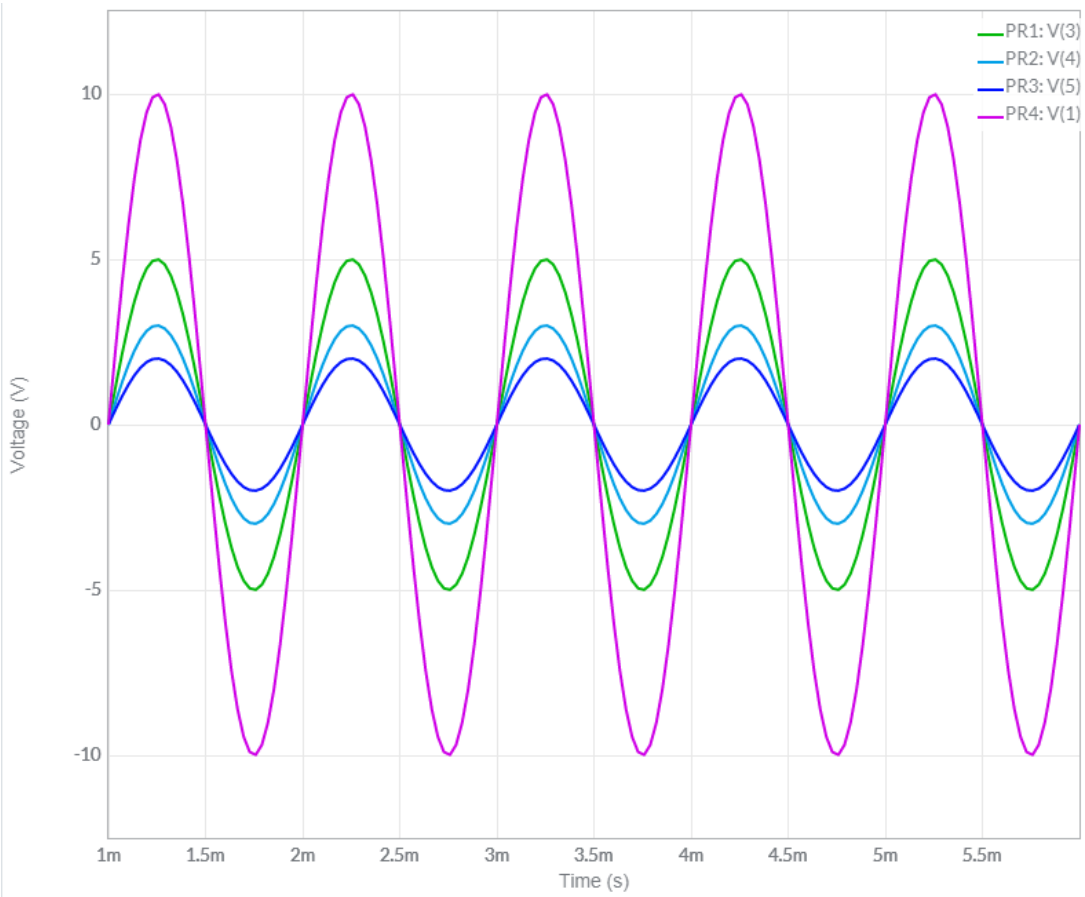
ABHISHEK SHARMA
CS THIRD YEAR, SECTION : "I", ROLL NO.: 01
ENROLLMENT NO.: 12019009001127
ANALOG ELECTRONICS LAB CLASS ASSIGNMENT [Viva Voce]
DATE : 22.10.2021
UNIVERSITY OF ENGINEERING AND MANAGEMENT, KOLKATA

Q1. Add 5V, 3V and 2V using non-inverting terminal.
Circuit Diagram :

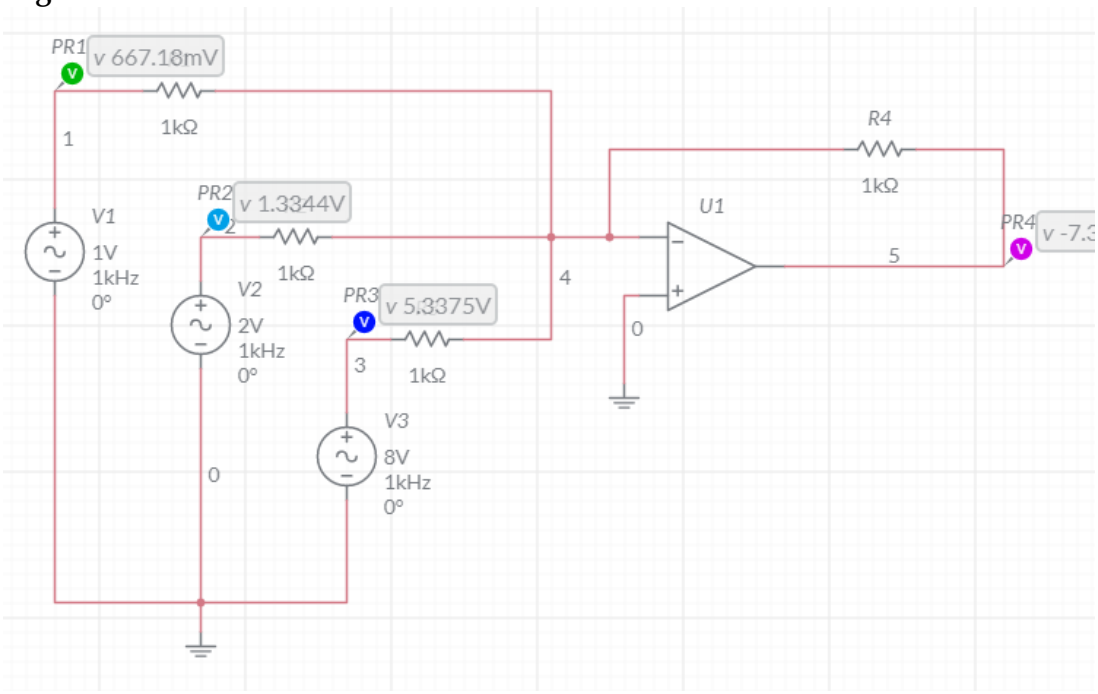


Graph :

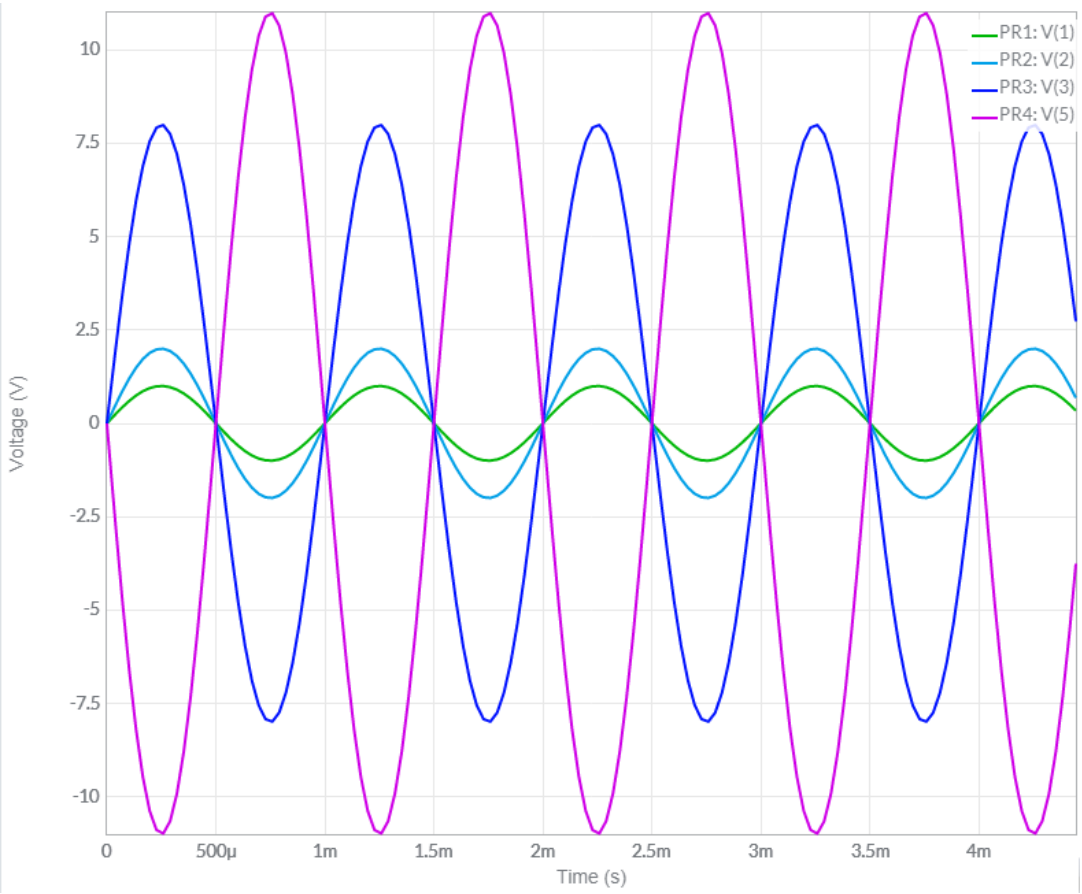


Q2. Add 1V, 2V and 8V using inverted terminal.

Circuit Diagram :

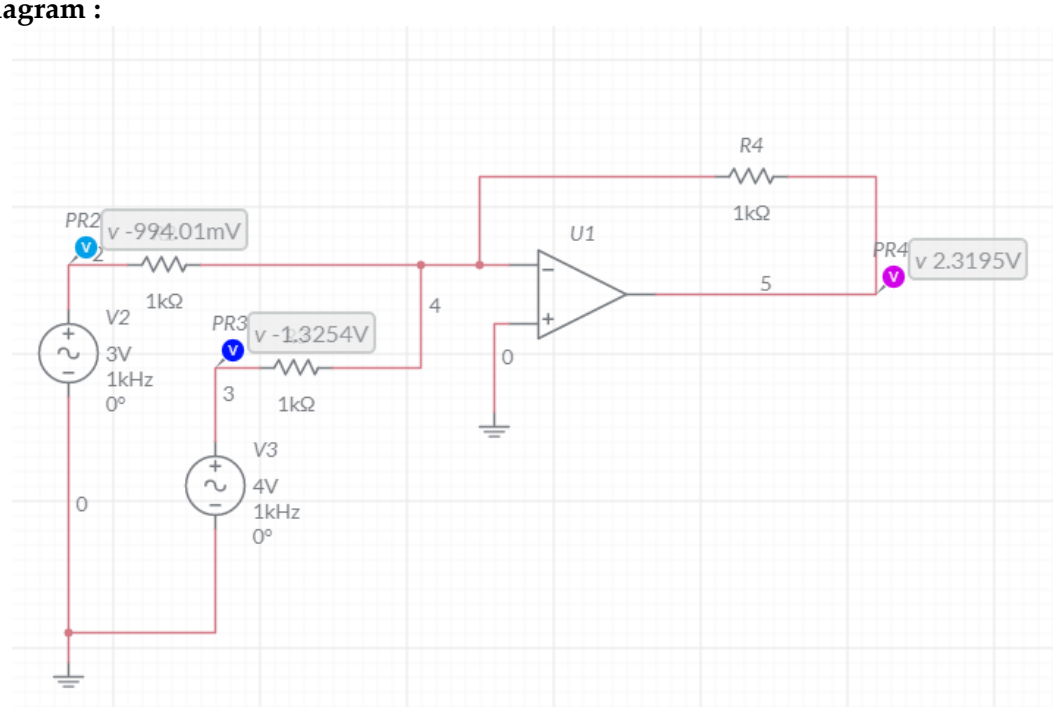


Graph :

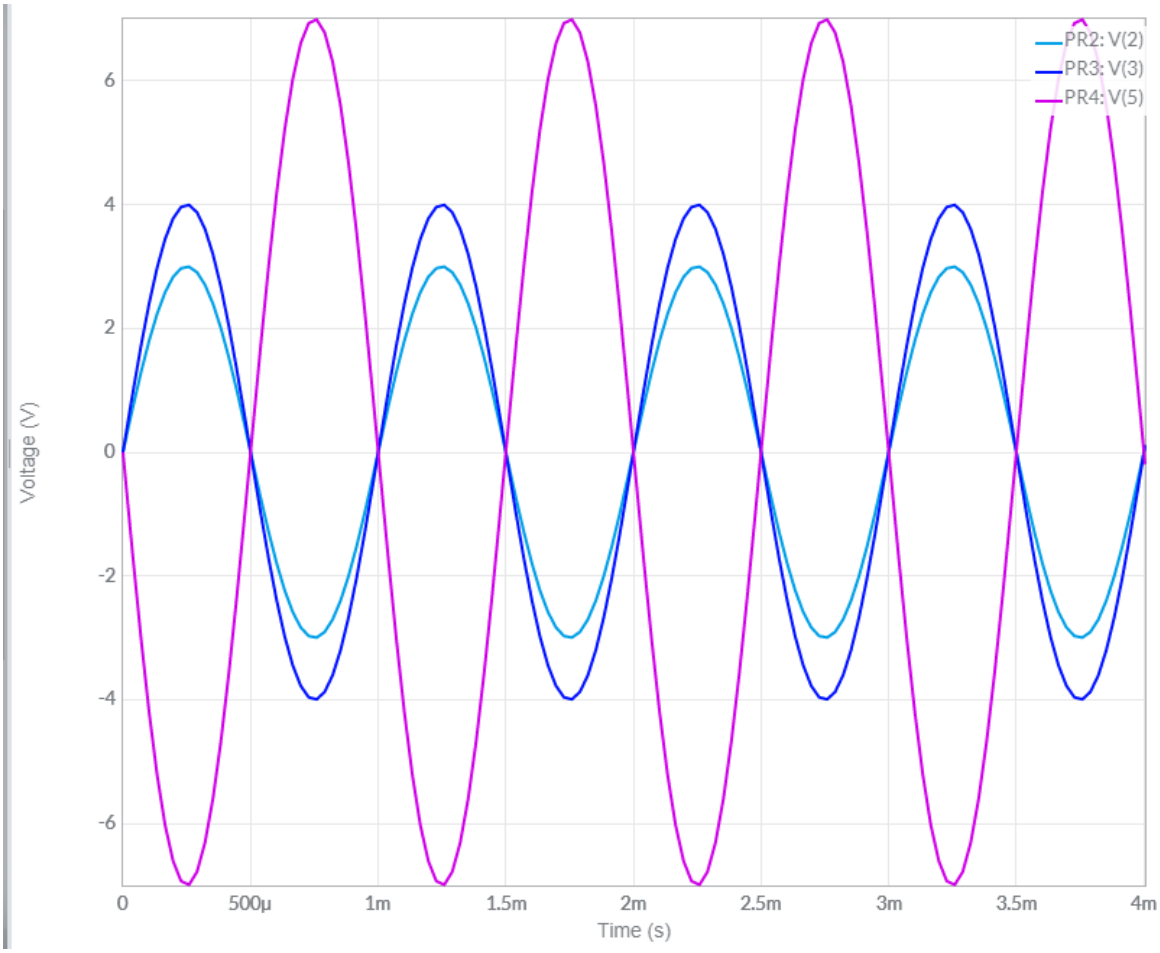


Q3. Perform the following addition: 0.5 × V1 + 2 × V2. Here V1 and V2 are AC voltages ranging from 6V to -6V and 2V to -2V respectively.

Circuit Diagram :

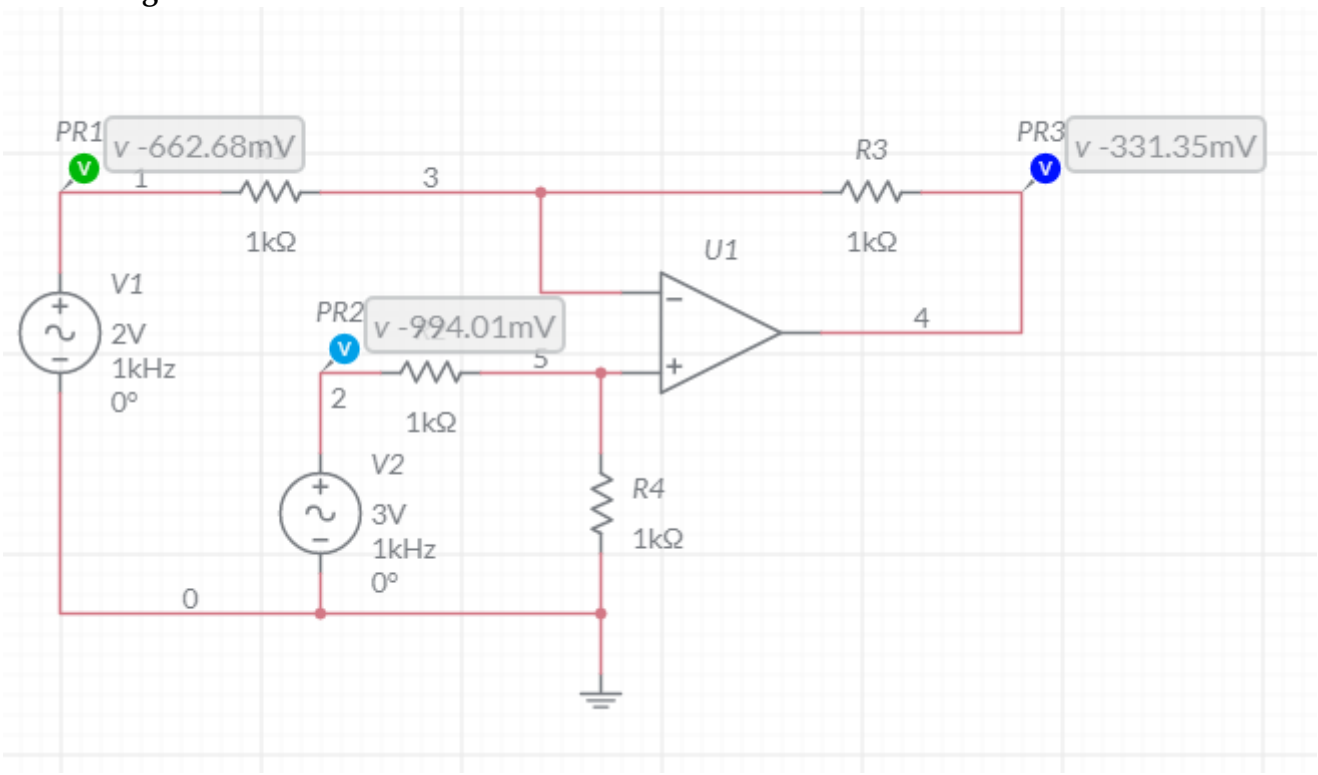


Graph :



Q4. Perform subtraction of V1 and V2 (i.e. V1 -V2). Here V1 and V2 are Av voltages ranging from 3V to -3V and 2V to -2V respectively.

Circuit diagram:



Graphical representation: