

# Assignment 1

Abhishek Thakur  
EE20RESCH11014

Download the codes from

[https://github.com/abhishekt711/FPGA\\_Lab/Assignment\\_1](https://github.com/abhishekt711/FPGA_Lab/Assignment_1)

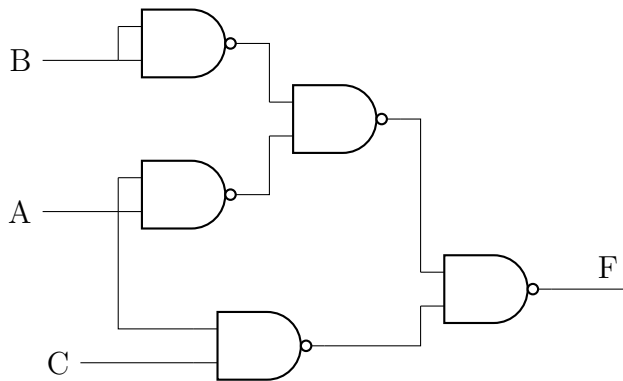
1 Question:6(a)

Draw the Logic Circuit of the following Boolean Expression:

$$\bar{A}\bar{B} + AC \quad (1)$$

2 Solution

The logic circuit for the boolean function  $\bar{A}\bar{B} + AC$  can be drawn using only NAND gate as:



A	B	C	Output
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

TABLE 0: Truth Table

$$F = \overline{\overline{\bar{A}\bar{B}AC}} \quad (2)$$

$$F = \overline{\bar{A}\bar{B}} + \overline{AC} \quad (3)$$

$$F = \bar{A}\bar{B} + AC \quad (4)$$

3 Truth Table