

# Digital Electronics

Logic gate :- A logic gate is a circuit design by using electronics components like diode, transistor, register and node. As the same employs logic gate is designed to perform logical operations in digital system like computer, communication system etc. It has more than input and output.

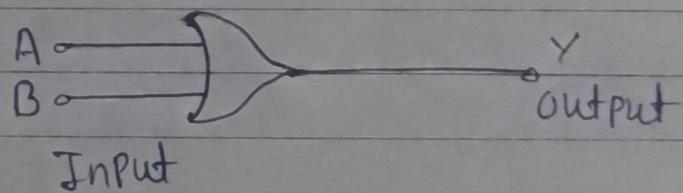
Classification into measure types of logic gates -

① Basic logic gates -

(i) OR Gate (ii) AND Gate (iii) NOT Gate

(i) OR Gate :-

Symbol



Input

formula

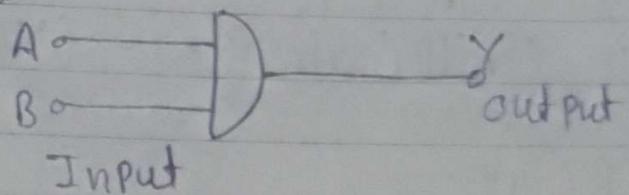
$$Y = A + B$$

Truth table

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1

(ii) AND Gate -

Symbol



formula

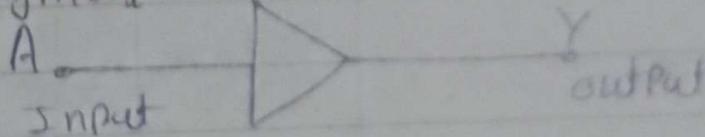
$$Y = A \cdot B \quad Y = A \cdot B$$

Truth Table -

A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1

(iii) NOT Gate -

Symbol



formula

$$Y = \bar{A} \quad (\text{Inverse or A})$$

Opposite

Truth Table

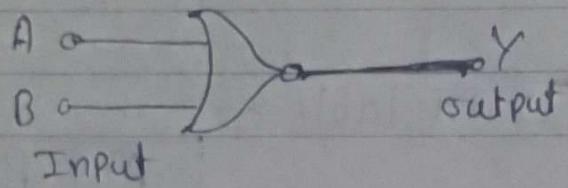
A	Y
0	1
1	0

## ② Universal Logic gates -

- (i) NOR Gate — (NOT + OR) Gate
- (ii) NAND Gate. — (NOT + AND) Gate

(i) NOR Gate -

Symbol -



formula -

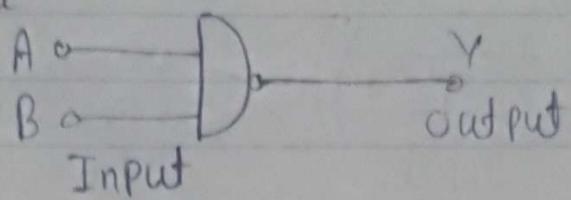
$$Y = \overline{A+B}$$

Truth Table

A	B	$A+B$	$Y = \overline{A+B}$
0	0	0	1
0	1	1	0
1	0	1	0
1	1	1	0

(ii) NAND Gate -

Symbol -



formula -

$$Y = \overline{A \cdot B}$$

Truth Table -

A	B	$A \cdot B$	$Y = \overline{A \cdot B}$
0	0	0	1
0	1	0	1
1	0	0	1
1	1	1	0

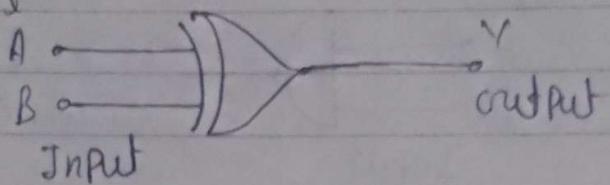
③ Derived Logic Gate

(i) X-OR Gate

(ii) X-NOR Gate

(i) X-OR Gate -

Symbol -



formula -

$$Y = A \oplus B$$

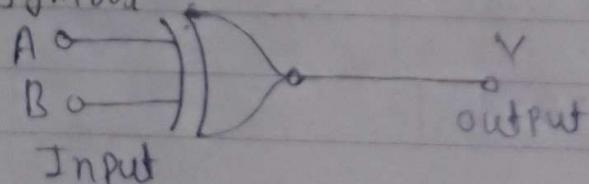
$$\text{or } Y = \bar{A} \cdot B + A \cdot \bar{B}$$

Truth Table -

A	B	$\bar{A} \cdot B$	$A \cdot \bar{B}$	$Y = \bar{A}B + A\bar{B}$
0	0	0	0	0
0	1	1	0	1
1	0	0	1	1
1	1	0	0	0

(ii) X-NOR Gate -

Symbol -



formula -

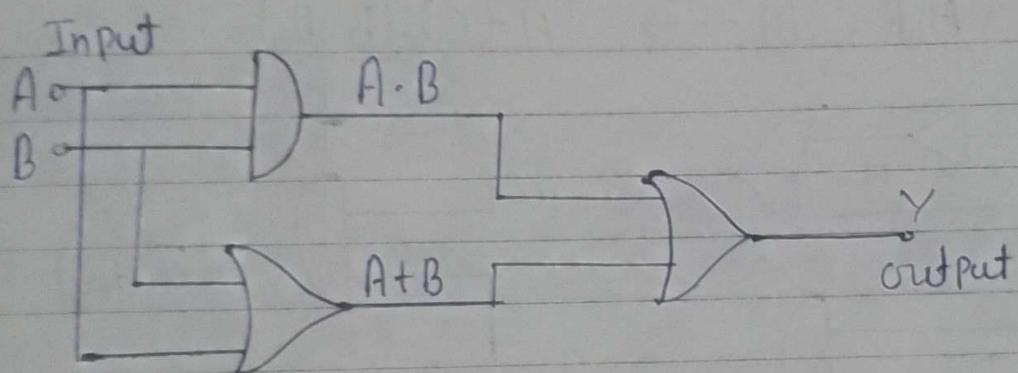
$$Y = A \odot B$$

$$\text{or } Y = \bar{A} \cdot \bar{B} + A \cdot B$$

Truth Table -

A	B	$\bar{A} \cdot \bar{B}$	$\bar{A} \cdot B$	$Y = \bar{A} \cdot \bar{B} + A \cdot B$
0	0	1	0	1
0	1	0	0	0
1	0	0	0	0
1	1	0	1	1

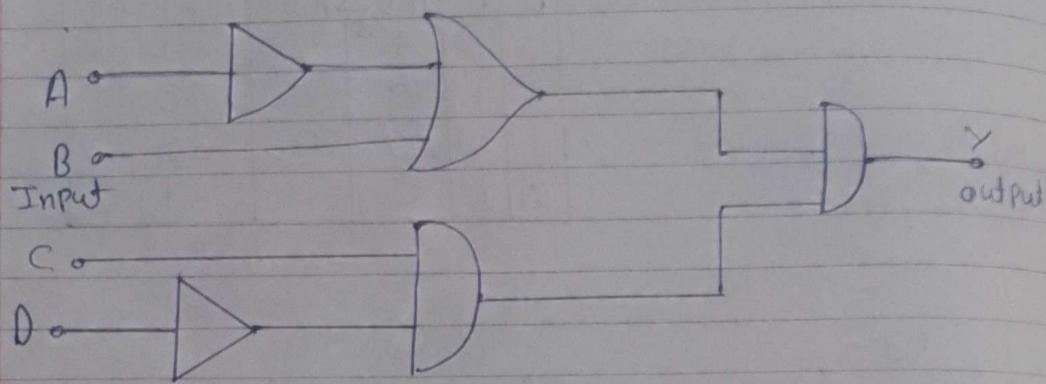
Ex-  
①  $(A, B) \longrightarrow (\bar{A} \cdot B) + (A + B)$



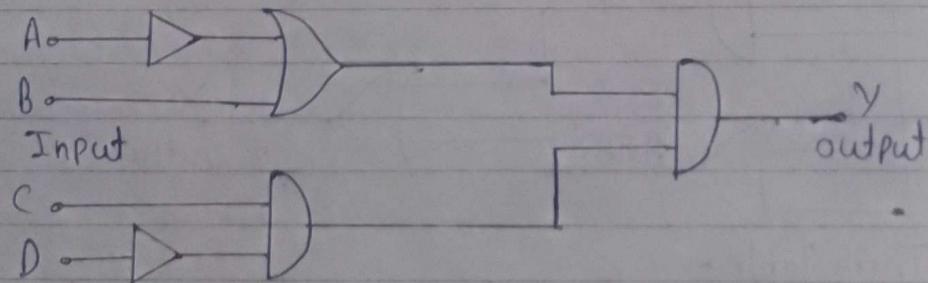
Truth Table -

A	B	$\bar{A} \cdot B$	$A + B$	$(\bar{A} \cdot B) + (A + B)$
0	0	0	0	0
0	1	0	1	1
1	0	0	1	1
1	1	1	1	1

$$(2) (A, B, C, D) \rightarrow (\bar{A} + B) \cdot (C \cdot \bar{D})$$



$$(3) (A, B, C, D) \rightarrow (\bar{A} + C) \cdot (B \cdot \bar{D})$$



Number System - Generally in any number system there is an ordered set of symbols called digits which are used to specify any number the digits are defined for performing operations such as addition, subtraction, multiplication, division etc. A collection of these digits form a number which is general has two parts namely integer and fractional.

### Types of number system

(i) Binary number system - (0,1).

Base - 2  
numbers - 0,1

(ii) Octal number System

Base - 8  
numbers - 0,1,2,3,4,5,6,7

(iii) Decimal number System

Base - 10  
numbers - 0,1,2,3,4,5,6,7,8,9

(iv) Hexa Decimal number System

Base - 16  
numbers - 0 to 15  
(where, 10 = A, 11 = B, 12 = C, 13 = D, 14 = E and 15 = F)

$$\text{Q:- } (48 \cdot 62)_{10} = (?)_2$$

Solve

$$\begin{array}{r} 78 \cdot 62 \\ \times 1 \cdot 1 \\ \hline 48 \quad 0 \cdot 62 \end{array}$$

$$\begin{array}{r}
 2 | 48 \\
 2 | 24 \quad 0 \\
 2 | 12 \quad 0 \\
 2 | 6 \quad 0 \\
 2 | 3 \quad 10 \\
 2 | 1 \quad 1
 \end{array}
 \quad
 \begin{array}{l}
 \uparrow \\
 48 \cdot 2 \quad 11000
 \end{array}$$

$$0 \cdot 62 \times 2$$

$$1 \cdot 24 = 1$$

$$0 \cdot 24 \times 2 = 0$$

$$0 \cdot 48 \times 2 = 0$$

$$0 \cdot 96 \times 2 = 1$$

$$0 \cdot 62 = 1001$$

$$(48 \cdot 62)_{10} = (11000 \cdot 1001)_2$$

$$\textcircled{0} - (1111.1011)_2$$

$$(1111.1011)_2 =$$

$$= 1 \times 2^3 + 1 \times 2^2 + 1 \times 2^1 + 1 \times 2^0 + 1 \times 2^{-1} + 0 \times 2^{-2} + 1 \times 2^{-3} + 1 \times 2^{-4}$$

$$= 8 + 4 + 2 + 1 + \frac{1}{2} + 0 + \frac{1}{8} + \frac{1}{16}$$

$$= 15 - 0.5 + 0.125 + 0.0625$$

$$(1111.1011)_2 = (15.6875)_{10}$$

De Morgan's theorem -

(1) First theorem -

Complement of sum is equal to the product of complements

$$\overline{A+B} = \overline{A} \cdot \overline{B}$$

$$\text{OR } (A+B)' = A' \cdot B'$$

(2) Second theorem

Complement of product is equal to the sum of complement.

$$\overline{A \cdot B} = \overline{A} + \overline{B}$$

$$\text{OR } (A \cdot B)' = A' + B'$$

Proof  $\Rightarrow$

Inputs		Outputs			
A	B	$(A+B)'$	$(A \cdot B)''$	$A' + B'$	$A' \cdot B'$
0	0	1	1	1	1
0	1	0	1	1	0
1	0	0	1	1	0
1	1	0	0	0	0

## Introduction to Semi-conductors →

Semi-conductors are materials with electrical conductivity ~~mid~~ between that of conductors like metals & insulators (like silicon & glass). They are crucial in electronics, enabling the creation of devices like transistors & of semi-conductors and integrated circuit. The conductivity of semi-conductors can be controlled making them versatile for a wide range of applications.

- key characteristic of it -

1 Intrinsic conductivity - Semi-conductor have a resistivity or inverse conductivity between that of conductors and insulators typically in the range of ten to the power of four ohm-m per cm

Energy band gap - They have a forbidden energy band gap between balanced band (where electrons are ~~are~~ bound) and conduction band (where electrons are free to move)

Temperature dependence - The conductivity of semi-conductors is temperature dependent increasing as temperature rises

charge circuits - Unlike conductors, semi-conductors don't have a constant abundance of charge carriers ( $e^-$ ) at all times. They rely on external factors like temperature or light to generate or modify these carriers.

## Types of Semi-conductors

- (i) Intrinsic semi-conductors
- (ii) extrinsic semi-conductors

## Application -

- (i) Transistor, solar cells, LED, ICs etc.