03.608 SYSTEM SOFTWARE LAB 0 - 0 - 4

The exercises may include the following:

1. Design of a single pass assembler for a hypothetical Machine
2. Design of a 2 – pass assembler for a hypothetical machine
3. Design of assembler which generates code with relocation option
4. Design of absolute loader
5. Design of relocating loader
6. Design of macro processor
7. Lexical analysis
8. Operator precedence relations
9. Recursive descent parser
10. First and follow
11. Intermediate code generation
12. Code generation

Internal Continuous Assessment *(Maximum Marks-50)*

20 Marks - Tests (minimum 1)
20 Marks - Up-to-date lab work, circuit design capability, keeping track of rough record and fair record, term projects, software exercises, etc.
10 Marks - Regularity in the class

University Examination Pattern *(Maximum marks – 100)*

Marks should be awarded as follows:

20 Marks - Algorithm/Design.
25 Marks - Viva voce.
30 marks - Implementing the work/Conducting the experiment.
25 Marks - Output/Results and inference.

General instructions:

- Evaluation is a serious process that is to be conducted under the equal responsibility of both the internal and external examiners.
- The number of candidates evaluated per day should not exceed 20