
ABISHEK GODA

Singapore Singapore • goda.abishek@gmail.com

🌐 rohabini.com/abishek

Work History

May 2013 - Present

Senior Engineer

Lantiq Asia Pacific Pte Ltd

FPGA Prototyping and Emulation using Palladium for complex SoC designs, edit and maintain C based test cases for post-silicon validation and bring up. I script my work when possible using python.

Education

Jan 2007 - Apr 2010

MS by Research, EE - VLSI Stream

Indian Institute of Technology, Madras

I researched the application of commodity off the shelf GPUs that run CUDA to implement acceleration of gate level simulation.

My Projects

Arduino Adventures: <https://github.com/abishek/arduino-adventures>