# Internet of Things lab 1 Fall 2023

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# Task 1:

# 1. Requirements:

- 1. SparkFun SAMD21 Pro RF
- 2. Laptop with Arduino 2.2.1 installed
- 3. Atmel-42181-SAM-D21\_Datasheet

# 2. Development Plan:

#### a. Procedure: -

- 1. Set the serial Output.
- 2. Set the LED pins high.
- 3. Set the time of the clock to 32 KHz.
- 4. Initialize the clock to 2048 Hz.
- 5. Disable the Watchdog Timer.
- 6. Wait for synchronization.
- 7. Initialize the clock watchdog timer.
- 8. Set the timer to be 2 second by keeping Register value to 0x09.
- 9. Enable the timer.

# b. Configuration:-

- 1. Setting the BUILDIN\_PIN high.
- 2. Clock set to 32 KHz
- 3. Watchdog timer first disabled,
- 4. Timer window set to 2 seconds.
- 5. Enable the Watchdog timer.

# 3. Test Plan:

- 1. First set the LED high, and print something in the void loop.
- 2. Set the clock frequency to 2048 Hz.
- 3. Disable the Watchdog.
- 4. Configure Watchdog and Enable it.
- 5. Test with differernt register setting from 0x07 to 0x0A to find the optimal setting to get 2 seconds of wait time.

## 4. Results:

# 1. Set up a WDT that reset without clearing it.

```
void setup() {
// Initialize the blue LED pin as an output (assuming it's connected to pin 7)
SerialUSB.begin(9600); // Set the baud rate to 9600
pinMode(LED_BUILTIN, OUTPUT);
digitalWrite(LED_BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
// Disable the watchdog
WDT->CTRL.reg = 0;
// Initialize Clock Generator 2 for 2048 Hz
GCLK->GENCTRL.reg = GCLK_GENCTRL_ID(2)
                                        GCLK GENCTRL GENEN
                                        GCLK GENCTRL SRC OSCULP32K
                                        GCLK GENCTRL DIVSEL;
while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
GCLK->CLKCTRL.reg = GCLK CLKCTRL ID WDT
                                        GCLK CLKCTRL CLKEN
                                        GCLK_CLKCTRL_GEN_GCLK2;
while (WDT->STATUS.bit.SYNCBUSY); // Wait for synchronizatioN
// set the timer to 2 sec
WDT->CONFIG.bit.PER = 0x09; // Set the timeout period to 2 seconds
// Enable the WDT
WDT->CTRL.bit.ENABLE = 1; // ENABLE WDT
while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
}
void loop() {
// Do nothing
```

# 2. Set the Blue LED at the beginning of the program.

```
SerialUSB.begin(9600); // Set the baud rate to 9600
pinMode(LED_BUILTIN, OUTPUT);
digitalWrite(LED_BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
```

# 3. Clock (clock generator 2) frequency: 2048 Hz

```
GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
```

# 4. Set the WDT period to 2 seconds.

```
// set the timer to 2 sec
WDT->CONFIG.bit.PER = 0x09; // Set the timeout period to 2 seconds
```

#### 5. Observe the behavior of the blue LED.

The LED blinked after every 2 seconds interval. As can be seen in Fig. 2.

# **Output:**

```
Output
 Sketch uses 10176 bytes (3%) of program storage space. Maximum is 262144 bytes.
 Atmel SMART device 0x10010005 found
           : ATSAMD21G18A
 Device
 Chip ID
           : 10010005
 Version
           : v2.0 [Arduino:XYZ] Sep 24 2018 14:26:24
 Address
           : 8192
           : 3968
 Pages
 Page Size : 64 bytes
 Total Size : 248KB
 Planes
           : 1
 Lock Regions: 16
 Locked
          : none
 Security
           : false
 Boot Flash : true
 BOD
           : true
 BOR
           : true
           : FAST_CHIP_ERASE
 Arduino
 Arduino
           : FAST_MULTI_PAGE_WRITE
 Arduino
            : CAN_CHECKSUM_MEMORY_BUFFER
 Erase flash
 done in 0.843 seconds
 Write 10432 bytes to flash (163 pages)
 [=======
                             ] 39% (64/163 pages)
 ] 78% (128/163 pages)
 [======] 100% (163/163 pages)
 done in 0.086 seconds
 Verify 10432 bytes of flash with checksum.
 Verify successful
 done in 0.008 seconds
 CPU reset.
```

Fig.1 - Console Output.

# **Screenshot**

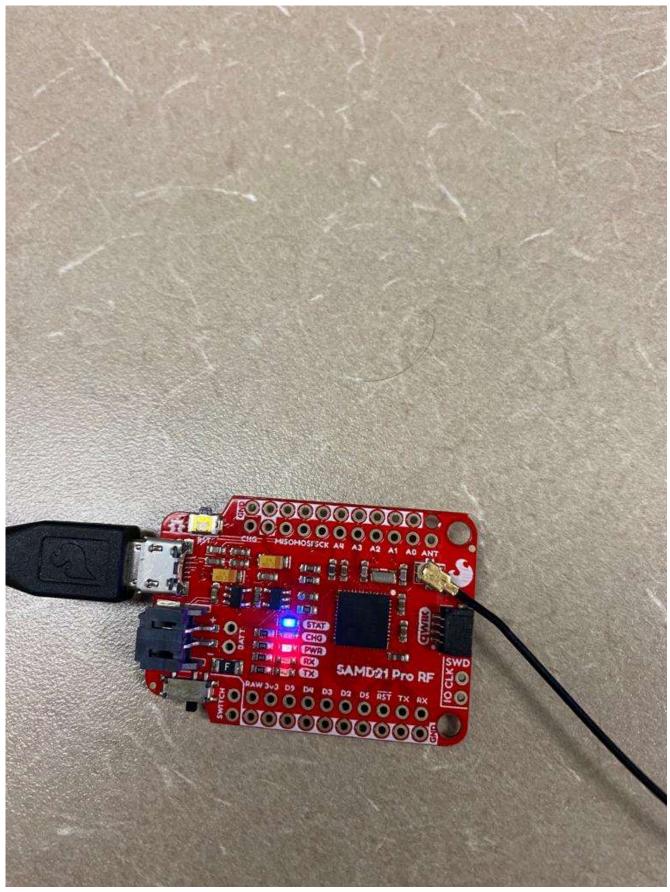


Fig.2 - Working of LED through with blinking for 2 sec.

#### Video Link

See working video - Link to Video.

# Task 2:

#### Case 1-

# 1. Requirements:

- 1. SparkFun SAMD21 Pro RF
- 2. Laptop with Arduino 2.2.1 installed
- 3. Atmel-42181-SAM-D21\_Datasheet

# 2. Development Plan:

#### a. Procedure: -

- 1. Set the serial Output.
- 2. Set the LED pins high.
- 3. Set the time of the clock to 32 KHz.
- 4. Initialize the clock to 2048 Hz.
- 5. Disable the Watchdog Timer.
- 6. Wait for synchronization.
- 7. Initialize the clock watchdog timer.
- 8. Set the timer to be 2 second by keeping Register value to 0x09.
- 9. Enable the timer.

- 10. Add for loop to the in the void loop() function.
- 11. Add code to kick the timer inside the loop.
- 12. Add a delay of 1 sec.

## b. Configuration:-

- 1. Setting the BUILDIN PIN high.
- 2. Clock set to 32 KHz
- 3. Watchdog timer first disabled,
- 4. Timer window set to 2 seconds.
- 5. Enable the Watchdog timer.

### 3. Test Plan:

- 1. Add a print statement in the void loop().
- 2. Add a delay after the print statement, this will print slower.
- 3. Add the for loop and check if the loop is complete.
- 4. Add the code to kick the code to make the watchdog running without resetting.

#### 4. Results:

## 1. Set the Blue LED at the beginning of the program.

```
pinMode(LED_BUILTIN, OUTPUT);
digitalWrite(LED_BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
```

# 2. Clock (clock generator 2) frequency: 2048 Hz

```
GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
```

# 3. Set the loop period to 1 second, loop 10 times in the main loop () function such that it repeats.

## 4. Clear(kick) the WDT in the loop

```
WDT->CLEAR.reg = WDT_CLEAR_CLEAR_KEY;
```

# 5. Count down the number of loops and print a countdown message.

```
for (int loopCount = 9; loopCount > 0; loopCount--) {
   SerialUSB.print("Countdown ");
   SerialUSB.println(loopCount);
   // internal code to print and control WDT
}
```

# 6. Set the period using the delay(ms) function.

```
delay(1000); // setting the loop period to 1 second
```

# 7. Set the WDT period to 4 seconds.

```
WDT->CONFIG.bit.PER = 0x0A; // Set the timeout period to 4 seconds
```

# **Output**

```
Output Serial Monitor
 Sketch uses 10568 bytes (4%) of program storage space. Maximum is 262144 bytes.
 Atmel SMART device 0x10010005 found
 Device
             : ATSAMD21G18A
 Chip ID
            : 10010005
 Version
            : v2.0 [Arduino:XYZ] Sep 24 2018 14:26:24
 Address
            : 8192
 Pages
             : 3968
 Page Size
             : 64 bytes
 Total Size : 248KB
 Planes
            : 1
 Lock Regions: 16
 Locked
            : none
 Security
             : false
 Boot Flash : true
 BOD
            : true
 BOR
            : true
 Arduino
            : FAST CHIP ERASE
 Arduino
             : FAST_MULTI_PAGE_WRITE
 Arduino
             : CAN_CHECKSUM_MEMORY_BUFFER
 Erase flash
 done in 0.831 seconds
 Write 10824 bytes to flash (170 pages)
 [=========
                              ] 37% (64/170 pages)
 [==========
                              ] 75% (128/170 pages)
 [======] 100% (170/170 pages)
 done in 0.099 seconds
 Verify 10824 bytes of flash with checksum.
 Verify successful
 done in 0.014 seconds
 CPU reset.
```

Fig.3 - Console Output for Task 2 Case 1.

# **Screenshot**

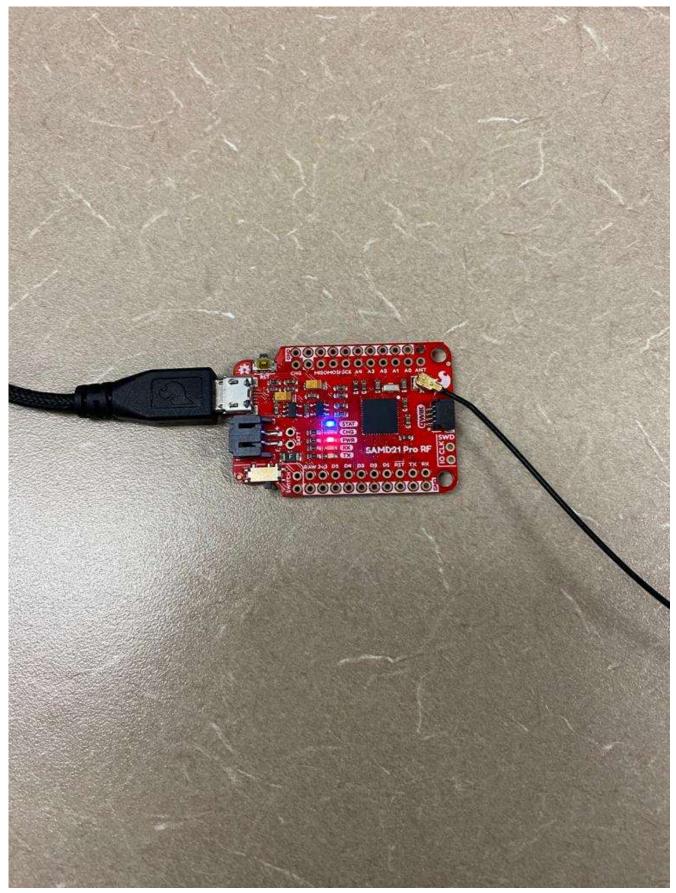


Fig.4 - LED remaining static throughout the execution.

```
Output
        Serial Monitor X
Message (Enter to send message to 'SparkFun SAMD21 Pro RF' on 'COM3'
Case 1 Starting loop
Countdown 9
Countdown 8
Countdown 7
Countdown 6
Countdown 5
Countdown 4
Countdown 3
Countdown 2
Countdown 1
Case 1 Starting loop
Countdown 9
Countdown 8
```

Fig 5. Countdown Timer Output.

#### Video Link -

See working video - Link to Video.

# Case 2:

# 1. Requirements:

- 1. SparkFun SAMD21 Pro RF
- 2. Laptop with Arduino 2.2.1 installed
- 3. Atmel-42181-SAM-D21\_Datasheet

# 2. Development Plan:

#### a. Procedure: -

- 1. Set the serial Output.
- 2. Set the LED pins high.
- 3. Set the time of the clock to 32 KHz.

- 4. Initialize the clock to 2048 Hz.
- 5. Disable the Watchdog Timer.
- 6. Wait for synchronization.
- 7. Initialize the clock watchdog timer.
- 8. Set the timer to be 2 second by keeping Register value to 0x09.
- 9. Enable the timer.
- 10. Add for loop to the in the void loop() function.
- 11. Comment code to kick the timer inside the loop.
- 12. Add a delay of 1 sec.

# b. Configuration:-

- 1. Setting the BUILDIN\_PIN high.
- 2. Clock set to 32 KHz
- 3. Watchdog timer first disabled,
- 4. Timer window set to 2 seconds.
- 5. Enable the Watchdog timer.

### 3. Test Plan:

- 1. Add a print statement in the void loop().
- 2. Add a delay after the print statement, this will print slower.
- 3. Add the for loop and check if the loop is complete.
- 4. Comment the code to kick the code to make the watchdog running without resetting.

### 4. Results:

Compare case 1 with clearing WDT and case 2 without clearing WDT and record the system behavior from the LED and the serial monitor message.

#### Discuss and explain the differences between the two cases.

The visual difference can be seen by carefully following the LED, in the first case the LED will be static and will keep on being in the ON state as the Watchdog timer is getting kicked before it can reset. Due to this the countdown will go from Count 9 to Count 0 as shown in Fig.7.

For the Second case, as we comment out the code that kicks the Watchdog therefore it resets the MCU. As a result we will see that the LED starts blinking after every 4 sec, because the watchdog timer is set to a window of 4 sec. Beacuse of the reset the Countdown timer in the for loop does not get to reset, as a result while the timer is going it will reset and we see only the first few counts from 9 to 0 as shown in Fig.8.

# **Output**

```
Sketch uses 10560 bytes (4%) of program storage space. Maximum is 262144 bytes.
Atmel SMART device 0x10010005 found
Device
           : ATSAMD21G18A
Chip ID
           : 10010005
Version
           : v2.0 [Arduino:XYZ] Sep 24 2018 14:26:24
Address
           : 8192
           : 3968
Pages
Page Size
           : 64 bytes
Total Size : 248KB
Planes
           : 1
Lock Regions : 16
Locked
           : none
Security
           : false
Boot Flash : true
BOD
           : true
BOR
           : true
Arduino
          : FAST_CHIP_ERASE
          : FAST_MULTI_PAGE_WRITE
Arduino
           : CAN_CHECKSUM_MEMORY_BUFFER
Arduino
Erase flash
done in 0.839 seconds
Write 10816 bytes to flash (169 pages)
                            ] 37% (64/169 pages)
[========
                            ] 75% (128/169 pages)
[======] 100% (169/169 pages)
done in 0.094 seconds
Verify 10816 bytes of flash with checksum.
Verify successful
done in 0.009 seconds
CPU reset.
```

Fig.6 - Console Output for Task 2 Case 2.

# **Screenshot**

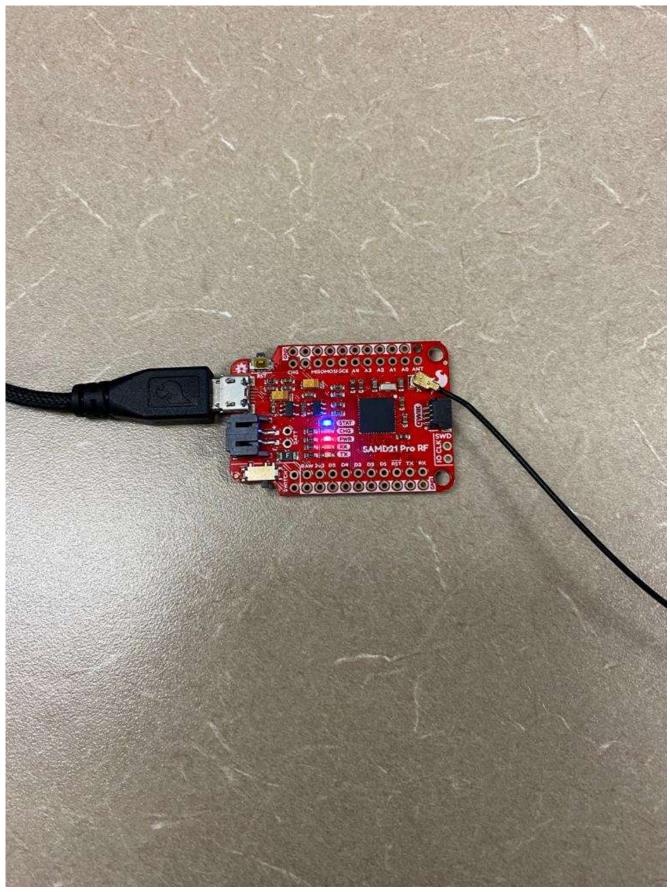


Fig.7 - Working of LED with blinking for 4 sec.

```
Output Serial Monitor X

Message (Enter to send message to 'SparkFun SAMD21 Pro RF' on 'COM3')

Countdown 7

Countdown 8

Countdown 7

Countdown 6

Countdown 8

Countdown 7

Countdown 7

Countdown 6

Countdown 7

Countdown 6

Countdown 7

Countdown 8

Countdown 8

Countdown 8

Countdown 8

Countdown 7

Countdown 8

Countdown 7

Countdown 8

Countdown 8

Countdown 8
```

Fig 8. Countdown Timer Output.

## Video Link -

See working video - Link to Video.

# Task 3:

# 1. Requirements:

- 1. SparkFun SAMD21 Pro RF
- 2. Laptop with Arduino 2.2.1 installed
- 3. Atmel-42181-SAM-D21\_Datasheet

# 2. Development Plan:

#### a. Procedure: -

- 1. Set the serial Output.
- 2. Set the LED pins high.
- 3. Set the time of the clock to 32 KHz.
- 4. Initialize the clock to 2048 Hz.
- 5. Disable the Watchdog Timer.
- 6. Wait for synchronization.
- 7. Initialize the clock watchdog timer.
- 8. Create another function that takes period input in milisecond and converts it into frequency of watchdog.
- 9. loop through values of clock of watchdog timers.
- 10. Find the smallest or equal value of frequency and clock of watchdog, this does the floor function.
- 11. Set the value to the return variable with corresponding clock frequency of watchdog timer.
- 12. Set the timer of watchdog in void setup() to the return variable.
- 13. Enable the Watchdog timer.

#### b. Configuration:-

- 1. Setting the BUILDIN\_PIN high.
- 2. Clock set to 32 KHz
- 3. Watchdog timer first disabled,
- 4. Timer window set to 2 seconds.
- 5. Enable the Watchdog timer.

### 3. Test Plan:

- 1. First set the LED high, and print something in the void loop.
- 2. Set the clock frequency to 2048 Hz.
- 3. Disable the Watchdog.
- 4. Configure Watchdog and Enable it.
- 5. Test with 3 different values of period, 1000, 2000 and 5000.
- 6. With the 1000, the blinking will be close to 1 sec and will be very fast.
- 7. With 2000, the blinking will be around 2 sec.
- 8. With 5000, the blinking will be around 4 sec.

### 4. Results:

### 1. Write a function that generates a WDT period by arbitrary input

```
int setWatchdog(int period)
// Convert the period from milliseconds to clock cycles
int clock_frequency = 2048;
int desired_cycles = period * clock_frequency / 1000;
// Define an array of available WDT periods in clock cycles
int available_cycles[] = {8, 16, 32, 64, 128, 256, 512, 1024, 2048, 4096, 8192, 16384};
int register_values_per_cycles[] = {0x0, 0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0
// Initialize register value to the smallest available period
int register value = 0x0;
// Loop through the array to find the closest available period that is less than or equal to the (
for (int i = 0; i < sizeof(available_cycles) / sizeof(available_cycles[0]); ++i)</pre>
        // find the closest cycle based on the floor calculation
        if (available_cycles[i] <= desired_cycles) {</pre>
        // register_value = register_values_per_cycles[i]; // Register values are indexed from 0x0
        register_value = i;
        } else {
        break; // Exit the loop as soon as we find a period greater than the desired period
}
// register_value = 0x0A;
return register_value;
```

# 2. Clock (clock generator 2) frequency: 2048 Hz

```
GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
```

## 3. Function input: period (millisecond)

#### Test 1- 2000 milliseconds

```
// set the timer to n milisecond
WDT->CONFIG.bit.PER = setWatchdog(2000);
```

#### Test 2-5000 milliseconds

```
// set the timer to n milisecond
WDT->CONFIG.bit.PER = setWatchdog(5000);
```

# 4a. Calculate the register value based on the period

```
// Convert the period from milliseconds to clock cycles
int clock_frequency = 2048;
int desired_cycles = period * clock_frequency / 1000;
```

# 4b. Take the floor to the closest value for values that cannot be mapped to register value.

```
// Loop through the array to find the closest available period that is less than or equal to the (
for (int i = 0; i < sizeof(available_cycles) / sizeof(available_cycles[0]); ++i)
{
     // find the closest cycle based on the floor calculation
     if (available_cycles[i] <= desired_cycles) {
        // register_value = register_values_per_cycles[i]; // Register values are indexed from 0x(
        register_value = i;
     } else {
        break; // Exit the loop as soon as we find a period greater than the desired period
     }
}</pre>
```

# **Output**

```
Output
 Sketch uses 10504 bytes (4%) of program storage space. Maximum is 262144 bytes.
 Atmel SMART device 0x10010005 found
             : ATSAMD21G18A
 Device
 Chip ID
             : 10010005
             : v2.0 [Arduino:XYZ] Sep 24 2018 14:26:24
 Version
 Address
             : 8192
             : 3968
 Pages
             : 64 bytes
 Page Size
 Total Size : 248KB
 Planes
             : 1
 Lock Regions : 16
 Locked
             : none
 Security
             : false
 Boot Flash : true
 BOD
             : true
 BOR
             : true
 Arduino
           : FAST_CHIP_ERASE
 Arduino
            : FAST_MULTI_PAGE_WRITE
 Arduino
             : CAN_CHECKSUM_MEMORY_BUFFER
 Erase flash
 done in 0.829 seconds
 Write 10760 bytes to flash (169 pages)
 [========
                              ] 37% (64/169 pages)
                              ] 75% (128/169 pages)
 [===========
 [======] 100% (169/169 pages)
 done in 0.101 seconds
 Verify 10760 bytes of flash with checksum.
 Verify successful
 done in 0.010 seconds
 CPU reset.
```

Fig.9 - Console Output for Task 3.

# **Screenshot**

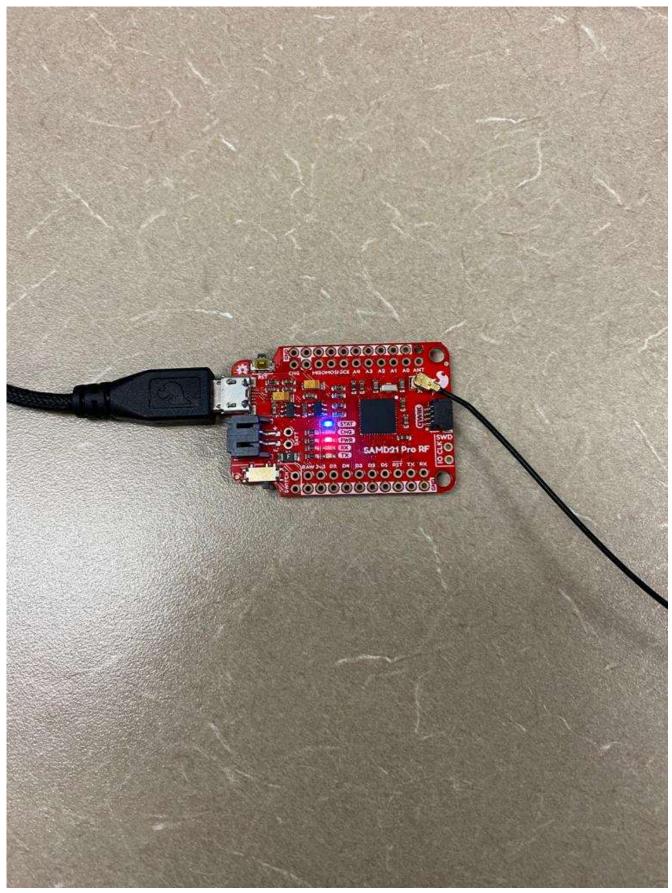


Fig.10 - Working of LED with blinking for 2000 milisecond.

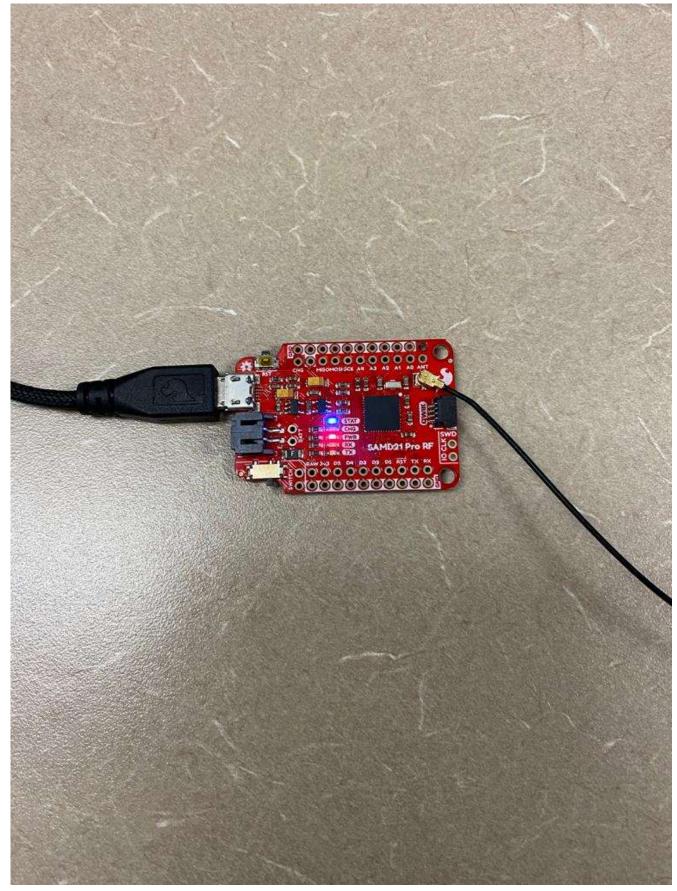


Fig.11 - Working of LED with blinking for 5000 milisecond.

#### Video Link -

See working video for test 1 at 2000 milisecond - Link to Video.

See working video for test 2 at 5000 milisecond - Link to Video.

# Task 4:

# 1. Requirements:

- 1. SparkFun SAMD21 Pro RF
- 2. Laptop with Arduino 2.2.1 installed
- 3. Atmel-42181-SAM-D21\_Datasheet

# 2. Development Plan:

#### a. Procedure: -

- 1. Set the serial Output.
- 2. Set the LED pins high.
- 3. Set the time of the clock to 32 KHz.
- 4. Initialize the clock to 2048 Hz.
- 5. Disable the Watchdog Timer.
- 6. Wait for synchronization.
- 7. Initialize the clock watchdog timer.
- 8. Set the timer to be 2 second by keeping Register value to 0x09.
- 9. In the Void Loop() add code to check the value of RCAUSE register from Power Manager, which has bit 5 as a representation of Watchdog reset.
- 10. Enable the Watchdog Timer.

## b. Configuration:-

- 1. Setting the BUILDIN\_PIN high.
- 2. Clock set to 32 KHz
- 3. Watchdog timer first disabled,
- 4. Timer window set to 2 seconds.
- 5. Enable the Watchdog timer.
- 6. Add an ifelse block in void loop to check the 5th bit of RCAUSE register and print if True.

# 3. Test Plan:

- 1. First set the LED high, and print something in the void loop.
- 2. Set the clock frequency to 2048 Hz.
- 3. Disable the Watchdog.
- 4. Configure Watchdog and Enable it.
- 5. Run code to find the print statement in the serial output.

# 1. After a reset event is there a way for the MCU to figure out if the last reset was due to WDT? If yes,

Yes, the SAMD21, provides a way to determine the cause of the last reset. This is done through a specific register that holds reset flags. In the case of the SAMD21, we can check the RCAUSE register to determine if the last reset was due to the Watchdog Timer (WDT). The 5th bit of this register is set if the last reset was caused by the WDT.

### 2. Write code that detects if the last reset was due to WDT:

### 3. If it was due to WDT, print a message in the console

```
SerialUSB.println("Last reset was due to the Watchdog Timer.");
```

# 4. What could be the importance of checking if the last reset was due to WDT?

Importance of Checking for WDT Reset

- Debugging: Helps identify software issues that led to the reset.
- Fault Recovery: Enables corrective actions like entering safe mode.
- Data Integrity: Triggers data verification if reset during critical operations.
- User Notification: Alerts users if manual intervention is needed.
- System Health: Useful for monitoring and maintenance.

By checking the WDT reset cause, the system can make informed decisions, enhancing its reliability.

#### **Screenshot**

```
Output Serial Monitor X

Message (Enter to send message to 'SparkFun SAMD21 Pro RF' on 'COM3')

"New Loop"

Last reset was due to the Watchdog Timer.
```

Fig 12 -The RCAUSE register has WDT reset bit as 1, showing that there was reset.

```
Output Serial Monitor X

Message (Enter to send message to 'SparkFun SAMD21 Pro RF' on 'COM3')

"New Loop"

Last reset was NOT due to the Watchdog Timer.
```

Fig 13 - The RCAUSE register has WDT reset bit as 0, showing that there was no reset.

# **Appendix**

The following codes for the different tasks:

#### Task 1:

```
void setup() {
  // Initialize the blue LED pin as an output (assuming it's connected to pin 7)
  SerialUSB.begin(9600); // Set the baud rate to 9600
 pinMode(LED_BUILTIN, OUTPUT);
  digitalWrite(LED BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
  GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
  // Disable the watchdog
  WDT->CTRL.reg = 0;
  // Initialize Clock Generator 2 for 2048 Hz
  GCLK->GENCTRL.reg = GCLK_GENCTRL_ID(2) |
                      GCLK GENCTRL GENEN
                      GCLK_GENCTRL_SRC_OSCULP32K
                      GCLK GENCTRL DIVSEL;
 while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
  GCLK->CLKCTRL.reg = GCLK_CLKCTRL_ID_WDT
  GCLK CLKCTRL CLKEN
  GCLK_CLKCTRL_GEN_GCLK2;
 while (WDT->STATUS.bit.SYNCBUSY); // Wait for synchronization
  // set the timer to 2 sec
 WDT->CONFIG.bit.PER = 0x09; // Set the timeout period to 2 seconds
  // Enable the WDT
 WDT->CTRL.bit.ENABLE = 1; // ENABLE WDT
 while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
}
void loop() {
  // Do nothing
}
```

#### Task 2:

```
void setup() {
  // Initialize the blue LED pin as an output (assuming it's connected to pin 7)
  SerialUSB.begin(9600); // Set the baud rate to 9600
 pinMode(LED_BUILTIN, OUTPUT);
  digitalWrite(LED_BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
  GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
  // Disable the watchdog
  WDT->CTRL.reg = 0;
  // Initialize Clock Generator 2 for 2048 Hz
  GCLK->GENCTRL.reg = GCLK GENCTRL ID(2)
                      GCLK GENCTRL GENEN
                      GCLK_GENCTRL_SRC_OSCULP32K
                      GCLK GENCTRL DIVSEL;
 while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
  GCLK->CLKCTRL.reg = GCLK_CLKCTRL_ID_WDT
  GCLK CLKCTRL CLKEN
  GCLK_CLKCTRL_GEN_GCLK2;
 while (WDT->STATUS.bit.SYNCBUSY); // Wait for synchronization
  // set the timer to 2 sec
 WDT->CONFIG.bit.PER = 0x0A; // Set the timeout period to 4 seconds
  SerialUSB.println("enabling WDT");
  // Enable the WDT
 WDT->CTRL.bit.ENABLE = 1; // ENABLE WDT
 WDT->INTENCLR.bit.EW = 1; // Disable early warning interrupt
 WDT->CTRL.bit.WEN = 0; // Disable window mode
}
void loop() {
  SerialUSB.println(" Case 1 Starting loop ");
  // Loop ten times
  for (int loopCount = 9; loopCount > 0; loopCount--) {
    //print the countdown
    SerialUSB.print("Countdown ");
    SerialUSB.println(loopCount);
    WDT->CLEAR.reg = WDT_CLEAR_CLEAR_KEY;
    delay(1000);
```

}			

#### Case 2:

```
void setup() {
 // Initialize the blue LED pin as an output (assuming it's connected to pin 7)
 SerialUSB.begin(9600); // Set the baud rate to 9600
 pinMode(LED_BUILTIN, OUTPUT);
 digitalWrite(LED_BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
 GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
  // Disable the watchdog
 WDT->CTRL.reg = 0;
  // Initialize Clock Generator 2 for 2048 Hz
 GCLK->GENCTRL.reg = GCLK_GENCTRL_ID(2)
                      GCLK GENCTRL GENEN
                      GCLK GENCTRL SRC OSCULP32K
                      GCLK GENCTRL DIVSEL;
 while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
 GCLK->CLKCTRL.reg = GCLK CLKCTRL ID WDT
 GCLK_CLKCTRL_CLKEN
  GCLK CLKCTRL GEN GCLK2;
 while (WDT->STATUS.bit.SYNCBUSY); // Wait for synchronization
 // set the timer to 2 sec
 WDT->CONFIG.bit.PER = 0x0A; // Set the timeout period to 4 seconds
 SerialUSB.println("enabling WDT");
 // Enable the WDT
 WDT->CTRL.bit.ENABLE = 1; // ENABLE WDT
 WDT->INTENCLR.bit.EW = 1; // Disable early warning interrupt
 WDT->CTRL.bit.WEN = 0; // Disable window mode
}
void loop() {
 SerialUSB.print(" Case 2 Starting loop ");
 // Loop ten times
 for (int loopCount = 9; loopCount > 0; loopCount--) {
   //print the countdown
   SerialUSB.print("Countdown ");
    SerialUSB.println(loopCount);
    // WDT->CLEAR.reg = WDT_CLEAR_CLEAR_KEY;
```

```
delay(1000);
}
```

#### Task 3:

```
int setWatchdog(int period)
{
 // Convert the period from milliseconds to clock cycles
 int clock_frequency = 2048;
 int desired_cycles = period * clock_frequency / 1000;
 // Define an array of available WDT periods in clock cycles
  int available cycles[] = {8,
                                         32, 64, 128, 256, 512, 1024, 2048, 4096, 8192, 1638<sup>2</sup>
                                   16,
 int register_values_per_cycles[] = \{0x0, 0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, (
 // Initialize register_value to the smallest available period
  int register value = 0x0;
 // Loop through the array to find the closest available period that is less than or equal to the
  for (int i = 0; i < sizeof(available_cycles) / sizeof(available_cycles[0]); ++i) {</pre>
   // find the closest cycle based on the floor calculation
   if (available cycles[i] <= desired cycles) {</pre>
      // register value = register values per cycles[i]; // Register values are indexed from 0x0
     register value = i;
    } else {
     break; // Exit the loop as soon as we find a period greater than the desired period
    }
  }
 // register value = 0x0A;
 return register value;
}
void setup() {
 // Initialize the blue LED pin as an output (assuming it's connected to pin 7)
 SerialUSB.begin(9600); // Set the baud rate to 9600
 pinMode(LED_BUILTIN, OUTPUT);
 digitalWrite(LED_BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
 GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
  // Disable the watchdog
 WDT->CTRL.reg = ∅;
 // Initialize Clock Generator 2 for 2048 Hz
 GCLK->GENCTRL.reg = GCLK_GENCTRL_ID(2)
                      GCLK GENCTRL GENEN
                      GCLK_GENCTRL_SRC_OSCULP32K |
                      GCLK GENCTRL DIVSEL;
 while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
```

```
GCLK->CLKCTRL.reg = GCLK_CLKCTRL_ID_WDT |
GCLK_CLKCTRL_CLKEN |
GCLK_CLKCTRL_GEN_GCLK2;

while (WDT->STATUS.bit.SYNCBUSY); // Wait for synchronization

// set the timer to n milisec
WDT->CONFIG.bit.PER = setWatchdog(5000);

// Enable the WDT
WDT->CTRL.bit.ENABLE = 1; // ENABLE WDT

while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
}

void loop() {
    // Do nothing
}
```

#### Task 4:

```
void setup() {
 // Initialize the blue LED pin as an output (assuming it's connected to pin 7)
 SerialUSB.begin(9600); // Set the baud rate to 9600
 pinMode(LED_BUILTIN, OUTPUT);
 digitalWrite(LED_BUILTIN, HIGH); // Set the blue LED HIGH at the beginning
 GCLK->GENDIV.reg = GCLK_GENDIV_ID(2) | GCLK_GENDIV_DIV(3);
  // Disable the watchdog
 WDT->CTRL.reg = 0;
  // Initialize Clock Generator 2 for 2048 Hz
 GCLK->GENCTRL.reg = GCLK GENCTRL ID(2)
                      GCLK GENCTRL GENEN
                      GCLK_GENCTRL_SRC_OSCULP32K
                      GCLK GENCTRL DIVSEL;
 while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
 GCLK->CLKCTRL.reg = GCLK_CLKCTRL_ID_WDT
 GCLK CLKCTRL CLKEN
  GCLK_CLKCTRL_GEN_GCLK2;
 while (WDT->STATUS.bit.SYNCBUSY); // Wait for synchronization
  // set the timer to 2 sec
 WDT->CONFIG.bit.PER = 0x09; // Set the timeout period to 2 seconds
 // Enable the WDT
 WDT->CTRL.bit.ENABLE = 1; // ENABLE WDT
 while(GCLK->STATUS.bit.SYNCBUSY); // Think about why this is used
}
void loop() {
 SerialUSB.println("\n\n\n\"New Loop\" \n\n\n");
    // Check if the last reset was due to the Watchdog Timer
    if (PM->RCAUSE.reg & (1 << 5)) { // WDT is the 5th bit in RCAUSE
    SerialUSB.println("Last reset was due to the Watchdog Timer.");
    }
 else {
    SerialUSB.println("Last reset was NOT due to the Watchdog Timer.");
```

}			