A logo with a black background

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**Department of Electronics and Electrical Communication Engineering.**

**Faculty of Engineering Cairo university**

**2-element slot-fed microstrip patch   
antenna array**

Cairo University - Faculty of Engineering

Electronics and Communications Engineering Department

**Antenna and waveguides**

**ELC 3050 – Fall 2024**

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# Introduction and Problem description

## Introduction

Microstrip Patch Antennas (MPAs) are widely used in modern wireless communication systems due to their low-profile, lightweight, and ease of integration with other circuit components. These antennas are particularly useful in high-frequency applications such as 5G and millimeter-wave (mm Wave) communications, where bandwidth demand and system performance are critical.

A microstrip patch antenna typically consists of a conducting patch placed on a dielectric substrate with a ground plane on the opposite side. The shape of the patch, the substrate material, and the dimensions are chosen to meet specific resonance conditions, providing efficient radiation at a given frequency.

In this project, the focus is on designing a **two-element array** of **slot-fed microstrip patch antennas** operating at a frequency of **26 GHz**, which is a key frequency band for 5G and future wireless communication systems. The array configuration aims to improve the antenna's gain and directivity, essential for high-frequency applications that require precise and high-performance communications.1

## Problem description

The goal of this project is to design and analyze a two-element array of slot-fed microstrip patch antennas operating at 26 GHz. The primary objective is to achieve the desired radiation characteristics, including a specific gain, directivity, and impedance matching, while also addressing the challenges associated with high-frequency operation.

Key aspects of the project include:

1. **Antenna Design**: Design of individual slot-fed microstrip patch antennas, including the selection of patch shape, size, and dielectric material to ensure resonance at 26 GHz.
2. **Slot Feeding**: The use of slot feeding for the antenna, where the excitation is achieved through a slot in the ground plane. This method offers benefits such as reduced spurious radiation and better impedance matching.
3. **Array Configuration**: The two-element array configuration must be designed to achieve constructive interference, enhancing the antenna’s gain and directivity. The array elements must be spaced appropriately to prevent undesirable interference or mutual coupling.
4. **Impedance Matching**: Ensuring that the antenna’s impedance is matched to the transmission line to maximize power transfer and minimize reflections at the operating frequency of 26 GHz.
5. **Simulation and Optimization**: Using simulation software (such as CST Microwave Studio, HFSS, or others) to model the antenna array, simulate its performance, and optimize its design for optimal radiation characteristics.
6. **Performance Analysis**: Evaluating the antenna array’s performance based on parameters such as return loss, gain, radiation pattern, and bandwidth to ensure that it meets the specifications for 26 GHz operation.

The problem involves not only designing the antenna itself but also considering the effects of the operating frequency, material properties, and array configuration on overall performance. Additionally, challenges such as minimizing loss, optimizing the array layout, and managing mutual coupling between elements must be addressed.

By successfully designing this two-element array of slot-fed microstrip patch antennas, the project aims to contribute to the development of efficient antenna solutions for high-frequency applications in next-generation communication systems, such as 5G and beyond.2

# Design Procedure

In this section, we will discuss the design procedure we followed to reach our final design. We actually worked on **two designs** in this project so we will discuss both of them, but we will divide the design procedure into two main stages, and each stage into multiple sections as follows:

1. **Single element slot-fed microstrip patch antenna.**
   1. Feeding structure
   2. Substrates material and thickness
   3. Slot dimensions
   4. Patch dimensions
2. **2-element array of the same antenna.**
   1. Feeding network
   2. Substrate dimensions
   3. Elements spacing

## Single element design

In this stage we worked on the single element alone before instantiating it in the full antenna system.

First, we will explain the feeding structure for **the slot-fed microstrip patch** antenna.

A diagram of a microstrip

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Figure : Slot-fed microstrip patch antenna structure

### Feeding structure

The feeding structure is simply composed of microstrip feed line, feeding slot – which is known as coupling aperture – and the radiating microstrip patch in addition to two substrates: feed substrate and antenna substrate as illustrated in **figure 1**. The microstrip technology is simple, with just two different-shaped conductors and a substrate between them. So, we worked with the information we have, to initially build the single element structure. And that applies to both designs.

### Substrates thickness

For the substrates material we used **Rogers RO4350B** with dielectric constantin the first design and **Rogers RO4003C** with dielectric constantin the second design. The used materials dielectric constant is typically between 3 and 4, where in this range the dimensions and the cost are acceptable as there is a trade-off between them in choosing material, as increases the dimensions decreases and the cost increases. We used the same material for both feed and antenna substrates.

For the substrates thickness, in the first design we chose from Rogers RO4350B standard thickness3 to make it irradiative , and to make it radiative .

In the second design we got the substrates thicknesses from **E. A. Soliman, A. Vasylchenko, V. Volski, G. A. E. Vandenbosch, and W. De Raedt** research paper4, and.

### Slots dimensions

For the slot dimensions, in the first design we got its dimensions from **E. Setijadi** research paper1 as a starting point before tuning. and .

For the second design we got its dimensions from **E. A. Soliman** research paper.4 and .

### Patch dimensions

For the patch dimensions, in the first design we used an online calculator to make it operate at the required frequency 26 GHz with 50 to be matched with the typical sources and we did the same for the microstrip feed line then we tuned the patch length to get the desired resonating frequency – where theoretically but it doesn’t apply accurately so we used the online calculator – and tuned its width to get the desired input impedance 50 . And finally, we got   
and.

In the second design we tested **E. A. Soliman** research dimensions4 and it worked well,   
 and .

Now, after we designed a single slot-fed microstrip patch antenna operating at 26 GHz with 50 as shown in figures ---, we instantiated it to build a 2-element array, but it was not that easy. So, in the next section we will explain its design procedure.

## 2-element array

# Results and Discussion

## Verification of EM tool results

Before discussing our results, we should verify the EM tool we used in the project. So, in this section we are willing to verify the EM tool results by benchmarking against another well-Known source. We designed a dipole and observed the output if it’s close to the excepted one ideally.

We used the material of the antenna (dipole) as copper and the radiation medium is air.

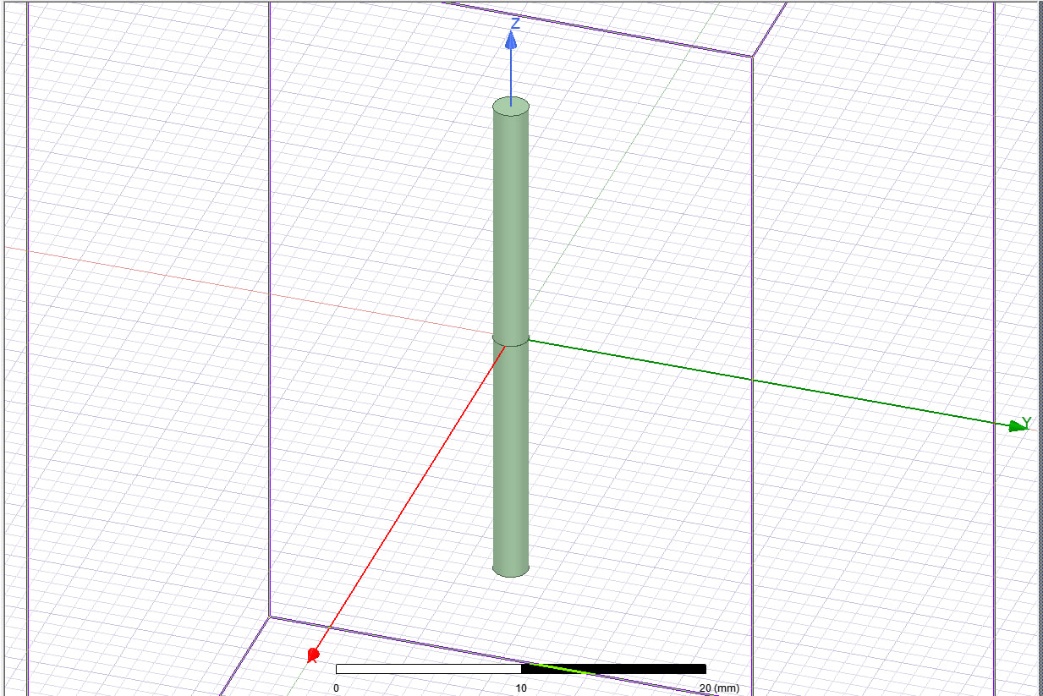


Figure : dipole structure

In the first simulation we adjusted the diameter of the dipole to and its length to and the gap between the two wires - due to the supposed feeding – to . The dipole structure is shown in **figure 1**.

From the dipole length we will get the resonance frequency as follows:

, .

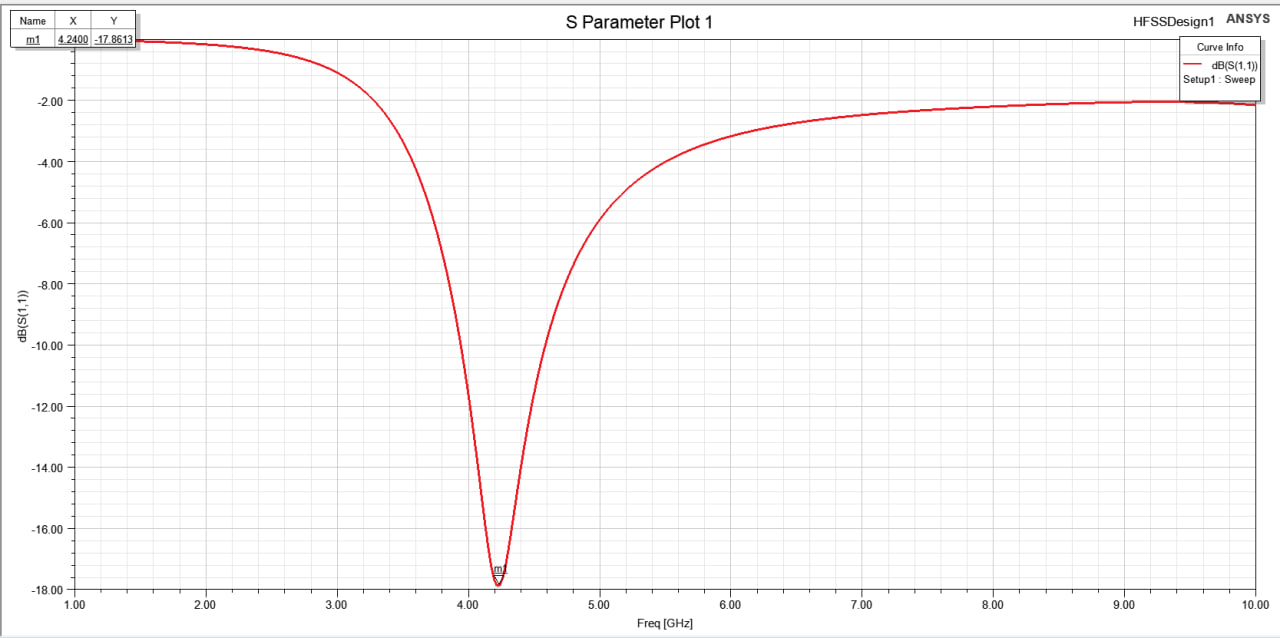


Figure : dipole return loss

As we see in **figure 2**, it is resonant at far from the desired frequency as a result of the non-ideality due to the wire diameter and the gab between the two wires which are both ideally zero, so we reduced them to the minimum possible values for the current mesh size.

The parameters for the next simulation are and the gap

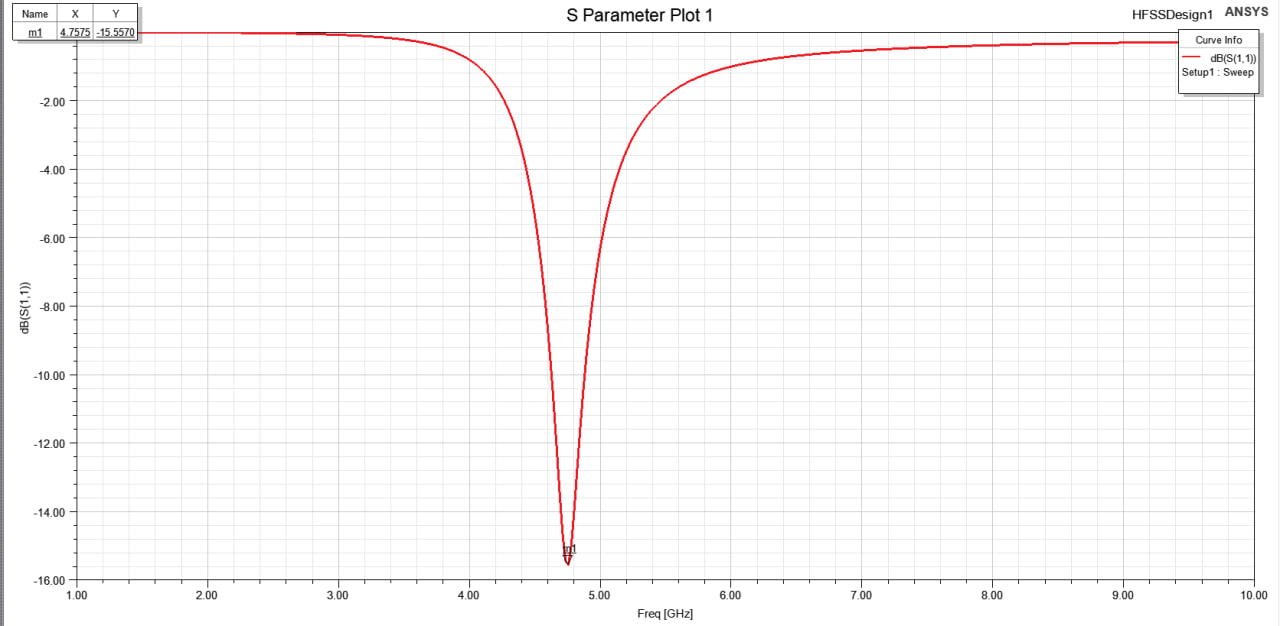


Figure : New dipole return loss

As shown in **figure 3**, the frequency became closer to the ideal frequency.

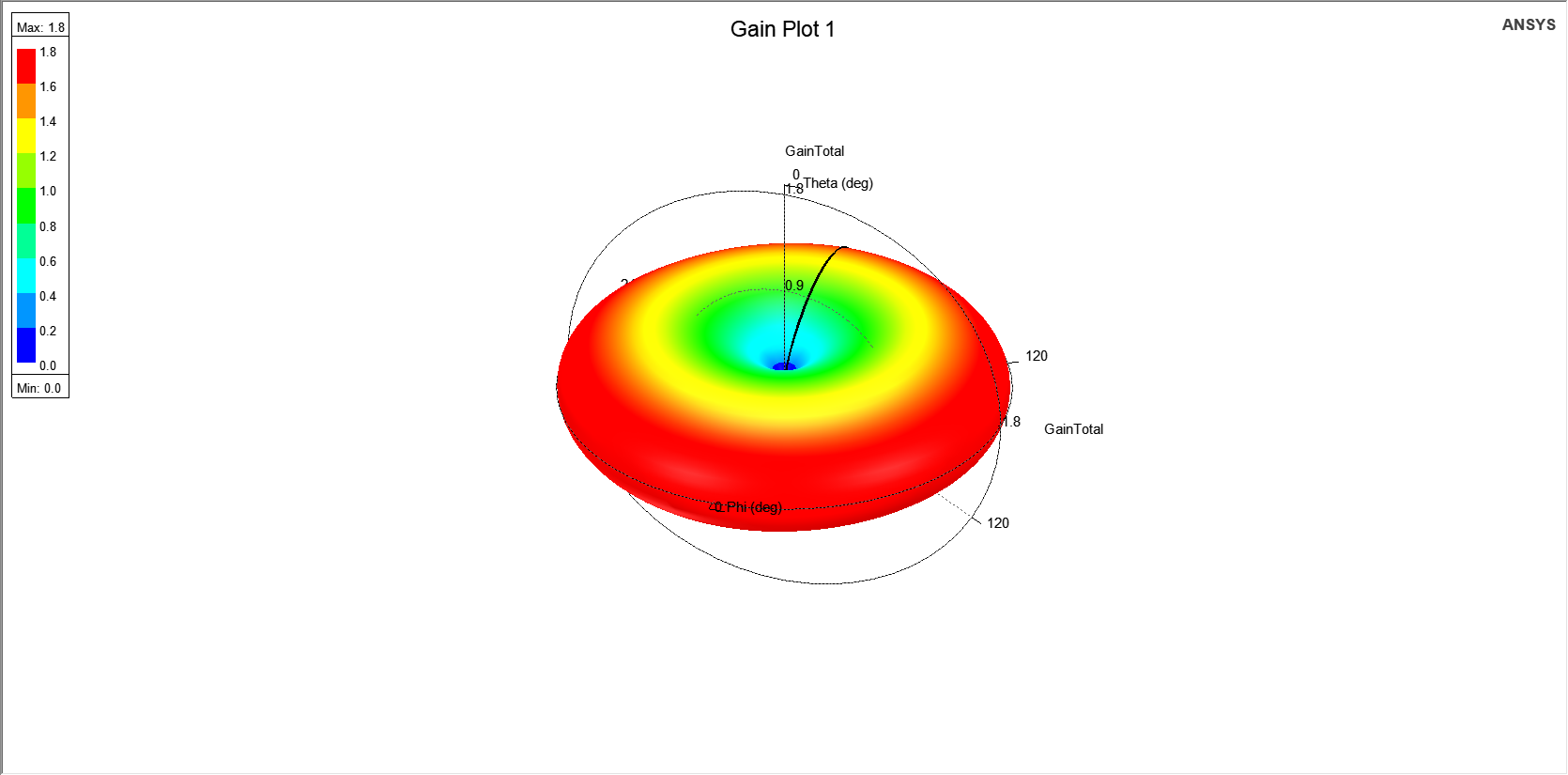


Figure : dipole gain (directivity)

For the radiation pattern, it is the same in both simulations and same as the expected shape from the lectures which is the donut shape as shown in figure 4. But it’s more directive with directivity 1.8 due to non-ideallty as we discussed above.

Note that the illustrated figure is the dipole gain but it’s the same as the directivity because it’s matched with the source.

Now, after we verified our EM tool we will present and discuss our simulation results for both designs in this project.

# Final Design Layout

# Conclusion

# References

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3. [Rogers Corporation, *RO4000 Series High Frequency Circuit Materials*. Advanced Connectivity Solutions, Chandler, AZ, USA, 2018.](https://www.bing.com/ck/a?!&&p=9432b18362b983a49c280a245b0b328aaccbd68ccc3eb9e1262388d704c7aadeJmltdHM9MTczNDgyNTYwMA&ptn=3&ver=2&hsh=4&fclid=34a66137-39f0-6990-3f67-758638496870&psq=Rogers-ro4000-laminates-ro4003c-and-ro4350b-data-sheet&u=a1aHR0cHM6Ly9yb2dlcnNjb3JwLmNvbS8tL21lZGlhL3Byb2plY3Qvcm9nZXJzY29ycC9kb2N1bWVudHMvYWR2YW5jZWQtZWxlY3Ryb25pY3Mtc29sdXRpb25zL2VuZ2xpc2gvZGF0YS1zaGVldHMvcm80MDAwLWxhbWluYXRlcy1ybzQwMDNjLWFuZC1ybzQzNTBiLS0tZGF0YS1zaGVldC5wZGY&ntb=1)
4. [E. A. Soliman, A. Vasylchenko, V. Volski, G. A. E. Vandenbosch, and W. De Raedt, "Series-Fed Microstrip Antenna Arrays Operating at 26 GHz," presented at the IEEE Antennas and Propagation Society International Symposium (APS), 2010.](https://www.researchgate.net/publication/224171519_Series-fed_microstrip_antenna_arrays_operating_at_26_GHz)