CprE 381, Computer Organization and Assembly Level Programming

Team Contract – Project Part 1

Project Teams Group: TermProj1_3_06

Team Members: Abrahim (Abe) Toutoungi

Mitchell Driscoll

Discuss the following aspects of teamwork with your team – make sure to get input from each member. Write down your team's consensus for each of the bolded headings. Italicized text contains instructions and examples and should be deleted once you've read it. Please see the example contract for rough length expectations.

Course Goals: *List and acknowledge the goals of your individual team members.*
Abe: Learn stuff and hopefully end with an A ish.
Mitchell: Minimize points lost and try not to fall behind

Team Expectations:

- **Conduct:** *What are the expectations for personal conduct of group members?*

Uphold our goals and objectives. Be nice.

- **Communication:** *What is the best mode of communication for the group? How often should communication occur? How fast should a response be expected?*

Teams and discord. Responses will depend on what we're working on.

- **Group conventions:** *Naming conventions? Compilation and simulation methodology? Testbench strategies? Do file usage? Version control strategies? Commenting standards?*

We can use what they use in the lab samples. s_ for signals etc. Comment a lot on everything. If it comes to it, we can use do files but neither of us have been using them until now. Use git and add like version x at the top. Just comment a lot.

- **Meetings:** *Given the significant portion of the course that the lab covers, it is expected that your team will spend more time working on the labs than in your scheduled lab sections. How will your group expect to handle this? Please include at least two additional times outside of lab that your team can meet (preferably in-person). Examples of other issues to consider include:*
  - *Work together in-person outside of lab sections?*

11 am – 1 pm on Thursdays

  - *Work together online outside of lab sections?*

Yes use discord or teams. Schedule based on week. Fine with weekends

  - *Work separately on responsibilities?*

Not ideally. But if someone has time when the other doesn't it's okay. Update teammate and comment code.

- **Peer Evaluation Criteria:** *Please create a brief criteria for how effort and contribution are defined. Note that teams with **vastly** divergent scores may require a meeting with course instructor and result in different grades for different group members. Teams with reasonably equitable scores will receive the same grade. As long as you are trying.*

Role Responsibilities: *Complete the following planning table. Each lab part should be the responsibility of one team member. Also make sure that no one team member is the lead on both the design and test aspects of a single lab part. These guidelines aid in all students having a complete view of the lab. Plan for an anticipated deadline (read the lab manual and ask your TAs for assistance in setting up a good timeline). Note that the non-lead is encouraged to participate and support the lead wherever possible, increasing both the quality of the lab part and each team member's knowledge.*

Lab Part	Estimated Time	Design		Test	
		Lead	Deadline	Lead	Deadline
High-level design	1 hr	Both	3/1	Both	3/6
Test programs	4 hr	Both	3/22	Both	3/25
Control logic	2 hr	Mitchell	3/1	Abe	3/6
Fetch logic	3 hr	Abe	3/1	Mitchell	3/6
Barrel shifter	2 hr	Mitchell	3/8	Abe	3/13
ALU integration + Misc updates	2 hr	Abe	3/15	Mitchell	3/20
High-level integration	4 hr	Abe	3/15	Mitchell	3/20
Synthesis (human effort)	1.5 hr	Both	3/22	Both	3/25

*Estimated Time is given as a **very rough** guide for even distribution of tasks assuming you've already read through the lab document and have the prerequisite knowledge. Depending on your group's skill and prerequisite knowledge, some tasks may take disproportionately long or short. For your future planning, track this – for future prelabs you will be asked to note why past tasks took longer than expected and how you might avoid such issues in the future.*

Integrity of Work: *Do not delete the following.* We agree that the work we provide to other team members and ultimately submit for a grade is a direct result of our own work as described in the course syllabus. Specifically, we will generate all VHDL code ourselves and not copy VHDL code from online sources, other groups, book companion material, or past student projects to which anyone outside of my team has contributed.

Student Signature _____AT_____ **Date** _2/23/24_____

Student Signature ___MD_____ **Date** __2/23/24_____