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## 1E2005

B.Tech. I Sem. (Main/Back) Examination - 2014 105 Basic Electrical & Electronics Engineering Common to all Branches

Time: 3 Hours]

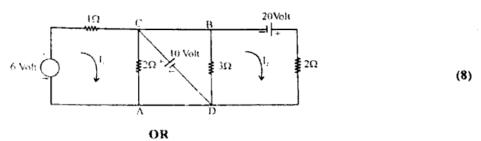
[Total Marks: 80

(Min. Passing Marks:

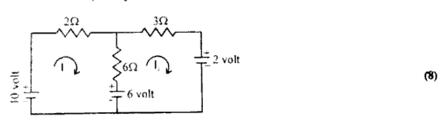
Instructions to Candidates

Attempt any five questions, selecting one question from each unit. All questions carry equal marks. Schematic diagrams must be shown wherever necessary. Any data you feel missing suitably be assumed and stated clearly. Units of quantities used/calculated must be stated clearly. UNIT - I

- State and explain Thevenin's theorem. Illustrate the application of this theorem with reference to an appropriate electric circuit
  - Find the voltage drop between terminal AB, CB and AD in Fig.



- State and explain superposition theorem. Illustrate the application of this theorem with reference to an appropriate 1. (a)
  - Using loop current method find the current I, and I, in Fig.



UNIT - H

- A series RL circuit has resistance and reactance of 15 ohm and 10 ohm respectively. Calculate the value of capacitor which when connected across the series combination in parallel, the system attain unity power factor.
- For a single phase sinusoidal waveform find the Average and RMS values in terms of maximum value. Determine (8) the form factor of sine wave.

OR

Two coils A and B are connected in series across a 240 V, 50Hz supply. The resistance of A is 5  $\Omega$  and the inductance of B is 0.015 H. If the input from the supply is 3kW and 2kVAR, find the inductance of A and the resistance of B. (16)Calculate the voltage across such coil.

## UNIT - III

Explain the principle of DC machines and construction of DC machine? 3.

(12)

