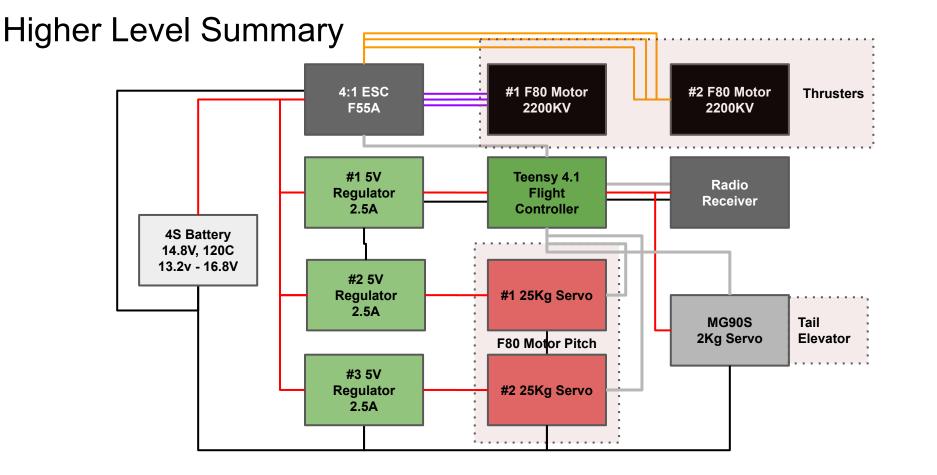
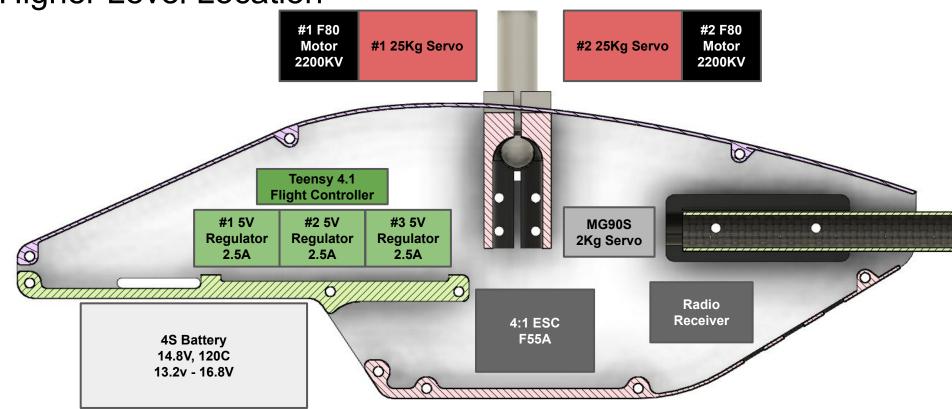
## VTOL V1 Circuit

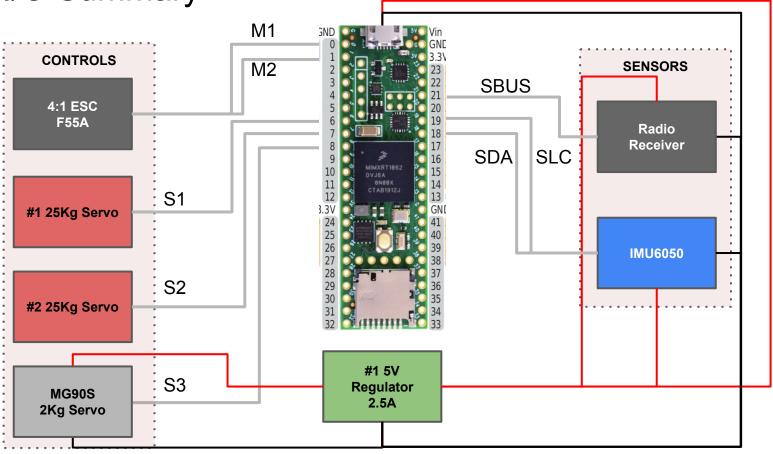
Drew Britten 2/1/2023



**Higher Level Location** 



Teensy I/O Summary



GND Vin (3.6 to 5.5 volts)														
<b>PWM</b>	CRX2	CS1	RX1	0	00	1		o G	ND					
<b>PWM</b>	CTX2	MISO1	TX1	1	0		31	<b>3</b>	3.3V (250 mA max)					
PWM	OUT2			2			1""1	0 2	3 A	9	CRX:	1	MCLK1	PWM
PWM	LRCLK2			3	O'		June.	0 2	2 A	8	CTX:	1		PWM
PWM	BCLK2		- b	4				<b>O</b> 2	1 A	7	RX5		BCLK1	
PWM	IN2		ıll di nter	5				<b>O</b> 2	0 A	6	TX5		LRCLK1	
PWM	OUT1D		gita	6	Our	7		0 1	9 A!	5			SCL	PWM
PWM	OUT1A	RX2	Cap	7	OL		, manic	$\bigcirc$ 1	8 A	4			SDA	PWM
PWM	IN1	TX2	All digital pins have Interrupt capability.	8	OF			0 1	7 A	3	TX4		SDA1	
PWM	OUT1C		<u> </u>	9	O	1		0 1	6 A	2	RX4		SCL1	
PWM	MQSR	CS		10	O	MIMXRT DVJ6A	1052	<b>O</b> 1	5 A	1	RX3	S/P	DIF IN	PWM
PWM	CTX1	MOSI		11		CTAB1	200	A 1888	4 A		TX3	S/P	DIF OUT	D14000000000000000000000000000000000000
PWM	MQSL	MISO		12	O.			A	3 (LI	ED)	SCK			PWM
3.3V						GND GND								
PWM	SCL2	TX6	A10	24	OL		100		1 A					
PWM	SDA2	RX6	A11	25			mi.	V. 188	0 A:					
		MOSI1	A12	26			A ILLE	1000		15	MISC		OUT1A	
ASS. 17.10 TO		SCK1	A13	27	DXC			1000		14	CS1		IN1	ACCUPATION
PWM		RX7		28		-6		<b>2</b> 3			CS			PWM
PWM		TX7		29	9		9	1000	6		CS			PWM
	CRX3			30					5		TX8			
	CTX3			31	2			~	4		RX8		MOLIVO	DIMM
	OUT1B			32			111-10	<b>Q</b> 3	3				MCLK2	PWM