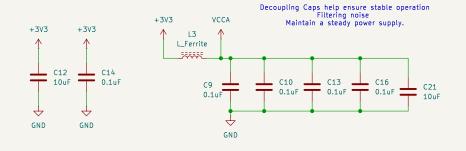
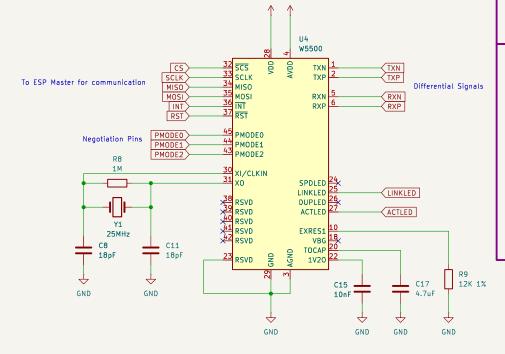


DECOUPLING CAPS

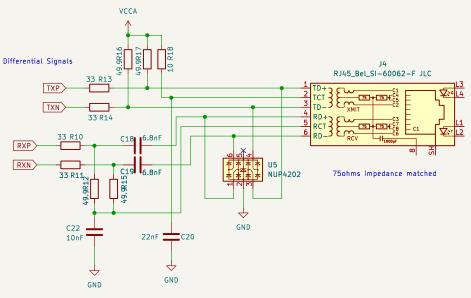


W5500 ETHERNET PHY/MAC

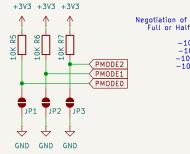
+3V3VCCA



JACK & MAGNETICS



AUTO-NEGOTIATING MODES



Negotiation of Speed and Connection type ie Full or Half Duplex through these pins. You can set:

-10 Mbps Half-Duplex
-10 Mbps Full-Duplex
-100 Mbps Half-Duplex
-100 Mbps Full-Duplex

Approved By: Jones Kisaka Deigned By: Rodney Osodo

Abstract Machines

Sheet: /Ethernet/ File: ethernet.kicad sch

Title: SO Base Board

Size: A4 Date: 2025-02-26 Rev: v0.1.0 KiCad E.D.A. 8.0.8 ld: 3/4

