MULTIPLEXER / MUX / DATA SELECTOR

- A multiplexer is a combinational circuit that selects one of many input lines (2^n) and directs it to its single output line.
- There are *n* selection lines whose bit combinations determine which input is selected.
- A multiplexer is also called a data selector, since it selects one of many inputs and steers the binary information to the output line.
- The size of a multiplexer is specified by the number 2^n of its input lines and the single output line. It is then implied that it also contains n selection lines.
- Example: 2X1 MUX, 4X1 MUX, 8X1 MUX, 16X1 MUX etc.

• 4-to-1 MUX

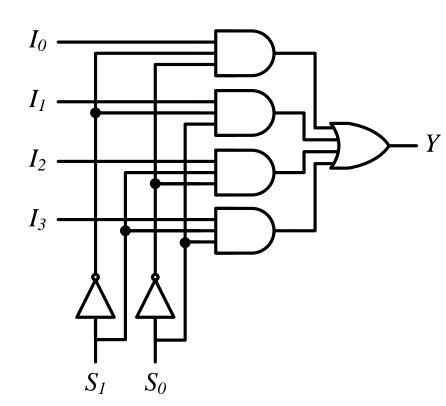


Fig: Logic diagram of a 4X1 MUX

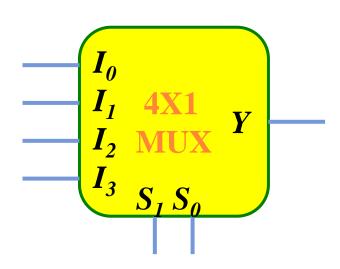


Fig: Block diagram of a 4X1 MUX

Function Table of a 4X1 MUX:

$S_1 S_0$	Y
0 0	$\mathbf{I_0}$
0 1	I_1
1 0	I_2
1 1	I_3

Output Equation:



USES OF MUX

• It is used for connecting two or more sources to a single destination among computer units.

• It is useful for constructing a common bus system etc.

FUNCTION IMPLEMENTATION USING MUX

- o (n+1) variable function can be implemented with 2ⁿ x 1 MUX
- Simplify the function in sum of minterms form
- Among (n+1) variables, n variables are used as selector and one variable is connected with input lines

Procedure 1

 $F(A, B, C) = \sum (1, 3, 5, 6)$ Steps:

- 1. Choose the selector variables. Lets choose,
 - B, C as selector S_1 and S_0
 - A as input line
- 2. In the first row, list the name of the input lines of the multiplexers horizontally
- 3. In the second row, list the minterms where A is complemented
- 4. In the third row, list the minterms where A is uncomplemented

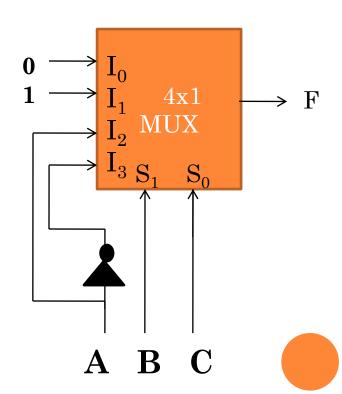
 $F(A, B, C) = \sum (1, 3, 5, 6)$ Steps:

- 5. Circle the minterms for which the function outputs 1
- 6. Fourth row presents the multiplexer inputs
 - If the two minterms in a column are not circled, apply 0 to the corresponding multiplexer input
 - If the two minterms in a column are circled, apply 1 to the corresponding multiplexer input
 - If the bottom minterm is circled and the top is not circled, apply A to the corresponding multiplexer input
 - If the top minterm is circled and the bottom is not circled, apply A' to the corresponding multiplexer input

 \circ F(A, B, C) = Σ (1, 3, 5, 6)

Implementation Table:

MUX input line	I_0	I ₁	\mathbf{I}_2	\mathbf{I}_3
A'	0	1	2	3
A	4	5	6	7
Input values	0	1	A	A'

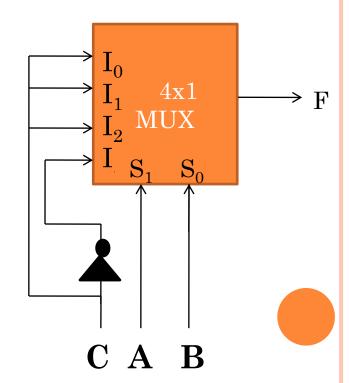


 \circ F(A, B, C) = Σ (1, 3, 5, 6)

What if A, B are the selectors and C goes to input line?

Implementation Table:

MUX input line	I_0	I ₁	\mathbf{I}_2	\mathbf{I}_3
C '	0	2	4	6
${f C}$	1	3	5	7
Input values	C	C	C	C'

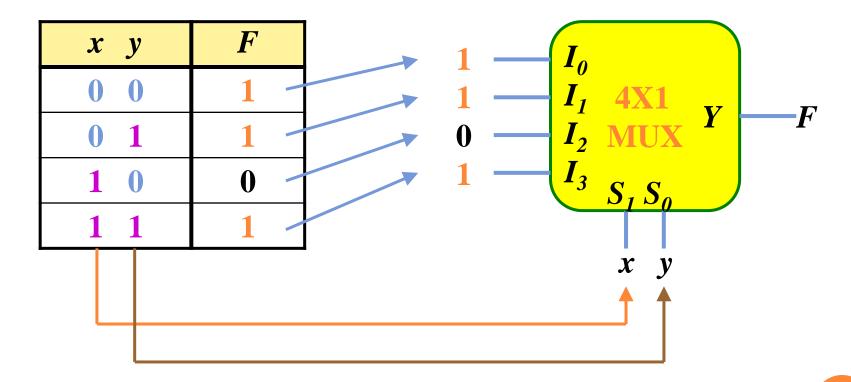


Procedure 2

• Steps:

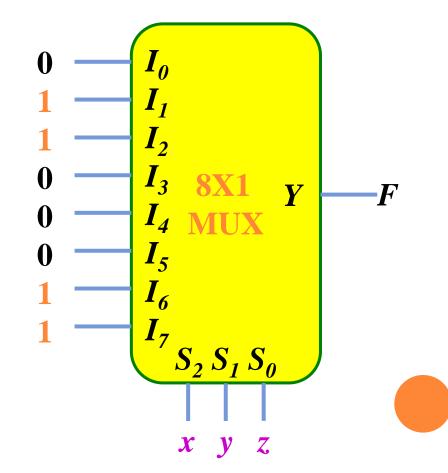
- 1. Complete the truth table from the SOP.
- 2. The first n-1 variables in the table are applied to the selection inputs of the multiplexer.
- 3. For each combination of the selection variables, we evaluate the output as a function of the last variable.
- 4. Apply these values to the data input in proper order.

• Example $F(x, y) = \sum_{x \in S} (0, 1, 3)$



• Example $F(x, y, z) = \sum (1, 2, 6, 7)$

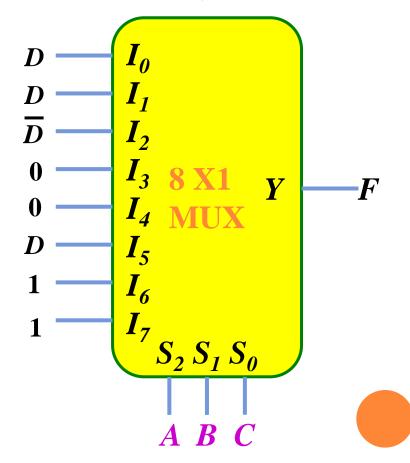
x	y	Z	$\boldsymbol{\mathit{F}}$
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1



 $F(x, y, z) = \sum (1, 2, 6, 7)$

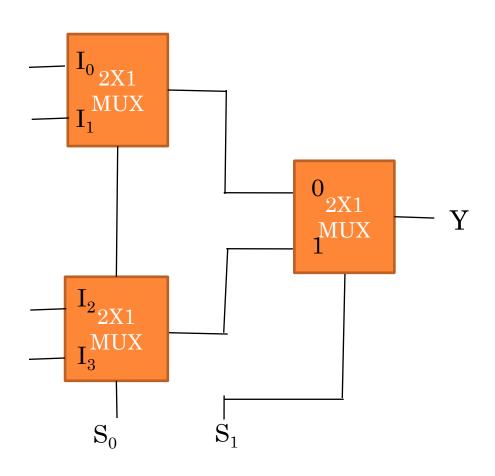
x y z	$oldsymbol{F}$		
0 0	0	$z - I_0$	
0 0 1	1		F
$\begin{bmatrix} 0 & 1 \end{bmatrix} 0$	1	$\begin{bmatrix} I_2 \end{bmatrix} = \begin{bmatrix} I_2 \end{bmatrix} \begin{bmatrix} $	
0 1 1	0	$F = \overline{z} \qquad 1 \longrightarrow I_3 \qquad I_{S_1 S_0}$	
1 0 0	0	$\mathbf{E} = 0$	
1 0 1	0	F = 0	
1 1 0	1	F = z + z' = 1	
1 1 1	1		

A B C D	F	
$\begin{bmatrix} 0 & 0 & 0 \end{bmatrix} 0$	0	
$\begin{bmatrix} 0 & 0 & 0 & 1 \end{bmatrix}$	1	$\int F = D$
$\begin{bmatrix} 0 & 0 & 1 \end{bmatrix}$	0	F = D
$\begin{bmatrix} 0 & 0 & 1 \end{bmatrix}$ 1	1	$\Gamma = D$
$\begin{bmatrix} 0 & 1 & 0 \end{bmatrix}$	1	$F = \overline{D}$
$\begin{bmatrix} 0 & 1 & 0 \end{bmatrix} 1$	0	F = D
0 1 1 0	0	\mathbf{L}_{E-0}
0 1 1 1	0	F = 0
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	0	7 7 0
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	0	$\int F = 0$
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	0	
$\begin{bmatrix} 1 & 0 & 1 \end{bmatrix}$	1	F = D
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	1	F = 1
1 1 0 1	1	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	1	F=1
1 1 1 1	1	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \

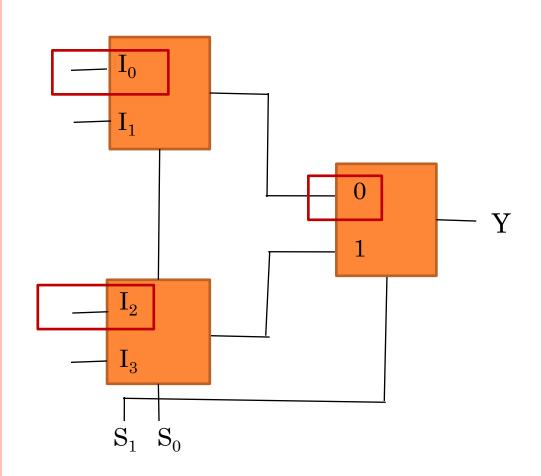


PROCEDURE 1 VS PROCEDURE 2

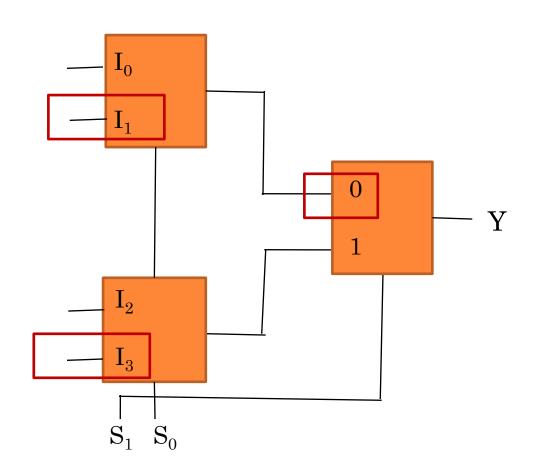
• Among the function variables, if the first or some middle variable other than the last one is to be used in input line then procedure 1 is preferable.



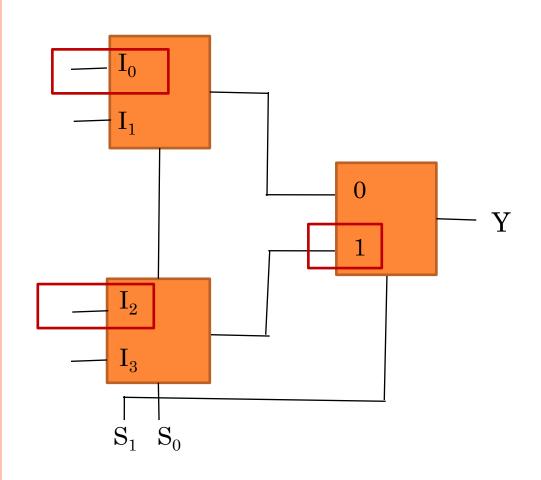
S_1	S_0	Y
0	0	I_0
0	1	I_1
1	0	I_2
1	1	I_3



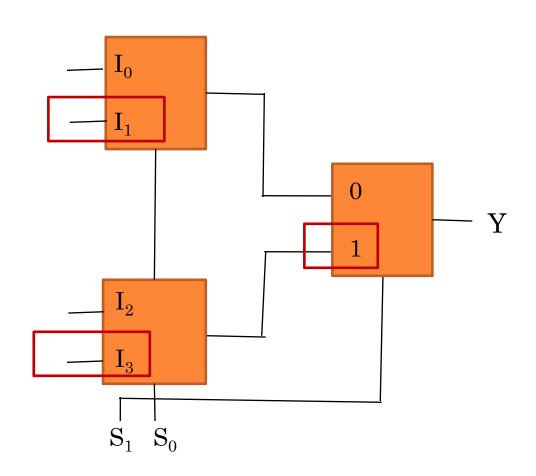
S_1	S_0	Y
0	0	I_0
0	1	I_1
1	0	I_2
1	1	I_3



\mathbf{S}_1	S_0	Y
0	0	I_0
0	1	I_1
1	0	I_2
1	1	I_3

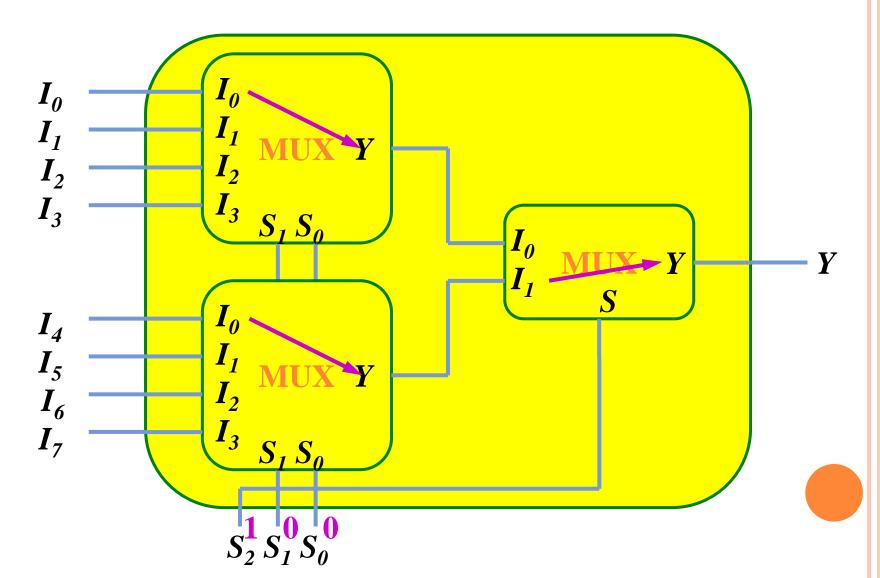


S_1	S_0	Y
0	0	I_0
0	1	I_1
1	0	I_2
1	1	I_3

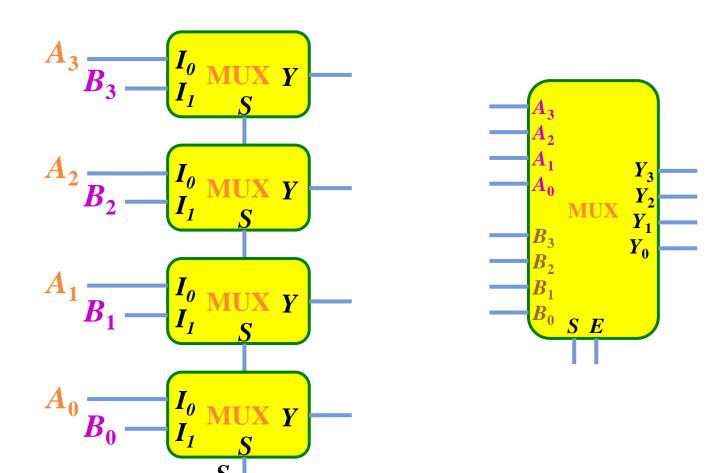


S_1	S_0	Y
0	0	I_0
0	1	I_1
1	0	${ m I}_2$
1	1	I_3

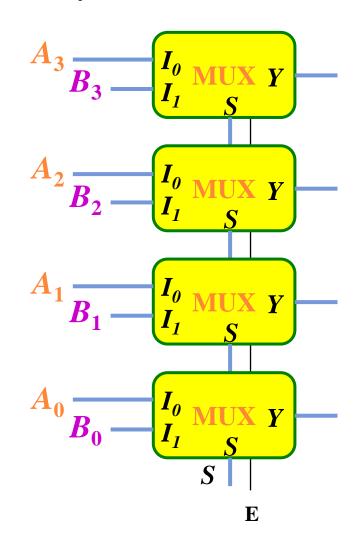
MULTIPLEXER EXPANSION [SELF STUDY] 8-TO-1 MUX USING DUAL 4-TO-1 MUX

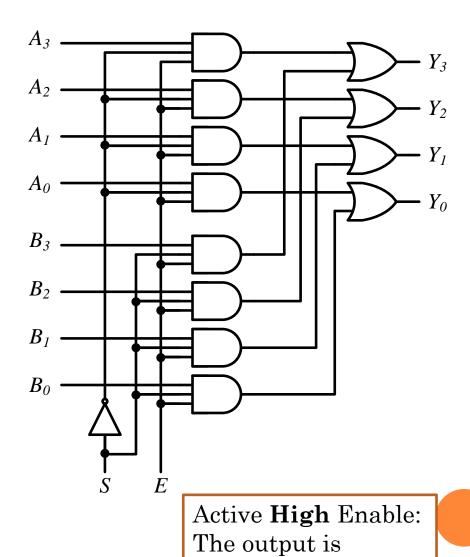


• Quad 2-to-1 MUX: Four 2x1 MUX can be used simultaneously



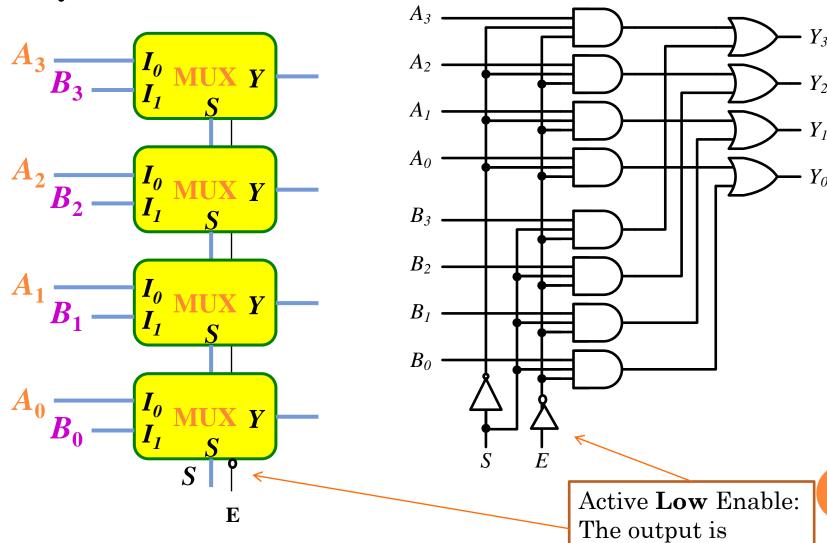
• Quad 2-to-1 MUX





enabled when E=1

o Quad 2-to-1 MUX



enabled when E=0