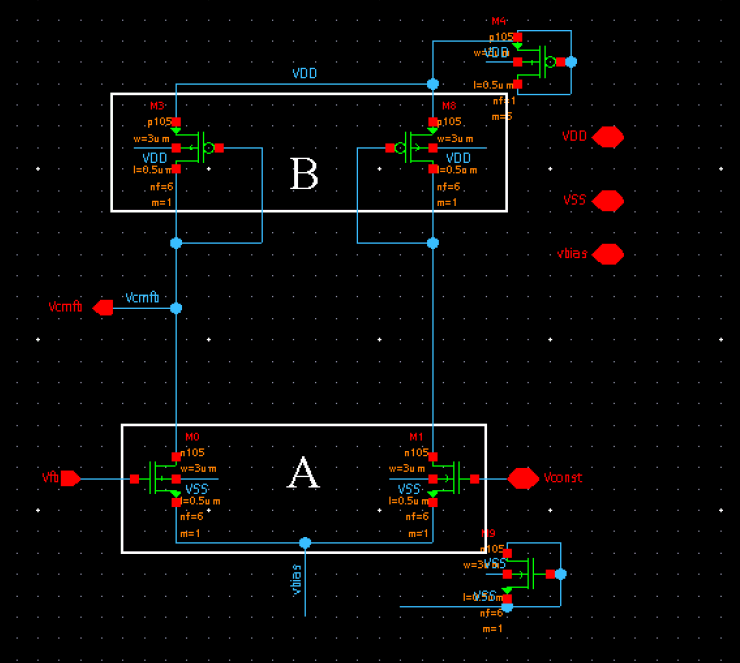
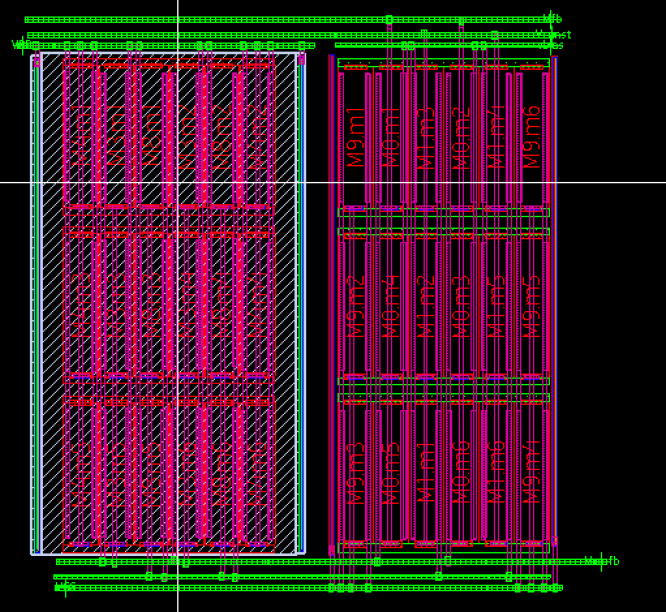
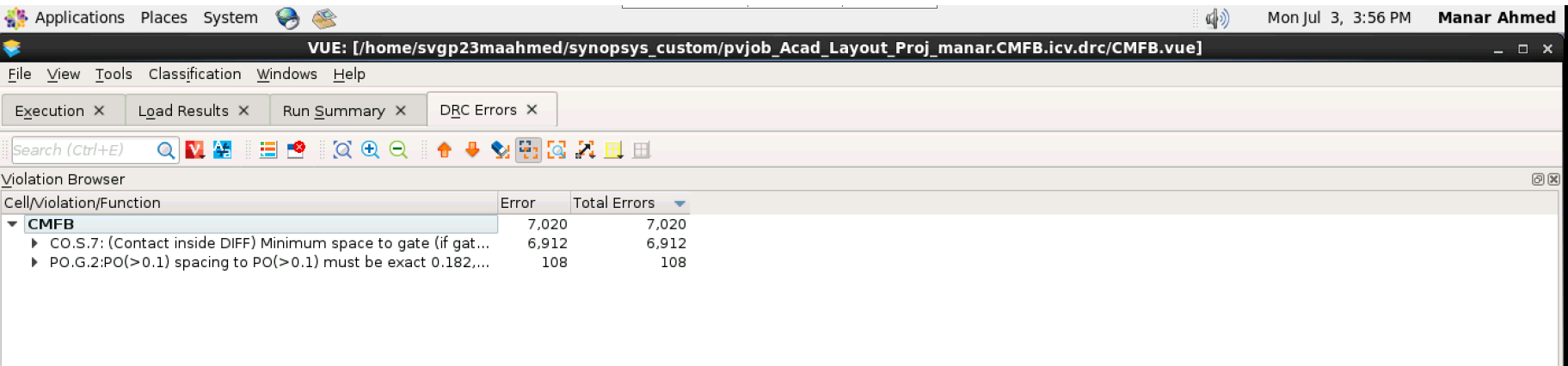
CMFB schematic



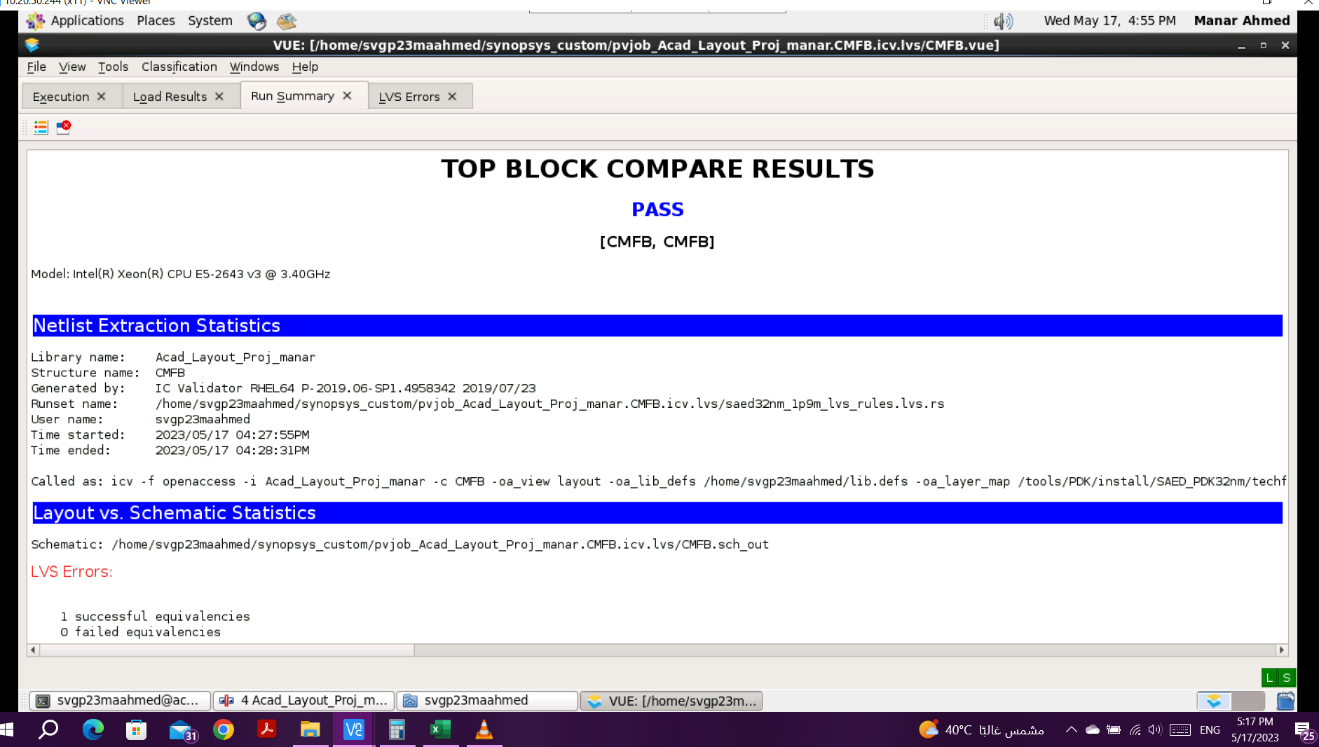
**CMFB layout**



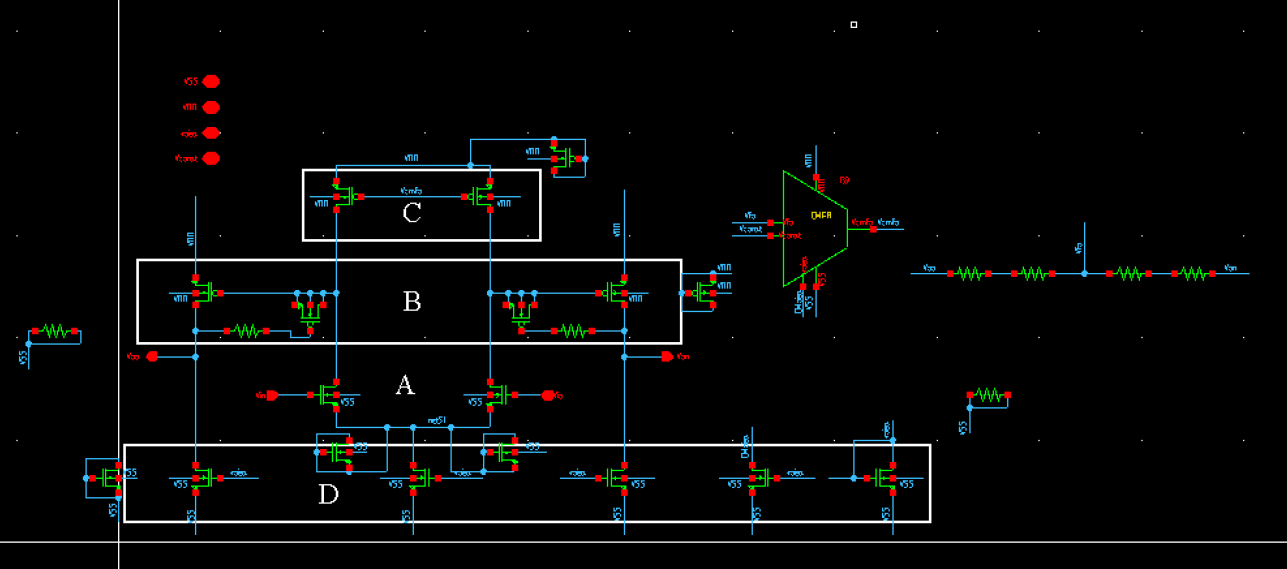
**CMFB DRC**



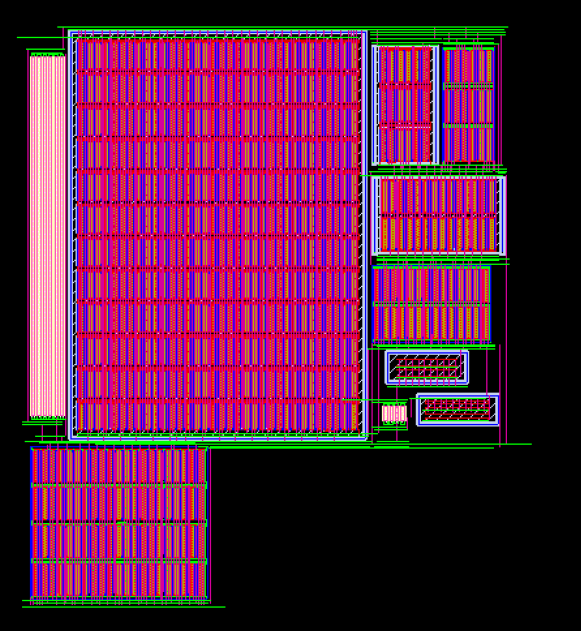
**CMFB LVS**



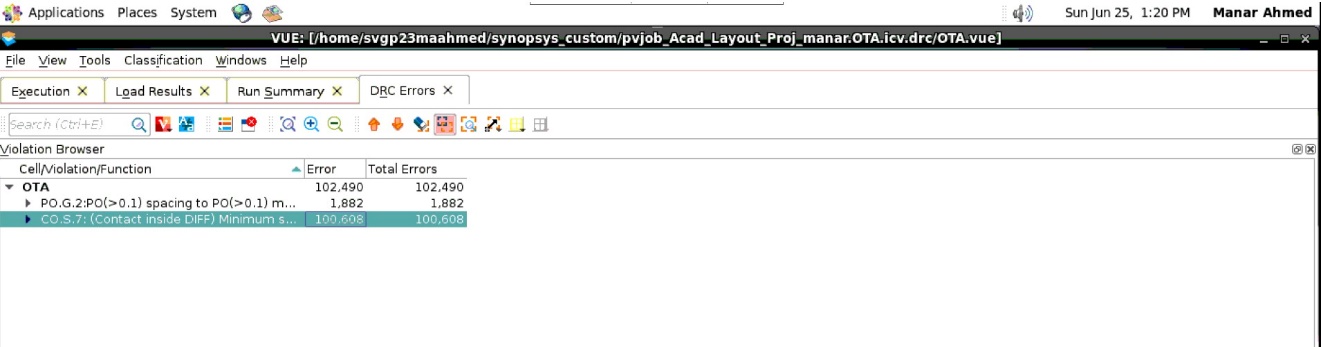
**OTA schematic**



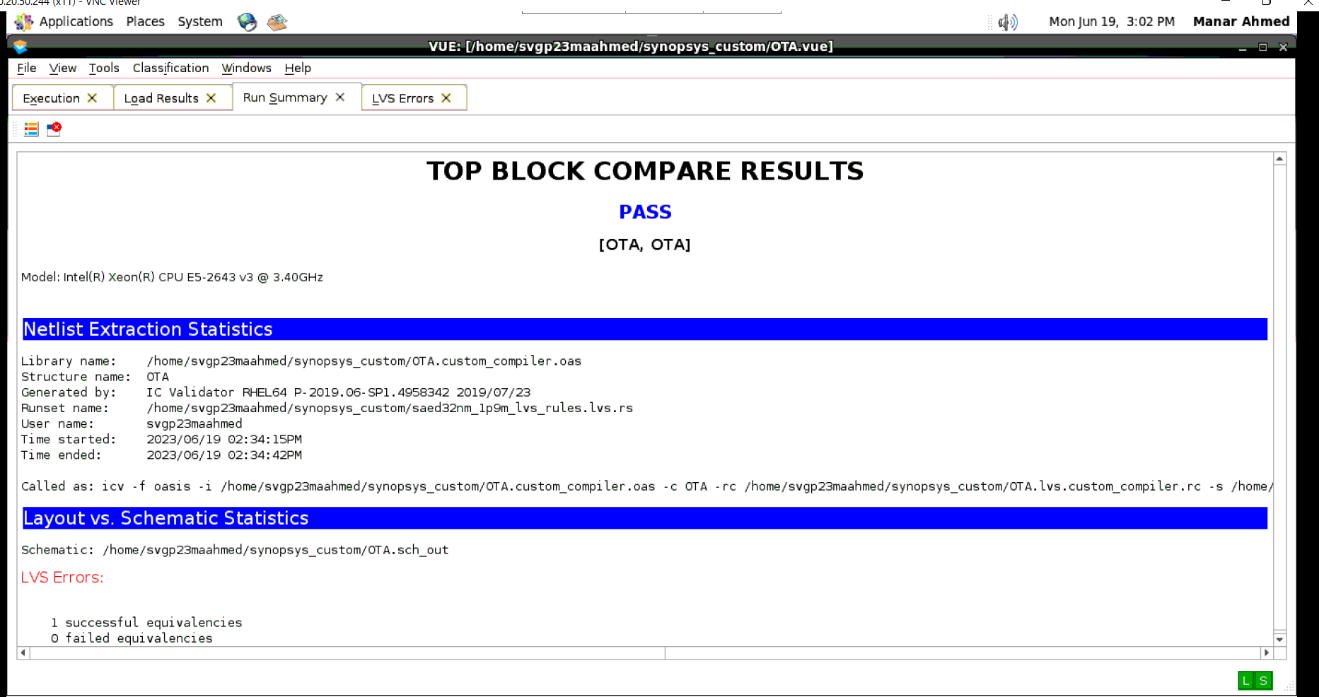
**OTA layout**



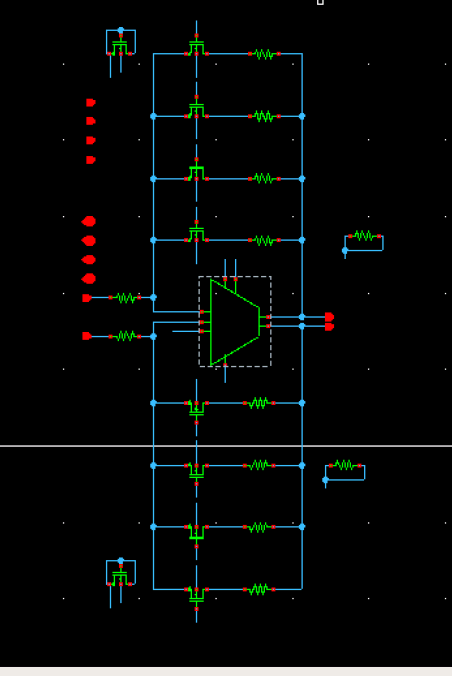
**OTA DRC**



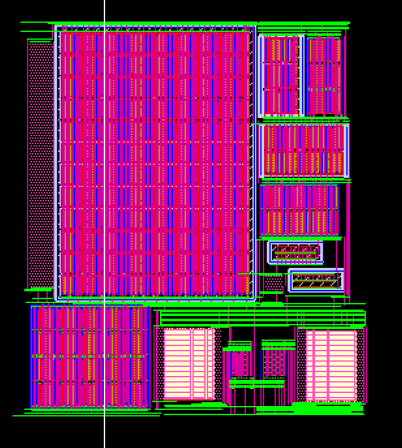
**OTA LVS**



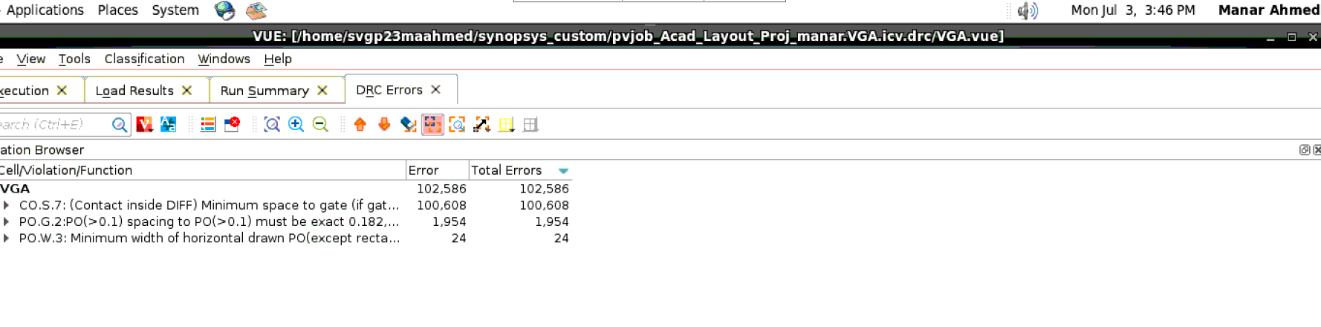
**VGA schematic**



**VGA layout**



**VGA DRC**



**VGA LVS**

