

dsPIC33FJ128MC706A pins						
Dec. 14, 2012						
ImageProc 2.5c						
<b>Pin number</b>	<b>dsPIC Functions</b>	<b>ImageProc Function</b>		<b>Pin number</b>	<b>dsPIC Functions</b>	<b>ImageProc Function</b>
1	PWM3H/RE5	Motor C IN1		33	U1TX/ <b>SDO1</b> /RF3	SPI1 radio
2	PWM4L/RE6	Motor D IN2		34	U1RC/ <b>SDI1</b> /RF2	SPI1 radio
3	PWM4H/RE7	Motor D IN1		35	U1TS*/ <b>SCK1</b> /INT0/RF6	SPI1 radio
4	SCK2/CN8/RG6	SPI2 FLASH+gyro		36	SDA1/RG3	I2C1 camera, JP3-5
5	SDI2/CN9/RG7	SPI2 FLASH+gyro		37	SCL1/RG2	I2C1 camera, JP3-3
6	SDO2/CN10/RG8	SPI2 FLASH+gyro		38	Vdd	
7	MCLR*	JP1-5 (ICD)		39	OSC1/CLKIN/RC12	camera xclk
8	SS2*/CN11/RG9	SPI2 FLASH (SS2_1)		40	OSC2/CLKO/RC15	gyro SS2_2
9	Vss			41	Vss	
10	Vdd			42	IC1/FLTA*/INT1/RD8	gyro INT
11	AN5/QEB/IC8/CN7/RB5	PAD4		43	IC2/U1CTS*/FLT B*/INT2/RD9	low volt detect
12	AN4/QEA/IC7/CN6/RB4	PAD3		44	IC3/INT3/RD10	PAD5
13	AN3/INDX/CN5/RB3	PAD2		45	IC4/INT4/RD11	radio IRQ
14	AN2/ <b>SS1</b> */CN4/RB2	SPI1 radio		46	OC1/RD0	camera D0
15	PGEC3/AN1/Vref-/CN3/RB1	PAD1		47	PGED2/SOSCI/T4CK/CN1/RC13	camera Vsync
16	PGED3/ <b>AN0</b> /Vref+/CN2/RB0	Batt Level		48	PGEC2/SOSCO/T1CK/CN0/RC14	camera PWDN
17	PGEC1/AN6/OCFA/RB6	JP1-1 (ICD)		49	OC2/RD1	camera D1
18	PGED1/AN7/RB7	JP1-2 (ICD)		50	OC3/RD2	camera D2
19	Avdd			51	OC4/RD3	camera D3
20	Avss			52	OC5/IC5/CN13/RD4	camera D4
21	U2CTS*/AN8/RB8	MotorA BEMF		53	OC6/IC6/CN14/RD5	camera D5
22	AN9/RB9	Motor B BEMF		54	OC7/CN15/RD6	camera D6
23	TMS/AN10/RB10	Motor C BEMF		55	OC8/UPDN/CN16/RD7	camera D7
24	TDO/AN11/RB11	Motor D BEMF		56	Vacp/Vddcore	
25	Vss			57	Vdd	
26	Vdd			58	C1RX/RF0	camera HREF
27	TCK/AN1/RB12	LED1		59	C1TX/RF1	camera PCLK
28	TDI/AN13/RB13	LED2		60	PWM1L/RE0	Motor A IN2
29	U2RTS*/AN14/RB14	LED3		61	PWM1H/RE1	Motor A IN1
30	AN15/OCFB/CN12/RB15	Radio SLP_TR		62	PWM2L/RE2	Motor B IN2
31	U2RX/SDA2/CN17/RF4	JP2-6 U2RX		63	PWM2H/RE3	Motor B IN1
32	U2TX/SCL2/CN18/RF5	JP2-4 U2TX		64	PWM3L/RE4	Motor C IN2