**BUG 1:**

**Source Day 1/Two\_Wire\_SPI**

A screenshot of a social media post

Description automatically generatedA screen shot of a video game

Description automatically generated

Looking at pictures of bug 1 when the bottom half of getting errors generating bitstream error is “[Synth 8-27] else clause after check for clock not supported”

A screen shot of a video game

Description automatically generated

A screenshot of a social media post

Description automatically generated

The code was fixed by removing the elseif clause and replacing it with an if. This didn’t break simulation and can now generate bitstream 😊

**BUG 2:**

**Source Day 1/LED Control**

A screenshot of a social media post

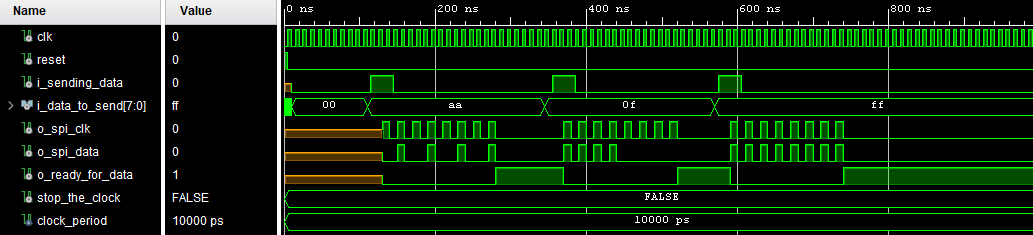
Description automatically generated

This code is suppose to use the two\_wire\_spi source to send some pixel data. It should be sending “00000000” for the first 4 transmissions but this doesn’t work. The outputs are just U I suspect this is due to the actual two\_wire\_spi code. You can see here we are waiting for ready\_for\_data to be high before sending anything. I think that the code is not initializing the o\_ready\_for\_data from the two\_wire\_spi source to be 1 initially. The testbench is shown below for led\_control.

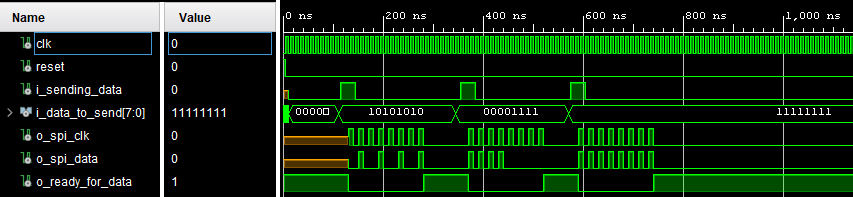
A screenshot of a computer

Description automatically generated

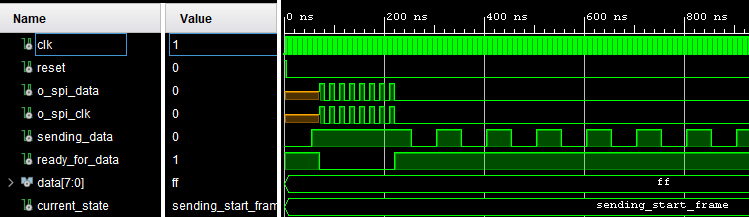
Below is the testbench of the two\_wire\_spi source which verifies my suspicions.



Below is the test bench for two\_wire\_spi fixed



Retesting the led\_control reveals a successful testbench now I just need to complete the code



**BUG 3:**

**Source Day 1/Two Wire Spi**

I decided I needed another output on the spi source to