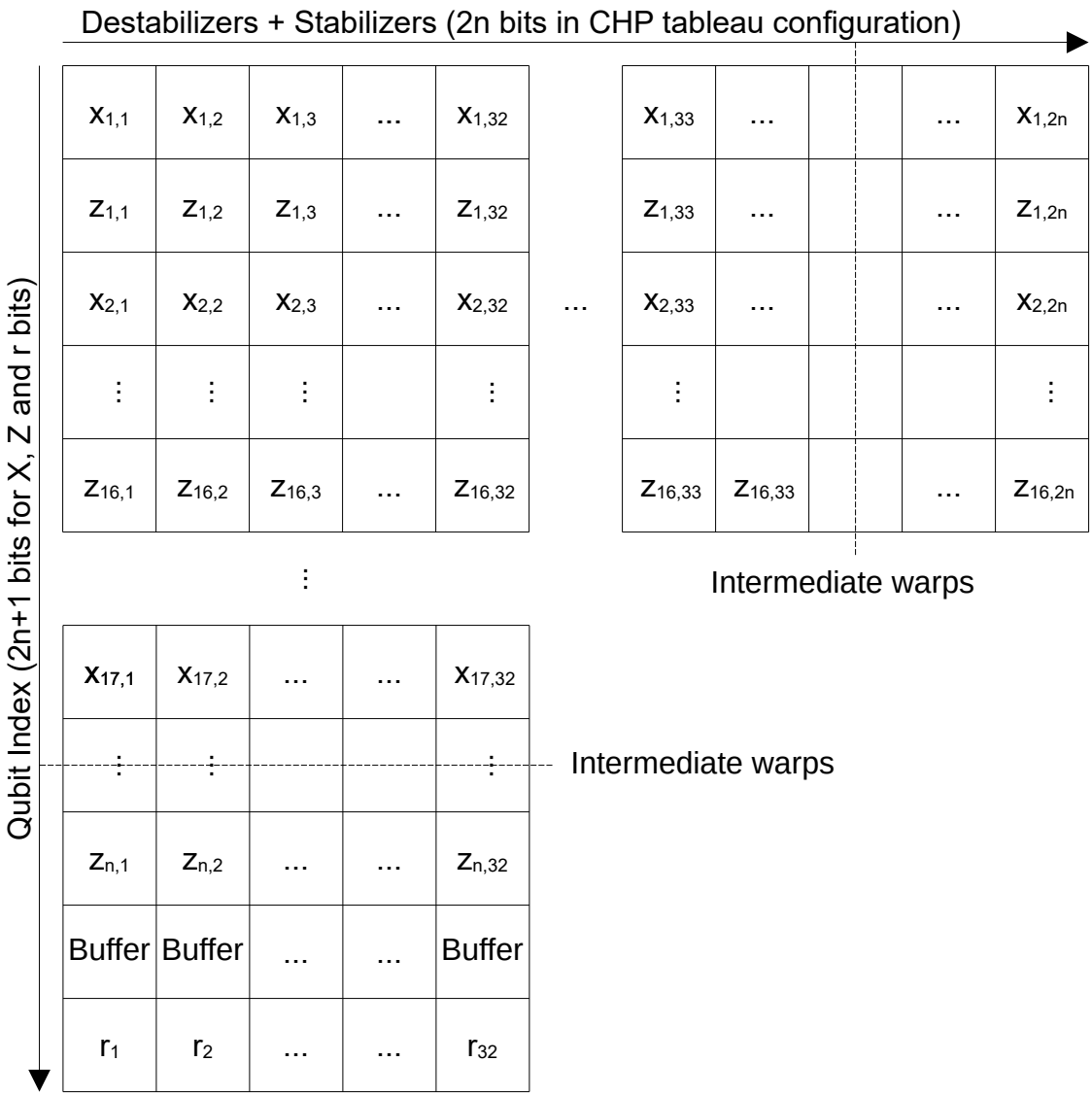
Ex: $x_{n,m}$ is the x bit at qubit n to generate stabilizer (Pauli string) m. Common single qubit operations require bit operations along on every bit for a given qubit. $4n^2+n$ bits / 1024 bits per warp = size of warp.



Bit-wise operations are intra-row (within lanes) meaning every bit in a lane receives the same operation. Every stabilizer can be edited simultaneously in single qubit operations. Single qubit gates on different qubits can be done simultaneously since they use separate lanes.

Most common operations are bit-wise between X and Z so warps are organized to minimize data access outside the warp.