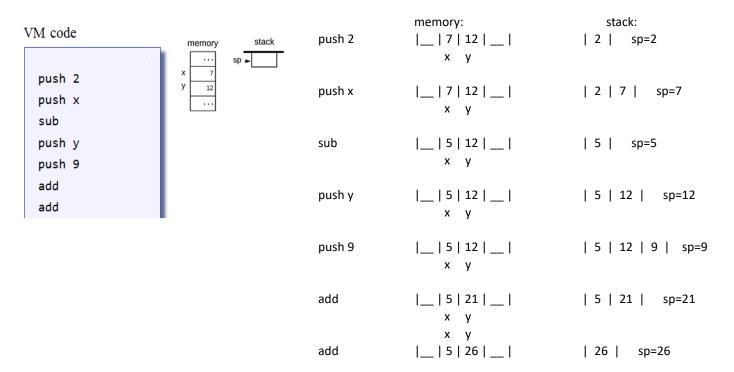
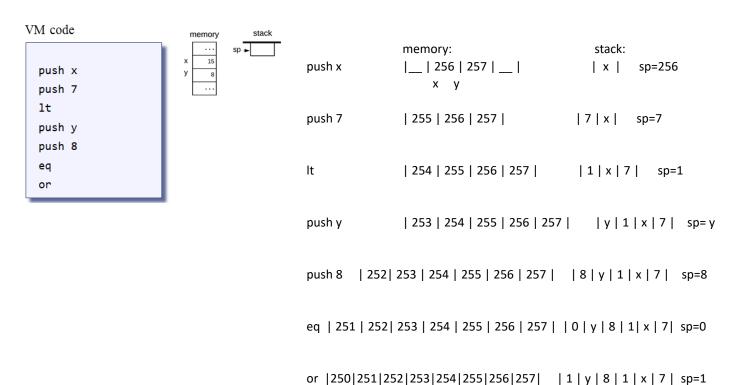
Name:	Alexandra	Burke
Date:		

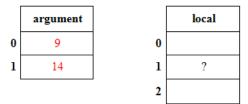
1. Stack Arithmetic Commands: What the state of the stack and the memory after the following VM code is executed. Where will the stack pointer (sp) end up, if it originally begins at address 256? Please illustrate the stack after every VM command has been executed.



2. Stack Logical Commands: Again, what the state of the stack and memory after the following VM code is executed. Where will the stack pointer (sp) end up, if it originally begins at address 256? Please illustrate the stack after every VM command has been executed.



3. Suppose the state of the argument and local memory segments are as follows:



Now consider the following VM code:

```
push constant 0
      pop local 0
2
3
      push constant 15
      pop local 1
5
      push local 1
6
      push argument 1
      pop local 2
8
9
      push local 0
10
      push argument 0
11
      add
      pop local 0
12
13
      push local 1
14
      push local 1
15
      push constant 1
16
      sub
17
       pop local 1
18
```

What will be the value of local 1 after the VM code has executed?_____29____

4. Suppose the state of the RAM is as follows and the adjacent assembly code will execute:

	RAM
0	3
1	2
2	0
3	6
4	5
5	1
6	4

```
1 2 A = M

3 A = M

4 A = M

5 D = M

6 @4

7 M = D
```

5. Suppose the state of the RAM is as follows and the adjacent assembly code will execute:

	RAM
0	3
1	2
2	0
3	6
4	5
5	1
6	4

1	0.5
2	A=M
3	A=A+1
4	A=A+1
5	D=M
6	A=A+1
7	M=D

What will be the value of the RAM[4] following the assembly code execution?____0__

6. Suppose the state of the RAM is as follows and the adjacent pseudocode (like C++) will execute:

	RAM
256	22
257	31
258	200
259	28

```
1 p1 = 256

2 p1 = p1 + 3

3 *p1 = *p1 + 3

4 p2 = p1 - 2

5 p1--

6 *(p2 + 1) = *p1 + *p2
```

What will be the value of the RAM[258] following the assembly code execution?_____231_____

Translate the following VM commands to <u>Assembly</u> instructions:

push constant 1
@1
D=A
@SP
A=M
M=D
@SP
M=M+1
push constant 5
@5
D=A
@SP
A=M
M=D
@SP
M=M+1
□ add
@SP
M=M-1
A=M
D=M @SP
(@SP M=M-1
A=M
M=M+D
@SP
M=M+1

pop static 7 //suppose inside of a file named Add

```
@SP
M=M-1
A=M
D=M
@Add.7
M=D
```

pop local 2

```
// pop local 2

@SP

M=M-1

A=M

D=M

@LCL // Load the base address of the local segment (LCL) into the A-register

D=M+2 // Add 2 to the base address to get the target memory address

M=D
```

□ eq

```
// SP points to the next available location on the stack
@SP
M=M-1
A=M
D=M
@SP
M=M-1
A=M
D=M-D
@EQUAL_LABEL
D; JEQ // Jump to the EQUAL_LABEL if D is equal to 0 (x == y)
D=0
@END LABEL
0;JMP // Unconditional jump to the END_LABEL
(EQUAL_LABEL)
D=-1
// Push the result (D) onto the stack
@SP
A=M
M=D
@SP
M=M+1
(END_LABEL)
```