Homework #1

CS220 - Computer Architecture + Machine Language

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Section: __1319__

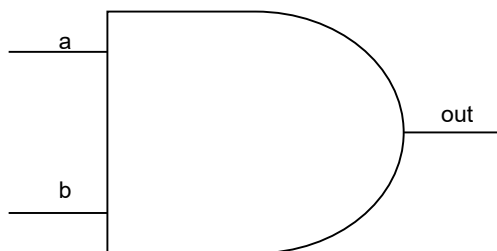Last Modified: ___6/18/2023___

NOT
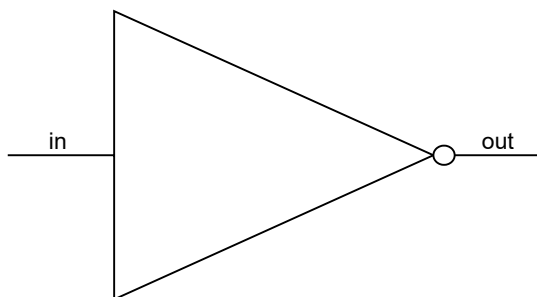
in	out
0	1
1	0

NOT: input is opposite of output

AND

a	b	out
0	0	0
0	1	0
1	0	0
1	1	1

AND: only time output is positive is if all inputs are positive

OR
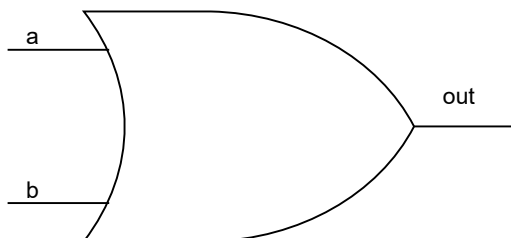
a	b	out
0	0	0
0	1	1
1	0	1
1	1	1

OR: output will be positive when any one or all inputs are positive

XOR
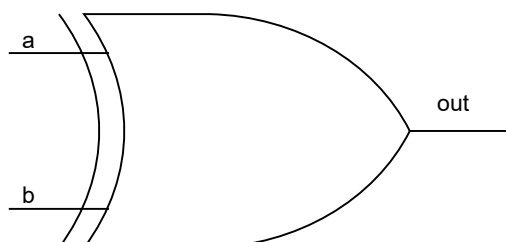
a	b	out
0	0	0
0	1	1
1	0	1
1	1	0

XOR: only positive when inputs not the same