



**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping**

| Port   |      | AF0  | AF1                | AF2       | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                             | AF10  | AF11                            | AF12                           | AF13         | AF14      | AF15      |
|--------|------|------|--------------------|-----------|--------------------------------|-----------------------|--|--|--|---|---------------------------------|---|---------------------------------|--------------------------------|--------------|-----------|-----------|
|        |      | SYS  | I2C4/UART5/TIM1/2  | TIM3/4/5  | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI6/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/I2C4/UART4/DFSDM1 | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMCD | SAI2/QUADSPI/DMMCM2/D FSDM1/OTG2_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH            | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD       | SYS       |
| Port A | PA0  | -    | TIM2_C H1/TIM2_ETR | TIM5_C H1 | TIM8_ETR                       | -                     | -  | -  | USART2_CTS                                 | UART4_TX                                  | -                               | SAI2_SD_B                                       | ETH_MII_CRS                     | -                              | -            | -         | EVEN TOUT |
|        | PA1  | -    | TIM2_C H2          | TIM5_C H2 | -                              | -                     | -  | -  | USART2_RTS                                 | UART4_RX                                  | QUADSPI_BK1_IO3                 | SAI2_MCK_B                                      | ETH_MII_RX_CLK/ETH_RMII_REF_CLK | -                              | -            | LCD_R2    | EVEN TOUT |
|        | PA2  | -    | TIM2_C H3          | TIM5_C H3 | TIM9_CH1                       | -                     | -  | -  | USART2_TX                                  | SAI2_SCK_B                                | -                               | -   | ETH_MDI_O                       | MDIOS_MDIO                     | -            | LCD_R1    | EVEN TOUT |
|        | PA3  | -    | TIM2_C H4          | TIM5_C H4 | TIM9_CH2                       | -                     | -  | -  | USART2_RX                                  | -   | LCD_B2                          | OTG_HS_ULPI_D0                                  | ETH_MII_COL                     | -                              | -            | LCD_B5    | EVEN TOUT |
|        | PA4  | -    | -                  | -         | -                              | -                     | SPI1_NSS/I2S1_WS                                     | SPI3_NSS/I2S3_WS                           | USART2_CK                                  | SPI6_NSS                                  | -                               | -   | -                               | OTG_HS_SOF                     | DCMI_H_SYNC  | LCD_VSYNC | EVEN TOUT |
|        | PA5  | -    | TIM2_C H1/TIM2_ETR | -         | TIM8_CH1N                      | -                     | SPI1_SCK/I2S1_CK                                     | -  | -  | SPI6_SCK                                  | -                               | OTG_HS_ULPI_CK                                  | -                               | -                              | -            | LCD_R4    | EVEN TOUT |
|        | PA6  | -    | TIM1_BKIN          | TIM3_C H1 | TIM8_BKIN                      | -                     | SPI1_MISO  | -  | -  | SPI6_MISO                                 | TIM13_C H1                      | -   | -                               | MDIOS_MDC                      | DCMI_PIXCLK  | LCD_G2    | EVEN TOUT |
|        | PA7  | -    | TIM1_C H1N         | TIM3_C H2 | TIM8_CH1N                      | -                     | SPI1_MOSI/I2S1_SD                                    | -  | -  | SPI6_MOSI                                 | TIM14_C H1                      | -   | ETH_MII_RX_DV/ETH_RMII_CRS_DV   | FMC_SD_NWE                     | -            | -         | EVEN TOUT |
|        | PA8  | MCO1 | TIM1_C H1          | -         | TIM8_BKIN2                     | I2C3_SCL              | -  | -  | USART1_CK                                  | -   | -                               | OTG_FS_SOF                                      | CAN3_RX                         | UART7_RX                       | LCD_B3       | LCD_R6    | EVEN TOUT |
|        | PA9  | -    | TIM1_C H2          | -         | -                              | I2C3_SMBA             | SPI2_SCK/I2S2_CK                                     | -  | USART1_TX                                  | -   | -                               | -   | -                               | -                              | DCMI_D0      | LCD_R5    | EVEN TOUT |
|        | PA10 | -    | TIM1_C H3          | -         | -                              | -                     | -  | -  | USART1_RX                                  | -   | LCD_B4                          | OTG_FS_ID                                       | -                               | MDIOS_MDIO                     | DCMI_D1      | LCD_B1    | EVEN TOUT |

**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0           | AF1                | AF2       | AF3                            | AF4                   | AF5                                    | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14   | AF15      |
|--------|------|---------------|--------------------|-----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS           | I2C4/UART5/TIM1/2  | TIM3/4/5  | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI4/5/6 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port A | PA11 | -             | TIM1_C H4          | -         | -                              | -                     | SPI2_NS S/I2S2_WS                      | UART4_RX                                   | USART1_CTS   | -   | CAN1_RX                            | OTG_FS_DM                                     | -                    | -                              | -            | LCD_R4 | EVEN TOUT |
|        | PA12 | -             | TIM1_ETR           | -         | -                              | -                     | SPI2_SCK/I2S2_CK                       | UART4_TX                                   | USART1_RTS   | SAI2_FS_B                                 | CAN1_TX                            | OTG_FS_DP                                     | -                    | -                              | -            | LCD_R5 | EVEN TOUT |
|        | PA13 | JTMS-SWDIO    | -                  | -         | -                              | -                     | -                                      | -  | -  | -   | -                                  | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PA14 | JTCK-SWCLK    | -                  | -         | -                              | -                     | -                                      | -  | -  | -   | -                                  | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PA15 | JTDI          | TIM2_C H1/TIM2_ETR | -         | -                              | HDMI-CEC              | SPI1_NS S/I2S1_WS                      | SPI3_NS S/I2S3_WS                          | SPI6_NS S  | UART4_RTS                                 | -                                  | -   | CAN3_TX              | UART7_TX                       | -            | -      | EVEN TOUT |
| Port B | PB0  | -             | TIM1_C H2N         | TIM3_C H3 | TIM8_CH 2N                     | -                     | -                                      | DFSDM1_CKOUT                               | -  | UART4_CTS                                 | LCD_R3                             | OTG_HS_ULPI_D1                                | ETH_MII_RXD2         | -                              | -            | LCD_G1 | EVEN TOUT |
|        | PB1  | -             | TIM1_C H3N         | TIM3_C H4 | TIM8_CH 3N                     | -                     | -                                      | DFSDM1_DATIN1                              | -  | -   | LCD_R6                             | OTG_HS_ULPI_D2                                | ETH_MII_RXD3         | -                              | -            | LCD_G0 | EVEN TOUT |
|        | PB2  | -             | -                  | -         | -                              | -                     | -                                      | SAI1_SD_A                                  | SPI3_MOSI/I2S3_SD                                      | -   | QUADSPI_CLK                        | DFSDM1_CKIN1                                  | -                    | -                              | -            | -      | EVEN TOUT |
|        | PB3  | JTDO/TRACESWO | TIM2_C H2          | -         | -                              | -                     | SPI1_SCK/I2S1_CK                       | SPI3_SCK/I2S3_CK                           | -  | SPI6_SCK                                  | -                                  | SDMMC2_D2                                     | CAN3_RX              | UART7_RX                       | -            | -      | EVEN TOUT |
|        | PB4  | NJTRST        | -                  | TIM3_C H1 | -                              | -                     | SPI1_MISO                              | SPI3_MISO                                  | SPI2_NS S/I2S2_WS                                      | SPI6_MISO                                 | -                                  | SDMMC2_D3                                     | CAN3_TX              | UART7_TX                       | -            | -      | EVEN TOUT |
|        | PB5  | -             | UART5_RX           | TIM3_C H2 | -                              | I2C1_SMB              | SPI1_MOSI/I2S1_SD                      | SPI3_MOSI/I2S3_SD                          | -  | SPI6_MOSI                                 | CAN2_RX                            | OTG_HS_ULPI_D7                                | ETH_PPS_OUT          | FMC_SD_CKE1                    | DCMI_D10     | LCD_G7 | EVEN TOUT |
|        | PB6  | -             | UART5_TX           | TIM4_C H1 | HDMI-CEC                       | I2C1_SCL              | -                                      | DFSDM1_DATIN5                              | USART1_TX  | -   | CAN2_TX                            | QUADSPI_BK1_NCS                               | I2C4_SCL             | FMC_SD_NE1                     | DCMI_D5      | -      | EVEN TOUT |



**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0       | AF1               | AF2       | AF3                            | AF4                   | AF5                                    | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                         | AF12                           | AF13         | AF14   | AF15      |
|--------|------|-----------|-------------------|-----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|------------------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS       | I2C4/UART5/TIM1/2 | TIM3/4/5  | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI4/5/6 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH         | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port B | PB7  | -         | -                 | TIM4_C H2 | -                              | I2C1_SD A             | -                                      | DFSDM1_CKIN5                               | USART1_RX  | -   | -                                  | -   | I2S4_SD A                    | FMC_NL                         | DCMI_V SYNC  | -      | EVEN TOUT |
|        | PB8  | -         | I2C4_SCL          | TIM4_C H3 | TIM10_C H1                     | I2C1_SCL              | -                                      | DFSDM1_CKIN7                               | UART5_RX   | -   | CAN1_RX                            | SDMMC2_D4                                     | ETH_MII_TXD3                 | SDMMC_D4                       | DCMI_D6      | LCD_B6 | EVEN TOUT |
|        | PB9  | -         | I2C4_SDA          | TIM4_C H4 | TIM11_CH1                      | I2C1_SDA              | SPI2_NSS/I2S2_WS                       | DFSDM1_DATIN7                              | UART5_TX   | -   | CAN1_TX                            | SDMMC2_D5                                     | I2C4_SMB A                   | SDMMC_D5                       | DCMI_D7      | LCD_B7 | EVEN TOUT |
|        | PB10 | -         | TIM2_C H3         | -         | -                              | I2C2_SCL              | SPI2_SCK/I2S2_CK                       | DFSDM1_DATIN7                              | USART3_TX  | -   | -                                  | QUADSPI_BK1_NCS                               | OTG_HS_ULPI_D3               | ETH_MII_RX_ER                  | -            | LCD_G4 | EVEN TOUT |
|        | PB11 | -         | TIM2_C H4         | -         | -                              | I2C2_SDA              | -                                      | DFSDM1_CKIN7                               | USART3_RX  | -   | -                                  | OTG_HS_ULPI_D4                                | ETH_MII_TX_EN/ETH_RMII_TX_EN | -                              | DSI_TE       | LCD_G5 | EVEN TOUT |
|        | PB12 | -         | TIM1_BKIN         | -         | -                              | I2C2_SMB A            | SPI2_NSS/I2S2_WS                       | DFSDM1_DATIN1                              | USART3_CK  | UART5_RX                                  | CAN2_RX                            | OTG_HS_ULPI_D5                                | ETH_MII_TXD0/ETH_RMII_TXD0   | OTG_HS_ID                      | -            | -      | EVEN TOUT |
|        | PB13 | -         | TIM1_C H1N        | -         | -                              | -                     | SPI2_SCK/I2S2_CK                       | DFSDM1_CKIN1                               | USART3_CTS   | UART5_TX                                  | CAN2_TX                            | OTG_HS_ULPI_D6                                | ETH_MII_TXD1/ETH_RMII_TXD1   | -                              | -            | -      | EVEN TOUT |
|        | PB14 | -         | TIM1_C H2N        | -         | TIM8_CH2N                      | USART1_TX             | SPI2_MISO                              | DFSDM1_DATIN2                              | USART3_RTS   | UART4_RTS                                 | TIM12_CH1                          | SDMMC2_D0                                     | -                            | OTG_HS_DM                      | -            | -      | EVEN TOUT |
|        | PB15 | RTC_REFIN | TIM1_C H3N        | -         | TIM8_CH3N                      | USART1_RX             | SPI2_MOSI/I2S2_SD                      | DFSDM1_CKIN2                               | -  | UART4_CTS                                 | TIM12_CH2                          | SDMMC2_D1                                     | -                            | OTG_HS_DP                      | -            | -      | EVEN TOUT |

**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0     | AF1               | AF2      | AF3                            | AF4                   | AF5                                    | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                       | AF12                           | AF13         | AF14      | AF15      |
|--------|------|---------|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------------|--------------------------------|--------------|-----------|-----------|
|        |      | SYS     | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI4/5/6 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH       | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD       | SYS       |
| Port C | PC0  | -       | -                 | -        | DFSDM1_CKIN0                   | -                     | -                                      | DFSDM1_DATIN4                              | -  | SAI2_FS_B                                 | -                                  | OTG_HS_ULPI_STP                               | -                          | FMC_SD_NWE                     | -            | LCD_R5    | EVEN TOUT |
|        | PC1  | TRACED0 | -                 | -        | DFSDM1_DATAIN0                 | -                     | SPI2_MOSI/I2S2_SD                      | SAI1_SD_A                                  | -  | -   | -                                  | DFSDM1_CKIN4                                  | ETH_MD_C                   | MDIOS_MDC                      | -            | -         | EVEN TOUT |
|        | PC2  | -       | -                 | -        | DFSDM1_CKIN1                   | -                     | SPI2_MISO                              | DFSDM1_CKOUT                               | -  | -   | -                                  | OTG_HS_ULPI_DIR                               | ETH_MII_TXD2               | FMC_SD_NE0                     | -            | -         | EVEN TOUT |
|        | PC3  | -       | -                 | -        | DFSDM1_DATAIN1                 | -                     | SPI2_MOSI/I2S2_SD                      | -  | -  | -   | -                                  | OTG_HS_ULPI_NXT                               | ETH_MII_TX_CLK             | FMC_SD_CKE0                    | -            | -         | EVEN TOUT |
|        | PC4  | -       | -                 | -        | DFSDM1_CKIN2                   | -                     | I2S1_MCK                               | -  | -  | SPDIF_RX2                                 | -                                  | -   | ETH_MII_RXD0/ETH_RMII_RXD0 | FMC_SD_NE0                     | -            | -         | EVEN TOUT |
|        | PC5  | -       | -                 | -        | DFSDM1_DATAIN2                 | -                     | -                                      | -  | -  | SPDIF_RX3                                 | -                                  | -   | ETH_MII_RXD1/ETH_RMII_RXD1 | FMC_SD_CKE0                    | -            | -         | EVEN TOUT |
|        | PC6  | -       | -                 | TIM3_CH1 | TIM8_CH1                       | -                     | I2S2_MCK                               | -  | DFSDM1_CKIN3   | USART6_TX                                 | FMC_NWAIT                          | SDMMC2_D6                                     | -                          | SDMMC_D6                       | DCMI_D0      | LCD_HSYNC | EVEN TOUT |
|        | PC7  | -       | -                 | TIM3_CH2 | TIM8_CH2                       | -                     | -                                      | I2S3_MCK                                   | DFSDM1_DATAIN3   | USART6_RX                                 | FMC_NE1                            | SDMMC2_D7                                     | -                          | SDMMC_D7                       | DCMI_D1      | LCD_G6    | EVEN TOUT |
|        | PC8  | TRACED1 | -                 | TIM3_CH3 | TIM8_CH3                       | -                     | -                                      | -  | UART5_RTS  | USART6_CK                                 | FMC_NE2/FMC_NCE                    | -   | -                          | SDMMC_D0                       | DCMI_D2      | -         | EVEN TOUT |
|        | PC9  | MCO2    | -                 | TIM3_CH4 | TIM8_CH4                       | I2C3_SDA              | I2S_CKIN                               | -  | UART5_CTS  | -   | QUADSPI_BK1_IO0                    | LCD_G3  | -                          | SDMMC_D1                       | DCMI_D3      | LCD_B2    | EVEN TOUT |
|        | PC10 | -       | -                 | -        | DFSDM1_CKIN5                   | -                     | -                                      | SPI3_SCK/I2S3_CK                           | USART3_TX  | UART4_TX                                  | QUADSPI_BK1_IO1                    | -   | -                          | SDMMC_D2                       | DCMI_D8      | LCD_R2    | EVEN TOUT |



**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0     | AF1               | AF2      | AF3                            | AF4                   | AF5                                    | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14   | AF15      |
|--------|------|---------|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS     | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI4/5/6 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port C | PC11 | -       | -                 | -        | DFSDM1_DATAIN5                 | -                     | -                                      | SPI3_MISO                                  | USART3_RX  | UART4_RX                                  | QUADSPI_BK2_NCS                    | -   | -                    | SDMMC_D3                       | DCMI_D4      | -      | EVEN TOUT |
|        | PC12 | TRACED3 | -                 | -        | -                              | -                     | -                                      | SPI3_MOSI/I2S3_SD                          | USART3_CK  | UART5_TX                                  | -                                  | -   | -                    | SDMMC_CK                       | DCMI_D9      | -      | EVEN TOUT |
|        | PC13 | -       | -                 | -        | -                              | -                     | -                                      | -  | -  | -   | -                                  | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PC14 | -       | -                 | -        | -                              | -                     | -                                      | -  | -  | -   | -                                  | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PC15 | -       | -                 | -        | -                              | -                     | -                                      | -  | -  | -   | -                                  | -   | -                    | -                              | -            | -      | EVEN TOUT |
| Port D | PD0  | -       | -                 | -        | DFSDM1_CKIN6                   | -                     | -                                      | DFSDM1_DATAIN7                             | -  | UART4_RX                                  | CAN1_RX                            | -   | -                    | FMC_D2                         | -            | -      | EVEN TOUT |
|        | PD1  | -       | -                 | -        | DFSDM1_DATAIN6                 | -                     | -                                      | DFSDM1_CKIN7                               | -  | UART4_TX                                  | CAN1_TX                            | -   | -                    | FMC_D3                         | -            | -      | EVEN TOUT |
|        | PD2  | TRACED2 | -                 | TIM3_ETR | -                              | -                     | -                                      | -  | -  | UART5_RX                                  | -                                  | -   | -                    | SDMMC_CMD                      | DCMI_D11     | -      | EVEN TOUT |
|        | PD3  | -       | -                 | -        | DFSDM1_CKOUT                   | -                     | SPI2_SCK/I2S2_CK                       | DFSDM1_DATAIN0                             | USART2_CTS   | -   | -                                  | -   | -                    | FMC_CLK                        | DCMI_D5      | LCD_G7 | EVEN TOUT |
|        | PD4  | -       | -                 | -        | -                              | -                     | -                                      | DFSDM1_CKIN0                               | USART2_RTS   | -   | -                                  | -   | -                    | FMC_NOE                        | -            | -      | EVEN TOUT |
|        | PD5  | -       | -                 | -        | -                              | -                     | -                                      | -  | USART2_TX  | -   | -                                  | -   | -                    | FMC_NWE                        | -            | -      | EVEN TOUT |
|        | PD6  | -       | -                 | -        | DFSDM1_CKIN4                   | -                     | SPI3_MOSI/I2S3_SD                      | SAI1_SD_A                                  | USART2_RX  | -   | -                                  | DFSDM1_DATAIN1                                | SDMMC2_CK            | FMC_NWAIT                      | DCMI_D10     | LCD_B2 | EVEN TOUT |
|        | PD7  | -       | -                 | -        | DFSDM1_DATAIN4                 | -                     | SPI1_MOSI/I2S1_SD                      | DFSDM1_CKIN1                               | USART2_CK  | SPDIF_RX0                                 | -                                  | -   | SDMMC2_CMD           | FMC_NE1                        | -            | -      | EVEN TOUT |

**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0      | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14   | AF15      |
|--------|------|----------|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS      | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI6/I2C4/UART5/DFSDM1/SPDIF | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port D | PD8  | -        | -                 | -        | DFSDM1_CKIN3                   | -                     | -  | -  | USART3_TX  | SPDIF_RX1                                 | -                                  | -   | -                    | FMC_D13                        | -            | -      | EVEN TOUT |
|        | PD9  | -        | -                 | -        | DFSDM1_DATAIN3                 | -                     | -  | -  | USART3_RX  | -   | -                                  | -   | -                    | FMC_D14                        | -            | -      | EVEN TOUT |
|        | PD10 | -        | -                 | -        | DFSDM1_CKOUT                   | -                     | -  | -  | USART3_CK  | -   | -                                  | -   | -                    | FMC_D15                        | -            | LCD_B3 | EVEN TOUT |
|        | PD11 | -        | -                 | -        | -                              | I2C4_SMBA             | -  | -  | USART3_CTS   | -   | QUADSPI_BK1_IO0                    | SAI2_SD_A                                     | -                    | FMC_A16/FMC_CLE                | -            | -      | EVEN TOUT |
|        | PD12 | -        | -                 | TIM4_CH1 | LPTIM1_IN1                     | I2C4_SCL              | -  | -  | USART3_RTS   | -   | QUADSPI_BK1_IO1                    | SAI2_FS_A                                     | -                    | FMC_A17/FMC_ALE                | -            | -      | EVEN TOUT |
|        | PD13 | -        | -                 | TIM4_CH2 | LPTIM1_OUT                     | I2C4_SDA              | -  | -  | -  | -   | QUADSPI_BK1_IO3                    | SAI2_SCK_A                                    | -                    | FMC_A18                        | -            | -      | EVEN TOUT |
|        | PD14 | -        | -                 | TIM4_CH3 | -                              | -                     | -  | -  | -  | UART8_CTS                                 | -                                  | -   | -                    | FMC_D0                         | -            | -      | EVEN TOUT |
|        | PD15 | -        | -                 | TIM4_CH4 | -                              | -                     | -  | -  | -  | UART8_RTS                                 | -                                  | -   | -                    | FMC_D1                         | -            | -      | EVEN TOUT |
| Port E | PE0  | -        | -                 | TIM4_ETR | LPTIM1_ETR                     | -                     | -  | -  | -  | UART8_Rx                                  | -                                  | SAI2_MCK_A                                    | -                    | FMC_NBL0                       | DCMI_D2      | -      | EVEN TOUT |
|        | PE1  | -        | -                 | -        | LPTIM1_IN2                     | -                     | -  | -  | -  | UART8_Tx                                  | -                                  | -   | -                    | FMC_NBL1                       | DCMI_D3      | -      | EVEN TOUT |
|        | PE2  | TRACECLK | -                 | -        | -                              | -                     | SPI4_SCK   | SAI1_MCLK_A                                | -  | -   | QUADSPI_BK1_IO2                    | -   | ETH_MII_TXD3         | FMC_A23                        | -            | -      | EVEN TOUT |
|        | PE3  | TRACED0  | -                 | -        | -                              | -                     | -  | SAI1_SDB                                   | -  | -   | -                                  | -   | -                    | FMC_A19                        | -            | -      | EVEN TOUT |



**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0     | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14    | AF15      |
|--------|------|---------|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|---------|-----------|
|        |      | SYS     | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI6/I2C4/UART5/DFSDM1/SPDIF | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART5/DFSDM1/SPDIF | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD     | SYS       |
| Port E | PE4  | TRACED1 | -                 | -        | -                              | -                     | SPI4_NS_S  | SAI1_FS_A  | -  | -   | -                                  | DFSDM1_DATAIN3                                | -                    | FMC_A20                        | DCMI_D4      | LCD_B0  | EVEN TOUT |
|        | PE5  | TRACED2 | -                 | -        | TIM9_CH1                       | -                     | SPI4_MI_SO   | SAI1_SC_K_A                                      | -  | -   | -                                  | DFSDM1_CKIN3                                  | -                    | FMC_A21                        | DCMI_D6      | LCD_G0  | EVEN TOUT |
|        | PE6  | TRACED3 | TIM1_BKIN2        | -        | TIM9_CH2                       | -                     | SPI4_M_OSI   | SAI1_SD_A  | -  | -   | -                                  | SAI2_MC_K_B                                   | -                    | FMC_A22                        | DCMI_D7      | LCD_G1  | EVEN TOUT |
|        | PE7  | -       | TIM1_ETR          | -        | -                              | -                     | -  | DFSDM1_DATAIN2                                   | -  | UART7_Rx                                  | -                                  | QUADSPI_BK2_IO0                               | -                    | FMC_D4                         | -            | -       | EVEN TOUT |
|        | PE8  | -       | TIM1_C_H1N        | -        | -                              | -                     | -  | DFSDM1_CKIN2                                     | -  | UART7_Tx                                  | -                                  | QUADSPI_BK2_IO1                               | -                    | FMC_D5                         | -            | -       | EVEN TOUT |
|        | PE9  | -       | TIM1_C_H1         | -        | -                              | -                     | -  | DFSDM1_CKOUT                                     | -  | UART7_RTS                                 | -                                  | QUADSPI_BK2_IO2                               | -                    | FMC_D6                         | -            | -       | EVEN TOUT |
|        | PE10 | -       | TIM1_C_H2N        | -        | -                              | -                     | -  | DFSDM1_DATAIN4                                   | -  | UART7_CTS                                 | -                                  | QUADSPI_BK2_IO3                               | -                    | FMC_D7                         | -            | -       | EVEN TOUT |
|        | PE11 | -       | TIM1_C_H2         | -        | -                              | -                     | SPI4_NS_S  | DFSDM1_CKIN4                                     | -  | -   | -                                  | SAI2_SD_B                                     | -                    | FMC_D8                         | -            | LCD_G3  | EVEN TOUT |
|        | PE12 | -       | TIM1_C_H3N        | -        | -                              | -                     | SPI4_SC_K  | DFSDM1_DATAIN5                                   | -  | -   | -                                  | SAI2_SC_K_B                                   | -                    | FMC_D9                         | -            | LCD_B4  | EVEN TOUT |
|        | PE13 | -       | TIM1_C_H3         | -        | -                              | -                     | SPI4_MI_SO   | DFSDM1_CKIN5                                     | -  | -   | -                                  | SAI2_FS_B                                     | -                    | FMC_D10                        | -            | LCD_DE  | EVEN TOUT |
|        | PE14 | -       | TIM1_C_H4         | -        | -                              | -                     | SPI4_M_OSI   | -  | -  | -   | -                                  | SAI2_MC_K_B                                   | -                    | FMC_D11                        | -            | LCD_CLK | EVEN TOUT |
|        | PE15 | -       | TIM1_BKIN         | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_D12                        | -            | LCD_R7  | EVEN TOUT |

**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0 | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14   | AF15      |
|--------|------|-----|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/DMMCM2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port F | PF0  | -   | -                 | -        | -                              | I2C2_SD_A             | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A0                         | -            | -      | EVEN TOUT |
|        | PF1  | -   | -                 | -        | -                              | I2C2_SCL              | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A1                         | -            | -      | EVEN TOUT |
|        | PF2  | -   | -                 | -        | -                              | I2C2_SMB_A            | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A2                         | -            | -      | EVEN TOUT |
|        | PF3  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A3                         | -            | -      | EVEN TOUT |
|        | PF4  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A4                         | -            | -      | EVEN TOUT |
|        | PF5  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A5                         | -            | -      | EVEN TOUT |
|        | PF6  | -   | -                 | -        | TIM10_CH1                      | -                     | SPI5_NSS   | SAI1_SD_B                                  | -  | UART7_Rx                                  | QUADSPI_BK1_IO3                    | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PF7  | -   | -                 | -        | TIM11_CH1                      | -                     | SPI5_SCK   | SAI1_MCLK_B                                | -  | UART7_Tx                                  | QUADSPI_BK1_IO2                    | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PF8  | -   | -                 | -        | -                              | -                     | SPI5_MISO  | SAI1_SCK_B                                 | -  | UART7_RTS                                 | TIM13_CH1                          | QUADSPI_BK1_IO0                               | -                    | -                              | -            | -      | EVEN TOUT |
|        | PF9  | -   | -                 | -        | -                              | -                     | SPI5_MOSI  | SAI1_FS_B                                  | -  | UART7_CTS                                 | TIM14_CH1                          | QUADSPI_BK1_IO1                               | -                    | -                              | -            | -      | EVEN TOUT |
|        | PF10 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | QUADSPI_CLK                        | -   | -                    | -                              | DCMI_D11     | LCD_DE | EVEN TOUT |
|        | PF11 | -   | -                 | -        | -                              | -                     | SPI5_MOSI  | -  | -  | -   | -                                  | SAI2_SD_B                                     | -                    | FMC_SDNRAS                     | DCMI_D12     | -      | EVEN TOUT |





**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0 | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14    | AF15      |
|--------|------|-----|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|---------|-----------|
|        |      | SYS | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI6/I2C4/UART5/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/DMMCM2/OFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD     | SYS       |
| Port F | PF12 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A6                         | -            | -       | EVEN TOUT |
|        | PF13 | -   | -                 | -        | -                              | I2C4_SMBA             | -  | DFSDM1_DATAIN6                             | -  | -   | -                                  | -   | -                    | FMC_A7                         | -            | -       | EVEN TOUT |
|        | PF14 | -   | -                 | -        | -                              | I2C4_SCL              | -  | DFSDM1_CKIN6                               | -  | -   | -                                  | -   | -                    | FMC_A8                         | -            | -       | EVEN TOUT |
|        | PF15 | -   | -                 | -        | -                              | I2C4_SDA              | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A9                         | -            | -       | EVEN TOUT |
| Port G | PG0  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A10                        | -            | -       | EVEN TOUT |
|        | PG1  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A11                        | -            | -       | EVEN TOUT |
|        | PG2  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A12                        | -            | -       | EVEN TOUT |
|        | PG3  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A13                        | -            | -       | EVEN TOUT |
|        | PG4  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A14/FMC_BA0                | -            | -       | EVEN TOUT |
|        | PG5  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_A15/FMC_BA1                | -            | -       | EVEN TOUT |
|        | PG6  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | FMC_NE3                        | DCMI_D12     | LCD_R7  | EVEN TOUT |
|        | PG7  | -   | -                 | -        | -                              | -                     | -  | SAI1_MCLK_A                                | -  | USART6_CK                                 | -                                  | -   | -                    | FMC_INT                        | DCMI_D13     | LCD_CLK | EVEN TOUT |

**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0     | AF1               | AF2      | AF3                            | AF4                   | AF5   | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                         | AF12                           | AF13         | AF14   | AF15      |
|--------|------|---------|-------------------|----------|--------------------------------|-----------------------|---|--|--|---|------------------------------------|---|------------------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS     | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SPI4/I2S4/USART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH         | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port G | PG8  | -       | -                 | -        | -                              | -                     | SPI6_NS_S   | -  | SPDIF_RX2  | USART6_RTS                                | -                                  | -   | ETH_PPS_OUT                  | FMC_SD_CLK                     | -            | LCD_G7 | EVEN TOUT |
|        | PG9  | -       | -                 | -        | -                              | -                     | SPI1_MI_SO  | -  | SPDIF_RX3  | USART6_RX                                 | QUADSPI_BK2_IO2                    | SAI2_FS_B                                     | SDMMC2_D0                    | FMC_NE2/FMC_NCE                | DCMI_V_SYNC  | -      | EVEN TOUT |
|        | PG10 | -       | -                 | -        | -                              | -                     | SPI1_NS_S/I2S1_WS                                     | -  | -  | -   | LCD_G3                             | SAI2_SD_B                                     | SDMMC2_D1                    | FMC_NE3                        | DCMI_D2      | LCD_B2 | EVEN TOUT |
|        | PG11 | -       | -                 | -        | -                              | -                     | SPI1_SCK/I2S1_CK                                      | -  | SPDIF_RX0  | -   | -                                  | SDMMC2_D2                                     | ETH_MII_TX_EN/ETH_RMII_TX_EN | -                              | DCMI_D3      | LCD_B3 | EVEN TOUT |
|        | PG12 | -       | -                 | -        | LPTIM1_IN1                     | -                     | SPI6_MI_SO  | -  | SPDIF_RX1  | USART6_RTS                                | LCD_B4                             | -   | SDMMC2_D3                    | FMC_NE4                        | -            | LCD_B1 | EVEN TOUT |
|        | PG13 | TRACED0 | -                 | -        | LPTIM1_OUT                     | -                     | SPI6_SCK  | -  | -  | USART6_CTS                                | -                                  | -   | ETH_MII_TXD0/ETH_RMII_TXD0   | FMC_A24                        | -            | LCD_R0 | EVEN TOUT |
|        | PG14 | TRACED1 | -                 | -        | LPTIM1_ETR                     | -                     | SPI6_MOSI   | -  | -  | USART6_TX                                 | QUADSPI_BK2_IO3                    | -   | ETH_MII_TXD1/ETH_RMII_TXD1   | FMC_A25                        | -            | LCD_B0 | EVEN TOUT |
|        | PG15 | -       | -                 | -        | -                              | -                     | -   | -  | -  | USART6_CTS                                | -                                  | -   | -                            | FMC_SD_NCAS                    | DCMI_D13     | -      | EVEN TOUT |



**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0 | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14   | AF15      |
|--------|------|-----|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSMD1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port H | PH0  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PH1  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | -      | EVEN TOUT |
|        | PH2  | -   | -                 | -        | LPTIM1_N2                      | -                     | -  | -  | -  | -   | QUADSPI_BK2_IO0                    | SAI2_SCK_B                                    | ETH_MII_CRS          | FMC_SDCKE0                     | -            | LCD_R0 | EVEN TOUT |
|        | PH3  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | QUADSPI_BK2_IO1                    | SAI2_MCK_B                                    | ETH_MII_COL          | FMC_SDNE0                      | -            | LCD_R1 | EVEN TOUT |
|        | PH4  | -   | -                 | -        | -                              | I2C2_SCL              | -  | -  | -  | -   | LCD_G5                             | OTG_HS_ULPI_NXT                               | -                    | -                              | -            | LCD_G4 | EVEN TOUT |
|        | PH5  | -   | -                 | -        | -                              | I2C2_SDA              | SPI5_NSS   | -  | -  | -   | -                                  | -   | -                    | FMC_SDNWE                      | -            | -      | EVEN TOUT |
|        | PH6  | -   | -                 | -        | -                              | I2C2_SMB_A            | SPI5_SCK   | -  | -  | -   | TIM12_CH1                          | -   | ETH_MII_RXD2         | FMC_SDNE1                      | DCMI_D8      | -      | EVEN TOUT |
|        | PH7  | -   | -                 | -        | -                              | I2C3_SCL              | SPI5_MISO  | -  | -  | -   | -                                  | -   | ETH_MII_RXD3         | FMC_SDCKE1                     | DCMI_D9      | -      | EVEN TOUT |
|        | PH8  | -   | -                 | -        | -                              | I2C3_SDA              | -  | -  | -  | -   | -                                  | -   | -                    | FMC_D16                        | DCMI_HSYNC   | LCD_R2 | EVEN TOUT |
|        | PH9  | -   | -                 | -        | -                              | I2C3_SMB_A            | -  | -  | -  | -   | TIM12_CH2                          | -   | -                    | FMC_D17                        | DCMI_D0      | LCD_R3 | EVEN TOUT |
|        | PH10 | -   | -                 | TIM5_CH1 | -                              | I2C4_SMB_A            | -  | -  | -  | -   | -                                  | -   | -                    | FMC_D18                        | DCMI_D1      | LCD_R4 | EVEN TOUT |
|        | PH11 | -   | -                 | TIM5_CH2 | -                              | I2C4_SCL              | -  | -  | -  | -   | -                                  | -   | -                    | FMC_D19                        | DCMI_D2      | LCD_R5 | EVEN TOUT |
|        | PH12 | -   | -                 | TIM5_CH3 | -                              | I2C4_SDA              | -  | -  | -  | -   | -                                  | -   | -                    | FMC_D20                        | DCMI_D3      | LCD_R6 | EVEN TOUT |
|        | PH13 | -   | -                 | -        | TIM8_CH1N                      | -                     | -  | -  | -  | UART4_TX                                  | CAN1_TX                            | -   | -                    | FMC_D21                        | -            | LCD_G2 | EVEN TOUT |

**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0 | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8  | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14      | AF15      |
|--------|------|-----|-------------------|----------|--------------------------------|-----------------------|--|--|--|--|------------------------------------|---|----------------------|--------------------------------|--------------|-----------|-----------|
|        |      | SYS | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI6/SAI2/USART6/USART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD       | SYS       |
| Port H | PH14 | -   | -                 | -        | TIM8_CH2N                      | -                     | -  | -  | -  | UART4_RX                                   | CAN1_RX                            | -   | -                    | FMC_D22                        | DCMI_D4      | LCD_G3    | EVEN TOUT |
|        | PH15 | -   | -                 | -        | TIM8_CH3N                      | -                     | -  | -  | -  | -  | -                                  | -   | -                    | FMC_D23                        | DCMI_D11     | LCD_G4    | EVEN TOUT |
| Port I | PI0  | -   | -                 | TIM5_CH4 | -                              | -                     | SPI2_NSS/I2S2_WS                                     | -  | -  | -  | -                                  | -   | -                    | FMC_D24                        | DCMI_D13     | LCD_G5    | EVEN TOUT |
|        | PI1  | -   | -                 | -        | TIM8_BKIN                      | -                     | SPI2_SCK/I2S2_CK                                     | -  | -  | -  | -                                  | -   | -                    | FMC_D25                        | DCMI_D8      | LCD_G6    | EVEN TOUT |
|        | PI2  | -   | -                 | -        | TIM8_CH4                       | -                     | SPI2_MISO  | -  | -  | -  | -                                  | -   | -                    | FMC_D26                        | DCMI_D9      | LCD_G7    | EVEN TOUT |
|        | PI3  | -   | -                 | -        | TIM8_ETR                       | -                     | SPI2_MOSI/I2S2_SD                                    | -  | -  | -  | -                                  | -   | -                    | FMC_D27                        | DCMI_D10     | -         | EVEN TOUT |
|        | PI4  | -   | -                 | -        | TIM8_BKIN                      | -                     | -  | -  | -  | -  | -                                  | SAI2_MCK_A                                    | -                    | FMC_NB_L2                      | DCMI_D5      | LCD_B4    | EVEN TOUT |
|        | PI5  | -   | -                 | -        | TIM8_CH1                       | -                     | -  | -  | -  | -  | -                                  | SAI2_SCK_A                                    | -                    | FMC_NB_L3                      | DCMI_VSYNC   | LCD_B5    | EVEN TOUT |
|        | PI6  | -   | -                 | -        | TIM8_CH2                       | -                     | -  | -  | -  | -  | -                                  | SAI2_SD_A                                     | -                    | FMC_D28                        | DCMI_D6      | LCD_B6    | EVEN TOUT |
|        | PI7  | -   | -                 | -        | TIM8_CH3                       | -                     | -  | -  | -  | -  | -                                  | SAI2_FS_A                                     | -                    | FMC_D29                        | DCMI_D7      | LCD_B7    | EVEN TOUT |
|        | PI8  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -  | -                                  | -   | -                    | -                              | -            | -         | EVEN TOUT |
|        | PI9  | -   | -                 | -        | -                              | -                     | -  | -  | -  | UART4_RX                                   | CAN1_RX                            | -   | -                    | FMC_D30                        | -            | LCD_VSYNC | EVEN TOUT |
|        | PI10 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -  | -                                  | -   | ETH_MII_RX_ER        | FMC_D31                        | -            | LCD_HSYNC | EVEN TOUT |
|        | PI11 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -  | LCD_G6                             | OTG_HS_ULPI_DIR                               | -                    | -                              | -            | -         | EVEN TOUT |



**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0 | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                                | AF10  | AF11                 | AF12                           | AF13         | AF14       | AF15      |
|--------|------|-----|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|------------------------------------|---|----------------------|--------------------------------|--------------|------------|-----------|
|        |      | SYS | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART4/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/TIM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD        | SYS       |
| Port I | PI12 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_HS_YNC | EVEN TOUT |
|        | PI13 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_VS_YNC | EVEN TOUT |
|        | PI14 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_CLK    | EVEN TOUT |
|        | PI15 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | LCD_G2                             | -   | -                    | -                              | -            | LCD_R0     | EVEN TOUT |
| Port J | PJ0  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | LCD_R7                             | -   | -                    | -                              | -            | LCD_R1     | EVEN TOUT |
|        | PJ1  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_R2     | EVEN TOUT |
|        | PJ2  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | DSI_TE       | LCD_R3     | EVEN TOUT |
|        | PJ3  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_R4     | EVEN TOUT |
|        | PJ4  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_R5     | EVEN TOUT |
|        | PJ5  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_R6     | EVEN TOUT |
|        | PJ6  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_R7     | EVEN TOUT |
|        | PJ7  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_G0     | EVEN TOUT |
|        | PJ8  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_G1     | EVEN TOUT |
|        | PJ9  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_G2     | EVEN TOUT |
|        | PJ10 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                  | -   | -                    | -                              | -            | LCD_G3     | EVEN TOUT |

**Table 13. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx alternate function mapping (continued)**

| Port   |      | AF0 | AF1               | AF2      | AF3                            | AF4                   | AF5  | AF6  | AF7  | AF8                                       | AF9                               | AF10  | AF11                 | AF12                           | AF13         | AF14   | AF15      |
|--------|------|-----|-------------------|----------|--------------------------------|-----------------------|--|--|--|---|-----------------------------------|---|----------------------|--------------------------------|--------------|--------|-----------|
|        |      | SYS | I2C4/UART5/TIM1/2 | TIM3/4/5 | TIM8/9/10/11/LPTIM1/DFSDM1/CEC | I2C1/2/3/4/USART1/CEC | SPI1/I2S1/SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART5/DFSDM4/5/6 | SPI2/I2S2/SPI3/I2S3/SAI1/I2C4/UART5/DFSDM1 | SPI2/I2S2/SPI3/I2S3/SPI6/USART1/2/3/UART5/DFSDM1/SPDIF | SPI6/SAI2/USART6/UART4/5/7/8/OTG_FS/SPDIF | CAN1/2/IM12/13/14/QUADSPI/FMC/LCD | SAI2/QUADSPI/SDMMC2/DFSDM1/OTG_HS/OTG1_FS/LCD | I2C4/CAN3/SDMMC2/ETH | UART7/FMC/SDMMC1/MDIOS/OTG2_FS | DCMI/LCD/DSI | LCD    | SYS       |
| Port J | PJ11 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_G4 | EVEN TOUT |
|        | PJ12 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | LCD_G3                            | -   | -                    | -                              | -            | LCD_B0 | EVEN TOUT |
|        | PJ13 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | LCD_G4                            | -   | -                    | -                              | -            | LCD_B1 | EVEN TOUT |
|        | PJ14 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_B2 | EVEN TOUT |
|        | PJ15 | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_B3 | EVEN TOUT |
| Port K | PK0  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_G5 | EVEN TOUT |
|        | PK1  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_G6 | EVEN TOUT |
|        | PK2  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_G7 | EVEN TOUT |
|        | PK3  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_B4 | EVEN TOUT |
|        | PK4  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_B5 | EVEN TOUT |
|        | PK5  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_B6 | EVEN TOUT |
|        | PK6  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_B7 | EVEN TOUT |
|        | PK7  | -   | -                 | -        | -                              | -                     | -  | -  | -  | -   | -                                 | -   | -                    | -                              | -            | LCD_DE | EVEN TOUT |

## 4 Memory mapping

The memory map is shown in [Figure 22](#).

**Figure 22. Memory map**

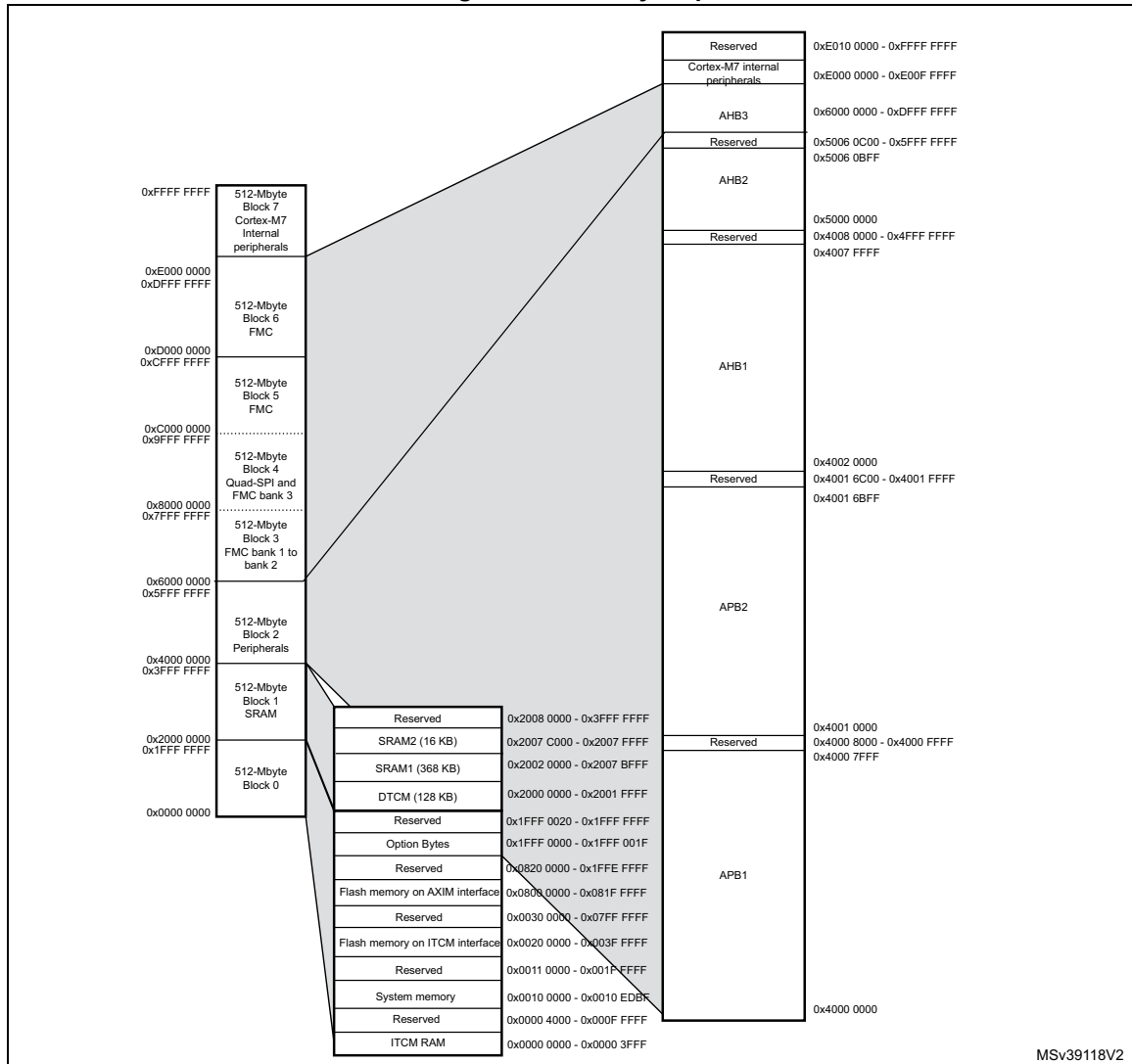


Table 14. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx register boundary addresses<sup>(1)</sup>

| Bus       | Boundary address          | Peripheral                     |
|-----------|---------------------------|--------------------------------|
| -         | 0xE00F FFFF - 0xFFFF FFFF | Reserved                       |
| Cortex-M7 | 0xE000 0000 - 0xE00F FFFF | Cortex-M7 internal peripherals |
| AHB3      | 0xD000 0000 - 0xDFFF FFFF | FMC bank 6                     |
|           | 0xC000 0000 - 0xCFFF FFFF | FMC bank 5                     |
|           | 0xA000 2000 - 0xBFFF FFFF | Reserved                       |
|           | 0xA000 1000 - 0xA000 1FFF | Quad-SPI control register      |
|           | 0xA000 0000 - 0xA000 0FFF | FMC control register           |
|           | 0x9000 0000 - 0x9FFF FFFF | Quad-SPI                       |
|           | 0x8000 0000 - 0x8FFF FFFF | FMC bank 3                     |
|           | 0x7000 0000 - 0x7FFF FFFF | FMC bank 2                     |
|           | 0x6000 0000 - 0x6FFF FFFF | FMC bank 1                     |
| -         | 0x5006 0C00 - 0x5FFF FFFF | Reserved                       |
| AHB2      | 0x5006 0800 - 0x5006 0BFF | RNG                            |
|           | 0x5005 2000 - 0x5005 FFFF | Reserved                       |
|           | 0x5005 1000 - 0x5005 1FFF | JPEG codec                     |
|           | 0x5005 0000 - 0x5005 03FF | DCMI                           |
|           | 0x5004 0000 - 0x5004 FFFF | Reserved                       |
|           | 0x5000 0000 - 0x5003 FFFF | USB OTG FS                     |



Table 14. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx register boundary addresses<sup>(1)</sup> (continued)

| Bus  | Boundary address          | Peripheral               |
|------|---------------------------|--------------------------|
| -    | 0x4008 0000- 0x4FFF FFFF  | Reserved                 |
| AHB1 | 0x4004 0000 - 0x4007 FFFF | USB OTG HS               |
|      | 0x4002 BC00- 0x4003 FFFF  | Reserved                 |
|      | 0x4002 B000 - 0x4002 BBFF | Chrom-ART (DMA2D)        |
|      | 0x4002 9400 - 0x4002 AFFF | Reserved                 |
|      | 0x4002 9000 - 0x4002 93FF | ETHERNET MAC             |
|      | 0x4002 8C00 - 0x4002 8FFF |                          |
|      | 0x4002 8800 - 0x4002 8BFF |                          |
|      | 0x4002 8400 - 0x4002 87FF |                          |
|      | 0x4002 8000 - 0x4002 83FF |                          |
|      | 0x4002 6800 - 0x4002 7FFF | Reserved                 |
|      | 0x4002 6400 - 0x4002 67FF | DMA2                     |
|      | 0x4002 6000 - 0x4002 63FF | DMA1                     |
|      | 0x4002 5000 - 0x4002 5FFF | Reserved                 |
|      | 0x4002 4000 - 0x4002 4FFF | BKPSRAM                  |
|      | 0x4002 3C00 - 0x4002 3FFF | Flash interface register |
|      | 0x4002 3800 - 0x4002 3BFF | RCC                      |
|      | 0x4002 3400 - 0x4002 37FF | Reserved                 |
|      | 0x4002 3000 - 0x4002 33FF | CRC                      |
|      | 0x4002 2C00 - 0x4002 2FFF | Reserved                 |
|      | 0x4002 2800 - 0x4002 2BFF | GPIOK                    |
|      | 0x4002 2400 - 0x4002 27FF | GPIOJ                    |
|      | 0x4002 2000 - 0x4002 23FF | GPIOI                    |
|      | 0x4002 1C00 - 0x4002 1FFF | GPIOH                    |
|      | 0x4002 1800 - 0x4002 1BFF | GPIOG                    |
|      | 0x4002 1400 - 0x4002 17FF | GPIOF                    |
|      | 0x4002 1000 - 0x4002 13FF | GPIOE                    |
|      | 0x4002 0C00 - 0x4002 0FFF | GIOD                     |
|      | 0x4002 0800 - 0x4002 0BFF | GPIOC                    |
|      | 0x4002 0400 - 0x4002 07FF | GPIOB                    |
|      | 0x4002 0000 - 0x4002 03FF | GPIOA                    |

Table 14. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx register boundary addresses<sup>(1)</sup> (continued)

| Bus  | Boundary address          | Peripheral         |
|------|---------------------------|--------------------|
| -    | 0x4001 7C00 - 0x4001 FFFF | Reserved           |
| APB2 | 0x4001 7800 - 0x4001 7BFF | MDIOS              |
|      | 0x4001 7400 - 0x4001 77FF | DFSDM1             |
|      | 0x4001 6C00 - 0x4001 73FF | DSI Host           |
|      | 0x4001 6800 - 0x4001 6BFF | LCD-TFT            |
|      | 0x4001 6000 - 0x4001 67FF | Reserved           |
|      | 0x4001 5C00 - 0x4001 5FFF | SAI2               |
|      | 0x4001 5800 - 0x4001 5BFF | SAI1               |
|      | 0x4001 5400 - 0x4001 57FF | SPI6               |
|      | 0x4001 5000 - 0x4001 53FF | SPI5               |
|      | 0x4001 4C00 - 0x4001 4FFF | Reserved           |
|      | 0x4001 4800 - 0x4001 4BFF | TIM11              |
|      | 0x4001 4400 - 0x4001 47FF | TIM10              |
|      | 0x4001 4000 - 0x4001 43FF | TIM9               |
|      | 0x4001 3C00 - 0x4001 3FFF | EXTI               |
|      | 0x4001 3800 - 0x4001 3BFF | SYSCFG             |
|      | 0x4001 3400 - 0x4001 37FF | SPI4               |
|      | 0x4001 3000 - 0x4001 33FF | SPI1/I2S1          |
|      | 0x4001 2C00 - 0x4001 2FFF | SDMMC1             |
|      | 0x4001 2400 - 0x4001 2BFF | Reserved           |
|      | 0x4001 2000 - 0x4001 23FF | ADC1 - ADC2 - ADC3 |
|      | 0x4001 1C00 - 0x4001 1FFF | SDMMC2             |
|      | 0x4001 1800 - 0x4001 1BFF | Reserved           |
|      | 0x4001 1400 - 0x4001 17FF | USART6             |
|      | 0x4001 1000 - 0x4001 13FF | USART1             |
|      | 0x4001 0800 - 0x4001 0FFF | Reserved           |
|      | 0x4001 0400 - 0x4001 07FF | TIM8               |
|      | 0x4001 0000 - 0x4001 03FF | TIM1               |

Table 14. STM32F765xx, STM32F767xx, STM32F768Ax and STM32F769xx register boundary addresses<sup>(1)</sup> (continued)

| Bus  | Boundary address          | Peripheral          |
|------|---------------------------|---------------------|
| -    | 0x4000 8000- 0x4000 FFFF  | Reserved            |
| APB1 | 0x4000 7C00 - 0x4000 7FFF | UART8               |
|      | 0x4000 7800 - 0x4000 7BFF | UART7               |
|      | 0x4000 7400 - 0x4000 77FF | DAC                 |
|      | 0x4000 7000 - 0x4000 73FF | PWR                 |
|      | 0x4000 6C00 - 0x4000 6FFF | HDMI-CEC            |
|      | 0x4000 6800 - 0x4000 6BFF | CAN2                |
|      | 0x4000 6400 - 0x4000 67FF | CAN1                |
|      | 0x4000 6000 - 0x4000 63FF | I2C4                |
|      | 0x4000 5C00 - 0x4000 5FFF | I2C3                |
|      | 0x4000 5800 - 0x4000 5BFF | I2C2                |
|      | 0x4000 5400 - 0x4000 57FF | I2C1                |
|      | 0x4000 5000 - 0x4000 53FF | UART5               |
|      | 0x4000 4C00 - 0x4000 4FFF | UART4               |
|      | 0x4000 4800 - 0x4000 4BFF | USART3              |
|      | 0x4000 4400 - 0x4000 47FF | USART2              |
|      | 0x4000 4000 - 0x4000 43FF | SPDIFRX             |
|      | 0x4000 3C00 - 0x4000 3FFF | SPI3 / I2S3         |
|      | 0x4000 3800 - 0x4000 3BFF | SPI2 / I2S2         |
|      | 0x4000 3400 - 0x4000 37FF | CAN3                |
|      | 0x4000 3000 - 0x4000 33FF | IWDG                |
|      | 0x4000 2C00 - 0x4000 2FFF | WWDG                |
|      | 0x4000 2800 - 0x4000 2BFF | RTC & BKP Registers |
|      | 0x4000 2400 - 0x4000 27FF | LPTIM1              |
|      | 0x4000 2000 - 0x4000 23FF | TIM14               |
|      | 0x4000 1C00 - 0x4000 1FFF | TIM13               |
|      | 0x4000 1800 - 0x4000 1BFF | TIM12               |
|      | 0x4000 1400 - 0x4000 17FF | TIM7                |
|      | 0x4000 1000 - 0x4000 13FF | TIM6                |
|      | 0x4000 0C00 - 0x4000 0FFF | TIM5                |
|      | 0x4000 0800 - 0x4000 0BFF | TIM4                |
|      | 0x4000 0400 - 0x4000 07FF | TIM3                |
|      | 0x4000 0000 - 0x4000 03FF | TIM2                |

1. The gray color is used for reserved Flash memory addresses.