

Submission Checklist for Project 1 PART A

Rule	Did you follow this rule?? (YES / NO)
The output from your simulator includes the memory hierarchy configuration, the final contents of all caches, and all required measurements	
The output from your simulator matches EXACTLY both the formatting and numerical values of the posted validation runs. The only exception is that you are permitted a variable amount of whitespace (tabs and spaces), although newlines must match (i.e., no extra lines).	
You explicitly confirmed that your outputs match EXACTLY (except for tabs and spaces) the posted validation runs, by running the command “diff –iw ...” as explained in the spec.	
Simulator compiles and runs on an Eos linux machine (e.g., remote-linux, grendel, VCL, etc.)	
You can just type “make” to build your simulator (via your Makefile)	
The simulator built by the Makefile actually runs correctly (some students introduce the Makefile at the last minute and don’t actually test the simulator version built by it – don’t make this mistake)	
The compiled simulator is called “sim_cache”	
Your simulator “sim_cache” takes in arguments as specified in the spec:	
Your simulator prints output to the console (i.e., screen) only.	
You have been vigilant about taking certain precautions: keeping backups, not touching files after the submission deadline (to keep timestamps valid, just in case we need to revisit the original files), etc.	
Simulator completes a single run in less than 2 minutes.	
Submit only a single zip file. The name of your submitted zip file is “project1A.zip”	
The size of your submitted zip file is less than 1MB (if you need to go over by a little bit, keep it reasonable)	
Include all the necessary files in your project1A.zip submission: * Source code files * Makefile	