

On the Methods and Applications
Of Cryptography On the
Texas Instruments TI-84+ CE
Graphing Calculator

A Whitepaper by Anthony Cagliano

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1 Introduction

HASHLIB was a project I began while developing another cutting-edge networking project, *VAPOR*, to provide on-device hashing capabilities. The primary function of these hashes was to allow the client, a TI-84+ CE graphing calculator, to validate the completeness of file transfers. It provided standard additive 24-bit and 32-bit checksums, CRC-32, SHA-1, and SHA-256.

It wasn't long before I realized that I could use **HASHLIB** as an opportunity to bring some more advanced cryptography to the TI-84+ CE. Thus, I endeavored to expand the capabilities of the library beyond just hashing. In its current state the library computes SHA-1 and SHA-256 (with the simpler, non-cryptographic hashes removed) and adds Blowfish symmetric encryption, a crypto-safe random number generator, and there are plans to add the public key side of RSA (to a max key size of 1024) as well as the bcrypt hash in the near future.

Some of you may be thinking "Why"? "Why do all this work on a calculator when much more secure and faster alternatives exist to implement this on?". The answer is multi-faceted. First, because I can. That is really the only answer needed. Beyond that, I wanted to prove that at least some of this could be done, reasonably fast on a TI-84+ CE. I also wanted to understand the logic behind the algorithms, so what better way to do that then to write them myself, or to derive an existing algorithm and adapt it for this device. As a consequence of undertaking this project, I now have an improved understanding of how cryptography works, as well as an understanding and respect for the professionals who do consistent and much more intricate work on much more complicated functions than I implemented to ensure that our Internet experience remains as safe as possible.

In this whitepaper, I will elaborate, where needed, on the methodologies I used to arrive at the algorithm I implemented, and where relevant, provide citations of routines I derived and how I modified them for this platform. I will also provide use-case examples for various algorithms, or combinations, of algorithms. I will conclude this introduction and move onto the good stuff by thanking those who have downloaded, assisted with, contributed to, or encouraged this project, and will credit contributors later in the document.

2 Abridged Library Information

2.1 Crypto-Safe Pseudo-Random Number Generator

HASHLIB contains a cryptographically-secure random number generator designed to seek out the best source of entropy on the device running the code, and use that state to populate an entropy pool. See the Proofs & Extended Information section for specifics about the implementation.

Functions

hashlib_CSPRNGInit(void)

Initializes the Crypto-Safe PRNG to read from the byte with the best entropy on the device. Adds initial entropy as well. Returns NULL if failure.

hashlib_CSPRNGAddEntropy(void)

Reads 128 bytes from the selected byte to the internal entropy pool.

hashlib_CSPRNGRandom(void)

Returns a pseudo random 32-bit integer derived from a hash of the entropy pool and 4 reads from the selected byte. Adds entropy as well. Returns 0 if failure.

hashlib_RandomBytes(uint8_t *ptr, size_t len)

An implementation based on *urandom*; Fills a pointed buffer to size indicated with random numbers. Uses internal CSPRNG.

2.2 SHA-1 & SHA-256

This library also contains the SHA-1 and SHA-256 cryptographic hashes. These implementations were derived from B-Con's `crypto-algorithms`¹ repository, which is in the public domain. To adapt this code to the TI-84+ CE, I needed to redefine the data type "WORD" as a `uint32_t`, so that it would fit within the architecture of the ez80 ALU. In addition, I needed to write handler functions for resetting and adding `uint64_t`-type values, since neither the CE operating system nor the C toolchain contain support for this. The `uint64_t` handler functions were written in ez80 Assembly by `beckadamtheinventor`². Finally, for both SHA-1 and SHA-256, I wrote a public static function

¹<https://github.com/B-Con/crypto-algorithms/>

²<https://github.com/beckadamtheinventor>

that allows for quick passing of a single block of data without requiring the user to define the SHA context usually necessary.

Functions

hashlib_Sha1Init(*sha1_ctx* **ctx*)

Initializes the state of pointed SHA-1 context structure.

hashlib_Sha1Update(*sha1_ctx* **ctx*, const uint8_t **buf*, size_t *len*)

Updates the SHA-1 state with indicated length of data at pointed buffer.

hashlib_Sha1Final(*sha1_ctx* **ctx*, const uint8_t **digest*)

Finalizes context arithmetic, returns hash into digest buffer.

hashlib_Sha256Init(*sha256_ctx* **ctx*)

Initializes the state of pointed SHA-256 context structure.

hashlib_Sha256Update(*sha256_ctx* **ctx*, const uint8_t **buf*, size_t *len*)

Updates the SHA-256 state with indicated length of data at pointed buffer.

hashlib_Sha256Final(*sha256_ctx* **ctx*, const uint8_t **digest*)

Finalizes context arithmetic, returns hash into digest buffer.

2.3 Advanced Encryption Standard (AES), CBC Mode

In addition to the faster cryptographic hashes, this library also provides two encryption implementations, the first of which is the `AES symmetric block cipher`. It accepts keys of length 128, 192, or 256 bits. This code was also derived in large part from B-Con's crypto-algorithms. However, the code on the repository did not implement plaintext padding, requiring me to write my own. That function will be elaborated on in the Miscellaneous section of this function list.

Functions

hashlib_AESLoadKey(const uint8_t **key*, aes_ctx **ks*, size_t *keysize*)

Loads the supplied key of length *keysize* into the AES key schedule structure.

```
hashlib_AESEncrypt(const uint8_t *plaintext, size_t len, uint8_t *ciphertext,  
aes_ctx *ks, const uint8_t *iv)
```

Encrypts the indicated size at pointed *plaintext buffer, writing the output to pointed *ciphertext.

```
hashlib_AESDecrypt(const uint8_t *ciphertext, size_t len, uint8_t *plaintext,  
aes_ctx *ks, const uint8_t *iv)
```

Decrypts the indicated size at pointed *ciphertext buffer, writing the output to pointed *plaintext.

2.4 Base64 Encode/Decode

As part of my effort to implement **bcrypt**, I decided to implement Base64 encoding and decoding. I started off writing these myself, but during the arduous process of attempting to debug bcrypt, I wound up scrapping my own routines for ones from the `OpenBSD repository`³.

Functions

```
hashlib_b64encode(char *b64buffer, const uint8_t *data, size_t len)
```

Base 64 encodes the indicated length, in input bytes, at pointed *data buffer and writes the output to pointed *b4buffer.

```
hashlib_b64decode(uint8_t *buffer, size_t len, const char *b64data)
```

Base 64 decodes the indicated length, in output bytes, at pointed *b64buffer and writes the output to pointed *buffer.

2.5 RSA

The RSA implementation planned for this library will be one of my own design with minimum derivation from external code. It will also cover only the public key side of the protocol, as generating a key pair on the calculator would be painfully slow. The implementation will also depend on beckadamtheinventor's planned *big-int* library. It will support variable key sizes from 512 bits to 1024 bits. As the implementation is still work-in-progress, that's really all I can say about it at the time.

³<https://github.com/openbsd/src/blob/master/lib/libc/crypt/bcrypt.c>

2.6 Miscellaneous

Lastly, there are a few functions that exist in this library, dissociated from specific implementations but useful for other purposes. In this section, you will see some information on them.

hashlib_EraseContext(void *ctx, size_t len)

Writes zeroes to a pointed context to prevent information leak.

hashlib_PadInputPlaintext(const uint8_t *plaintext, size_t len, uint8_t *outbuf, uint8_t alg, uint8_t schm)

Pads the input plaintext according to the selected scheme and algorithm. It is recommended to just pass SCHM_DEFAULT as the padding scheme. This selects PKCS7 for AES-CBC and OAEP encoding for RSA. The available padding modes are:

- (a) (AES) SCHM_PKCS7 (pad with the length of the padding)
- (b) (AES) SCHM_ISO_M2 (pad with the bytes 0x80 followed by 0x00 for the remaining length)
- (c) (AES) SCHM_ISO_M1 (pad with zeroes to the remaining length)
- (d) (AES) SCHM_ANSIX923 (pad with pseudorandom bytes to the remaining length)
- (e) (RSA) SCHM_RSA_OAEP (pad with 4 bytes and then a hashed salt. Hash is SHA-256, applied cyclically across the message).

3 Proofs & Extended Information

In this section, implementations that I devised myself will have their mechanisms elaborated on and, where applicable, mathematical proofs show. Please note that, in accordance with the general rule of thumb of "don't write your own crypto", I used adapted versions of tested algorithms where possible.

3.1 CSPRNG Elaboration

The CSPRNG is the most platform-dependent part of this code, and consequentially the part of it I did the most work on. The first question to answer in the quest to devise one was does the TI-84+ CE produce randomness, and if so, how does this differ by hardware revision. The calculator is unlike a computer in that it lacks many of the dynamic sources of entropy that go into generating randomness for those platforms. To this aim, the Cemetech⁴ user Zeroko was of much assistance. At their direction, I discovered that the address range \$D65800 through \$D659FF contains hardware "bus noise", and that certain parts of this address space are more heavily biased than others and that these sections differ by revision. This led me to write an algorithm, (with the help of `beckadamtheinventor`, who converted my C code into faster ez80 Assembly) that polls each bit of every byte in this address space looking for the bit with the least bias (out of N bit reads, the bit was set closest to N/2 times). The most optimal byte is set internally, and is unable to be modified by the user. This allows the CSPRNG to utilize the same code regardless of revision, without having to use a revision-based lookup table that might not work on all software (or hardware) revisions. Should a bit of sufficient entropy not be found (the maximum allowable deviation in the set bit count is $N/2 \pm 25\%$ of N, where N is the total number of bit reads), the function will return NULL, and calls to the other CSPRNG functions will return either 0 or NULL.

The internal state of the CSPRNG also reserves an entropy pool 128 bytes in length to which it reads from the optimal byte 128 times every time a random number is generated or an `Add Entropy` function is called by the user.

To generate a random number, the optimal byte is read from 4 times to produce an initial state of low entropy for the random number. After this, the entropy pool is hashed using SHA-256 to generate a 32-byte digest of high entropy. That hash is broken into 4-byte blocks (8 iterations), with bytes 1-4 of each hash iteration being xor'ed with bytes 1-4 of the random number, sequentially. The `Add Entropy` function is called at this point to scramble the state, even if the user does not call `Add Entropy` themselves. This ensures that this algorithm passes the next-bit test and state compromise tests, as even with moderate bias, and full knowledge of the current state of the entropy pool, it is nearly impossible to predict the result of 128 reads that would affect the next generated number with greater than 50% probability.

⁴<https://www.cemetech.net/>

Proof. **Entropy for Worst-Case Byte Selection**

The byte selected for use by the CSPRNG will have at least one bit with a maximum bias of 75% ($\pm 25\%$ deviation from 50/50), with the rest of the byte likely having a much higher bias. To this end, I will assume 100% bias, or 0 entropy, for them. The entropy of the 75/25 bit can be represented as the set of probabilities .75, .25, which can be applied to the equation for Shannon entropy:

$$H(x) = \sum_{i=1}^n P(i) \log_2 \frac{1}{P(i)}$$

Where n is the length of the set of probabilities and $P(i)$ is the probability of the i -th element in the set

$$H(x)_{min} = 0.750 \log_2 \frac{1}{0.750} + 0.250 \log_2 \frac{1}{0.250} = 0.811$$

Multiplying the entropy for a byte in the entropy pool (pending data on correlation) by the 128 bytes of the entropy pool plus the initial 4-byte read to the rand, you arrive at:

$$H(x)_{min}(128 + 4) = 0.811 \times 132.0 = 107.1$$

This means we have 107.1 bits of entropy per 32-bit number at minimum. Because the available entropy, 107.1 bits, exceeds (by a lot) the bit-width of the number we are generating, I assert that the probability of $r \leftarrow \{0, 1\}$ is within a negligible deviation, ϵ , from 50/50 for all bits in the 32-bit number.

Let A be an algorithm to test probability bit b is set s.t. $A : A(b) = (b == 1)$

$$Adv(PRG) = [\Pr(A(G(x))) - \Pr(r \leftarrow \{0, 1\})] \forall_b \leftarrow \{0, 1\}^{32}$$

$$Adv(PRG) = [(\frac{1}{2} \pm \epsilon) - \frac{1}{2}]$$

$$Adv(PRG) = \pm \epsilon$$

Because the advantage is negligible, the output of the CSPRNG is not distinguishable from a uniform distribution over the same 32 bits, thus this PRNG is secure. Bear in mind that this is with **maximum allowable bias**; The chosen entropy source will likely have a lower bias, and thus the entropy will become even higher and the advantage even closer to zero.

4 Applications for HASHLIB

Surprisingly, HASHLIB can have a few practical applications other than making its developer wish he was dead (which was a quite frequent occurrence during the development process). In fact, this entire project was born out of the need for a cryptographic hash to validate the completeness of a file transfer, and as such, this is a good example of a use for it. In addition, Blowfish is a symmetric block cipher with no effective cryptanalysis available to date. The implementation in HASHLIB is slow to perform a key expansion, but is decently fast otherwise. Over time, it will get even faster as efforts to optimize it are undertaken.

From a practical standpoint, you should not be doing things on a calculator that require decent security, like logging into secure servers, sending credit card info, and more anyway. Whatever cryptographic mechanisms I can pull off on a graphing calculator with a 48 mHz CPU is likely child's play compared to what a computer leveraging a CPU orders of magnitude faster can do. It would follow that I would need to choose between implementing strict security resulting in an algorithm that takes a year and a half to run, or sacrifice a little bit of security for a little bit of speed. Where possible, I chose the most secure mechanism I could.

This library offers two encryption implementations—one symmetric block cipher (AES) and one asymmetric public key cipher (RSA). The RSA implementation is encryption-only. It is intended to be used in a server-centric setup where, upon connection to a remote host, the server generates a public and private key pair and shares the public key with the client. The client, in this case, your calculator, generates an AES key in the length of 128-256 bits. Longer keys are more secure but will cause slower encryption. This key should be RSA encrypted with the public key and then sent back to the remote host. With both the client and the remote host now in possession of a shared secret, AES can be used for symmetric encryption when data security is needed. As these implementation will slow down your data transfers noticeably, it is recommended to only use them when needed. As previously mentioned, if you need genuine security, please do not use a calculator for what you are doing. I intended this library as proof of concept, a learning exercise for myself, and something fun for the TI programming community to experiment with.

5 References

- [1] B-Cons Crypto Algorithms
- [2] Cemetch — Producing Crypto-Safe Randomness on the TI-84+ CE
- [3] Wikipedia — CSPRNG Information
- [5] Wikipedia — Base64 Implementation

6 Contributors

- [1] beekadamtheinventor [coding assistance]
- [2] commandblockguy [coding assistance]
- [3] Zeroko [information on entropy on TI-84+ CE]