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# A Simple Cache-Sensitive Skip List (SCSSL)

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#### Abstract

We present a cache-sensitive skip list (SCSSL) that is remarkably simple and easy to implement. It requires only minor modifications to the original skip list data structure to adapt it effectively to modern CPU architectures. This adaptation results in a performance improvement typically ranging from 1.2x to 1.6x, and on some systems, can reach 2x to 3x for insertion-heavy workloads. Unlike other optimization techniques, SCSSL does not rely on specialized hardware instructions like SIMD or intricate multi-threaded programming paradigms. Instead, its design inherently benefits from improved data locality due to the use of arrays at the data level, a feature that modern compilers can further leverage. This makes the proposed design broadly applicable, suitable not only for in-memory databases and key-value stores but also for resource-constrained embedded systems.

Keywords: skip list; cache-sensitive; index structure; data structures; performance optimization

## 1 Introduction

In computer science, ordered data structures are fundamental for organizing and managing data efficiently. The performance of search, insertion, and deletion operations within these structures is crucial for a wide array of applications, as it directly impacts overall system performance. Numerous methods have been developed to achieve high performance in ordered data structures, such as AVL trees and Red-Black trees. However, these balanced tree structures are relatively complex to implement due to the necessity of rebalancing operations. This landscape changed when William Pugh introduced the skip list in 1990 [Pugh90]. The skip list, a probabilistic data structure, offered a simpler implementation alternative to traditional balanced trees while providing comparable performance characteristics.

Thanks to its simplicity and efficiency, the skip list has been widely adopted in various systems. Notable examples include databases like LevelDB [GoogleLDB], Redis [Redis], RocksDB [FacebookRDB], and HBase [ApacheHB], as well as in-memory caches and the inverted index component of search engines like Lucene.

A standard skip list consists of multiple levels: a data level (level 0) that stores all data nodes in a sorted linked list, and multiple index levels above it. These index levels contain a subset of the nodes from lower levels and act as "express lanes" to expedite traversal to the desired data nodes. However, nodes in a skip list are typically allocated dynamically, leading to non-contiguous memory addresses. This fragmentation can degrade performance on modern CPUs due to frequent cache misses.

Although cache-sensitive skip list variants like the Cache-Sensitive Skip List (CSSL) [CSSLXX] and ESL (Express Lane Skip List) [ESLXX] have been developed to better align with modern CPU architectures, they often introduce significant complexity. For instance, CSSL utilizes SIMD instructions, which may not be universally available or optimal across all platforms. ESL proposes merging index level nodes into arrays and employs sophisticated version-based locking protocols and lock-free operation logs for concurrency. We believe that the primary appeal of the skip list lies in its inherent simplicity. Overly complex cache-sensitive designs can diminish this advantage.

Our work is motivated by the observation that simple architectural changes can yield substantial performance gains by improving cache locality. We propose a modification that retains the core simplicity of the skip list while significantly enhancing its cache-friendliness.

## 2 Design Overview

In this paper, we present a Simple Cache-Sensitive Skip List (SCSSL). The core idea is to modify the nodes at the data level (level 0) to store a small, sorted array of elements (e.g., key-value pairs or just keys) instead of a single element. This approach leverages CPU cache locality for these small arrays, thereby enhancing performance. For example, Figure 1 illustrates a conceptual SCSSL where data nodes hold arrays of maximum size 2.

The design of SCSSL hinges on three key principles:

- 1. **Array-based Data Nodes:** At the data level (level 0), each node contains a sorted array with a fixed maximum length. For integer keys, a maximum length that allows the node to fit well within CPU cache lines (e.g., up to 128 elements, depending on key/value sizes) is chosen.
- 2. **Index Level Pivot:** For navigation through the index levels (levels 1 and above), only the first element (i.e., the smallest key) of the array in a data-level node (or an index node pointing to such an array) is used as the pivot for comparison.
- 3. **Insertion Handling:** The last element (i.e., the largest key) of an array in a data node is primarily considered during insertion operations, particularly when an array is full and a new key needs to be accommodated.

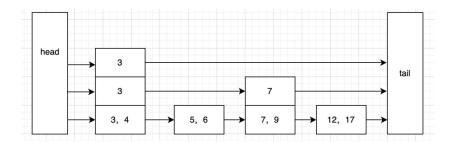


Figure 1: A conceptual view of the Simple Cache-Sensitive Skip List (SCSSL) where data nodes (level 0) store arrays of elements (e.g., max array size = 2 as depicted). Index nodes point to the start of these arrays.

Our key contributions include:

- 1. Introducing arrays within data-level nodes, which maintains the asymptotic complexity of skip list operations while significantly improving CPU cache hit rates due to the contiguous storage of multiple elements.
- 2. Reducing the effective height of the skip list and pointer overhead, as each data-level node now stores multiple elements, leading to fewer nodes overall for a given dataset size.
- 3. Presenting modified insertion, search, and deletion algorithms tailored for these array-based data nodes, offering a practical and simple approach for enhancing skip list performance, particularly in database indexing and similar applications.

# 3 Algorithms

This section details the algorithms for search, insertion, and deletion in SCSSL. These operations are adapted from standard skip list algorithms to accommodate the array-based nodes at the data level.

In SCSSL, each node at the data level (level 0) stores its elements in a sorted array of a fixed maximum size. When a new node is created, its level is chosen randomly, similar to a standard skip list. A node promoted to level k has k+1 forward pointers (indexed 0 to k). The maximum number of levels in the skip list is a predefined constant, 'MAX<sub>P</sub>OSSIBLE<sub>L</sub>EVEL'. The skip list temploys a header node to initiate searches; its k

Figure 2 illustrates an example of an insertion operation in SCSSL.

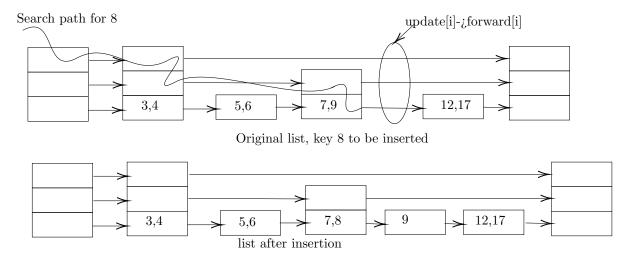


Figure 2: Insertion of key 8 into an SCSSL (max array size = 2). Key 8 displaces 9 from node (7,9). Key 9 is then inserted into a new node.

#### 3.1 Initialization

The initialization process is elementary. A header node is created with an array of 'MAX $_POSSIBLE_LEVEL+1$ ' forwardpointers, all initialized to NIL. To simplify comparisons, the header node's key array (if it were to have one in the same  $\infty$ .

### 3.2 Search

The search algorithm (Algorithm 1) navigates the skip list from the highest level down to level 0. At each level 'i', it traverses forward as long as the first key of the array in the next node ('current.forward[i].FirstKey()') is less than or equal to the search key 'k'. Once this traversal is complete, the 'current' node is the one whose own array might contain 'k' (specifically, 'current.FirstKey()  $_i$ = k', and either 'current.forward[0]' is **NIL** or 'current.forward[0].FirstKey()  $_i$  k'). The final step involves searching for 'k' within 'current''s array.

```
Algorithm 1: Search for Key k in SCSSL
```

#### 3.3 Insertion

The insertion algorithm (Algorithm 2) first searches for the appropriate position for the new key 'k', maintaining an 'update' array to store pointers to the predecessor nodes at each level. The core logic then addresses several cases based on whether the target data node ('target\_node', which is 'update [0]') can accommodate 'k':

- $\bullet$  Current node is header: If 'target\_node' is the header, a new node is usually created, or 'k' is inserted into 'header. for weather the node is header.
- Target node full, 'k' fits within existing range (CASE 1 from paper): If 'target\_node' sarrayis full but'k' is small

- Target node not full (CASE 2 from paper): If 'target\_node' 'sarray is not full and 'k' belongs there (i.e., 'k >=  $target_node.FirstKey()$ '), 'k' is inserted directly into 'target\_node' 'sarray. The operation then completes.
- Key 'k' belongs after 'target\_node' (CASE3 from paper): If 'k' is greater than all elements in 'target\_node' sarray ('k target\_node.array\_last\_element()'), insertion is attempted in 'target\_node.forward[0]'. If 'target\_node.forward[0]' is suita
- New node creation (Other CASES): If none of the above conditions lead to an inplace insertion, a new node is created for 'k'. Its level is randomly determined. If this new level exceeds the list's 'currentGlobalMaxLevel', the 'update' array and 'currentGlobalMaxLevel' are adjusted. Pointers are then updated to link the new node.

```
Algorithm 2: Insert Key k into SCSSL
```

```
Input: Key k, current global maximum level 'currentGlobalMaxLevel', header node 'header',
            constant 'MAX_POSSIBLE_LEVEL'
   Output: Potentially updated 'currentGlobalMaxLevel'
 1 Function Insert(k, currentGlobalMaxLevel, header, MAX_POSSIBLE_LEVEL):
       update[MAX\_POSSIBLE\_LEVEL + 1];
                                                                    // Array to store predecessors
 2
       current \leftarrow header;
 3
       for i \leftarrow currentGlobalMaxLevel\ 0\ \mathbf{do}
 4
           while current.forward[i] \neq \mathbf{NIL} \land current.forward[i].FirstKey() \leq k \ \mathbf{do}
 5
               current \leftarrow current.forward[i];
 6
           update[i] \leftarrow current;
 7
       target\_node \leftarrow update[0];
 8
       if target\_node \neq header \land target\_node.is\_full() \land k <
        target\_node.array\_last\_element() \land k >= target\_node.FirstKey() then
           // CASE 1
           replaced\_key \leftarrow target\_node.array.pop\_last();
10
           target\_node.insert\_into\_array(k);
11
           k \leftarrow replaced\_key;
       if target\_node \neq header \land \neg target\_node.is\_full() \land k >= target\_node.FirstKey() then
13
           // CASE 2
           target\_node.insert\_into\_array(k);
14
           return currentGlobalMaxLevel;
15
           // Insertion complete
       current \leftarrow target\_node.forward[0];
16
       if current \neq NIL \land \neg current.is\_full() \land k >= current.FirstKey() then
17
           // CASE 3
           current.insert\_into\_array(k);
18
           return currentGlobalMaxLevel;
19
          // Insertion complete
       else
20
           // OTHER CASES: Create a new node for k
           new\_node\_level \leftarrow random\_level();
21
           if new_node_level > currentGlobalMaxLevel then
22
               \mathbf{for}\ i \leftarrow currentGlobalMaxLevel + 1 \mathbf{to} new\_node\_level\ \mathbf{do}
23
                  update[i] \leftarrow header;
24
               currentGlobalMaxLevel \leftarrow new\_node\_level;
25
           new\_node \leftarrow Node([k], new\_node\_level);
26
           for i \leftarrow 0 to new\_node\_level do
27
               new\_node.forward[i] \leftarrow update[i].forward[i];
28
               update[i].forward[i] \leftarrow new\_node;
29
       return currentGlobalMaxLevel;
30
```

#### 3.4 Deletion

The deletion algorithm (Algorithm 3) also starts by finding the key 'k'. The traversal logic advances 'current' as long as the \*entire array\* in 'current.forward[i]' contains keys strictly smaller than 'k' (i.e., 'current.forward[i].array\_last\_element() < k'). This ensures that 'update[0].forward[0]' becomes the first node whose array coul If 'k' is found and deleted from this node's array:

- If the array becomes empty, the node itself is removed from the skip list by adjusting the forward pointers of its predecessors.
- If removing a node causes some highest levels to become empty, 'currentGlobalMaxLevel' is decremented.

### **Algorithm 3:** Delete Key k from SCSSL

```
Input: Key k, current global maximum level 'currentGlobalMaxLevel', header node 'header',
            constant 'MAX_POSSIBLE_LEVEL'
   Output: (Boolean indicating success, potentially updated 'currentGlobalMaxLevel')
 1 Function Delete(k, currentGlobalMaxLevel, header, MAX_POSSIBLE_LEVEL):
       update[MAX\_POSSIBLE\_LEVEL + 1];
 2
       current \leftarrow header;
 3
       for i \leftarrow currentGlobalMaxLevel\ 0\ \mathbf{do}
 4
           while current.forward[i] \neq \mathbf{NIL} \land current.forward[i].array\_last\_element() < k \ \mathbf{do}
 5
              current \leftarrow current.forward[i];
 6
          update[i] \leftarrow current;
 7
      node\_to\_check \leftarrow current.forward[0];
 8
      if node\_to\_check = NIL then
 9
10
          return (False, currentGlobalMaxLevel);
      if k < node\_to\_check.FirstKey() then
11
         return (False, currentGlobalMaxLevel);
12
       deleted\_from\_arr \leftarrow node\_to\_check.delete\_from\_array(k);
13
      if \neg deleted\_from\_arr then
14
          return (False, currentGlobalMaxLevel);
15
      if node_to_check.is_array_empty() then
16
          for i \leftarrow 0 to node\_to\_check.level do
17
              if update[i].forward[i] \neq node\_to\_check then
18
19
              update[i].forward[i] \leftarrow node\_to\_check.forward[i];
20
           while currentGlobalMaxLevel > 0 \land header.forward[currentGlobalMaxLevel] = NIL do
21
              currentGlobalMaxLevel \leftarrow currentGlobalMaxLevel - 1;
22
      return (True, currentGlobalMaxLevel);
23
```

## 4 Experimental Evaluation

## 4.1 Experimental Environment

The experiments were conducted in the following environments:

- Hardware 1 (MacBook): Apple M2 CPU, 16 GB RAM.
- Hardware 2 (Server): Intel(R) Core(TM) i7-6700 CPU @ 3.40GHz, 24 GB DDR4 2133 MHz RAM. This server was used not only for performance testing but also specifically for analyzing CPU cache miss rates using appropriate profiling tools (e.g., 'perf').
- Compiler: The algorithms were implemented in C++. The test programs were compiled using g++ (version specific to environment, e.g., 9.0 or higher) with the '-O3' optimization flag.
- Benchmark Configuration: Insertion operations in a skip list are prone to CPU cache misses due to dynamic memory allocations and pointer chasing. Therefore, our benchmarks primarily focus on insertion performance using randomly generated integer keys. The number of elements ranged from 100 to 300,000. Search performance was also evaluated. However, we do not present detailed search results as the performance difference between SCSSL and the standard skip list was found to be negligible for typical search operations. This is likely because the dominant factor in search is the traversal of levels, which has a similar path length, and the final array scan in SCSSL is very fast for small arrays. Both the standard skip list and SCSSL were configured with a maximum level ('MAX<sub>P</sub>OSSIBLE<sub>L</sub>EVEL') of 16. For SCSSL, themaximum arraysize within adata node was settom

#### 4.2 Results and Discussion

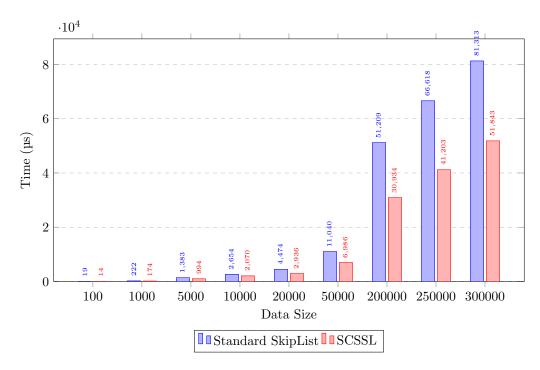


Figure 3: Insertion Time Comparison: Standard Skip List vs. SCSSL on macOS (Apple M2). Lower is better.

On the MacBook (Apple M2 processor, Figure 3), SCSSL demonstrates a speedup of 1.28x-1.39x for small datasets (i1,000 elements) during insertion. For medium-scale datasets (i20,000 elements), the speedup increases to a range of 1.55x-1.65x. This trend indicates that the cache-friendly benefits of SCSSL become more pronounced as the dataset size grows, leading to more memory accesses.

On the server system (Intel i7-6700, Figure 4), SCSSL achieves even more significant speedups for insertions, reaching up to nearly 3x compared to the standard skip list for larger datasets (e.g., 300,000 elements). This enhanced improvement on the server platform might be attributed to differences in cache architecture, memory subsystem performance, or the relative cost of cache misses compared to the M2 architecture.

Table 1: Cache Performance Comparison (Insertions, 300,000 elements) on Server (Intel i7-6700)

Test Case	Cache References	Cache Misses	Cache Miss Rate
SCSSL	51,567,218	459,636	0.89%
Standard Skip List	142,249,678	47,251,323	33.22%

Table 1 presents the cache performance data for inserting 300,000 random elements on the server. The standard skip list exhibits a high L1 data cache miss rate of 33.22%. In contrast, SCSSL drastically reduces this rate to a mere 0.89%. This substantial reduction in cache misses directly corroborates our hypothesis that embedding arrays within data nodes effectively improves data locality and cache utilization, which translates into the observed speedups. The significantly lower number of cache references for SCSSL also suggests more efficient data access patterns.

## 5 Conclusion

We have presented SCSSL, a novel cache-sensitive skip list variant that introduces a simple yet highly effective modification: replacing single elements in data-level nodes with small, sorted arrays. This architectural change allows SCSSL to enhance CPU cache performance by improving data locality, while crucially maintaining the algorithmic simplicity that is a hallmark of skip lists.

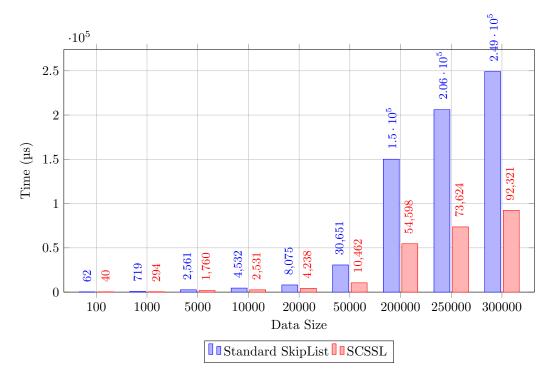


Figure 4: Insertion Time Comparison: Standard Skip List vs. SCSSL on Server (Intel i7-6700). Lower is better.

Experimental results demonstrate significant performance improvements for insertion operations, particularly on systems where cache performance is a critical factor. The observed speedups of 1.2x-3x, coupled with a dramatic reduction in cache miss rates (e.g., from 33.22% to 0.89% in our server tests), validate the efficacy of this approach. SCSSL offers a practical and easily implementable method to boost skip list performance in modern computing environments without resorting to complex algorithmic changes or specialized hardware features. Its simplicity makes it an attractive option for a wide range of applications, from embedded systems to large-scale in-memory data stores.

Future work could explore optimal array sizes for different hardware platforms and key/value types, as well as investigate the application of SCSSL principles in concurrent skip list implementations.

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