## Exercise 5

To compile: unzip our uploaded code, and run make inside code/. The slurm scripts are stored inside code/slurm/.

To debug: run the debug outputs (\*.dbg) and attach gdb to respective pids

## 5.1 Reading

## 5.2 Heat Relaxation — Sequential Implementation

(see code/) Visualization in img/heat.gif

# 5.3 Heat Relaxation — Experiments

The Sequential Heat Relaxation Implementation of subsection 5.2 resulted in the values observed in Table 1. These Numbers resulted from 100 iterations per grid size, with two buffers.

Table 1: Resulting Numbers for a sequential Heat Relaxation

Grid Size	Time per iteration/s	Flops total	GFLOP64/s
128x128	0.0001	11290300	2.0623
512 x 512	0.0041	182784700	0.4470
$1024 \times 1024$	0.0112	732570300	0.6544
$2048 \times 2048$	0.2091	2933146300	0.1403
4096x4096	0.8034	11738317500	0.1461

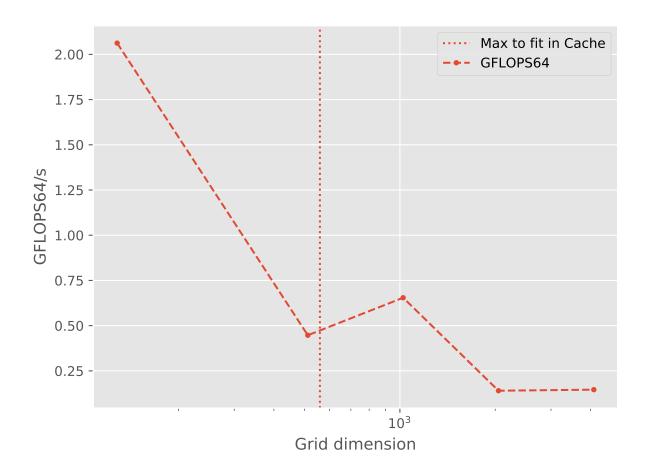


Figure 1: Resulting GFLOPS64 for increasing Dimensions, with the maximum dimension for fitting both buffers into cache marked

the resulting numbers are not surprising. The Size Limit for fitting both buffers into Cache<sup>1</sup>, when reached will result in the effective GFLOPS plummeting.

$$Max Grid Size = \sqrt{\frac{Cache Size}{Byte per Double \cdot \# of Buffers}}$$
 (1)

$$=\sqrt{\frac{10\,\mathrm{MB}}{8\,\mathrm{B}\cdot2}}\tag{2}$$

$$\approx 790$$
 (3)

But these Numbers reperesent the maximum possible dimension if nothing else has to be stored in the cache.

When increasing the dimension further, a further decrease in performance will be expected, due to an increase in cache misses.

#### 5.4 Heat Relaxation — Pre-considerations for parallelization

Some considerations for applying MPI to the heat relaxation problem:

 $<sup>^110</sup> MB$  for a Intel Xeon E5-1620

- 1. To de-compose the complete problem into subtasks would mean splitting the grid into parts for which a separate rank is respinsible. This would require the ability to communicate between "neighboring" ranks.
- 2. For this grid either 1D or 2D partitioning is possible, e.g.: splitting the MxN Grid into 4 rectangles with one dimension equal to the original grid  $(\frac{M}{4}xN \text{ or } Mx\frac{N}{4})$  or splitting the complete Grid into 4 rectangles of the same aspect ratio as the original  $(\frac{M}{2}x\frac{N}{2})$ .

The better partitioning for a given Problem would be the one, in which we are required to exchange fewer grid points (Volumne-Surface Rule). Figure 2 is demonstration this rule: here all fully filled gridpoints are calculated by a corresponding rank and the not filled outlines depict the points required by the same colored rank for its next iteration.

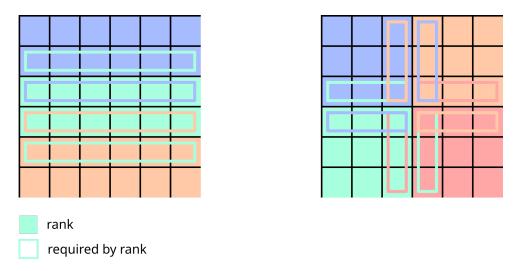


Figure 2: Example of Partitioning Schemes, 1D (NS) on the left, 2D (NEWS) on the right

With the Example of before (4 ranks, MxN Grid, 1D (North-South) and 2D (NEWS) Partitioning), we would need to exchange the following number of points for each method:

# exchanged points<sub>1D, NS</sub> = 
$$N \cdot 2 \cdot \#$$
 partitions – # border partitions =  $N \cdot 2 \cdot 4 - 2 = 8\mathbf{N} - \mathbf{2}$  (5)

# exchanged points<sub>2D, NEWS</sub> = 
$$\sqrt{\# \text{ partitions}} \cdot 2 \cdot N$$
  
+  $\sqrt{\# \text{ partitions}} \cdot 2 \cdot M - \# \text{ border partitions}$  (6)  
=  $4\mathbf{N} + 4\mathbf{M} - 4$  (7)

That means for our use case (big and nearly square grids) the 2D partitioning will always require less points to be exchanged.

3. We know that only the points near the surfaces need to be exchanged. To minimize delays due to these calculations, we should calculate the surface points ideally the moment their required neighbors are available and send them off immedeatly. This is allowing the rest of the volumne to be calculated in the meantime.

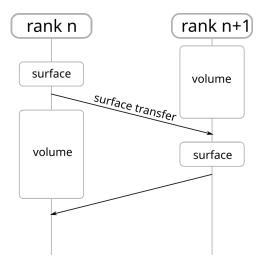


Figure 3: Transfer and Prioritisation of Surface Calculations