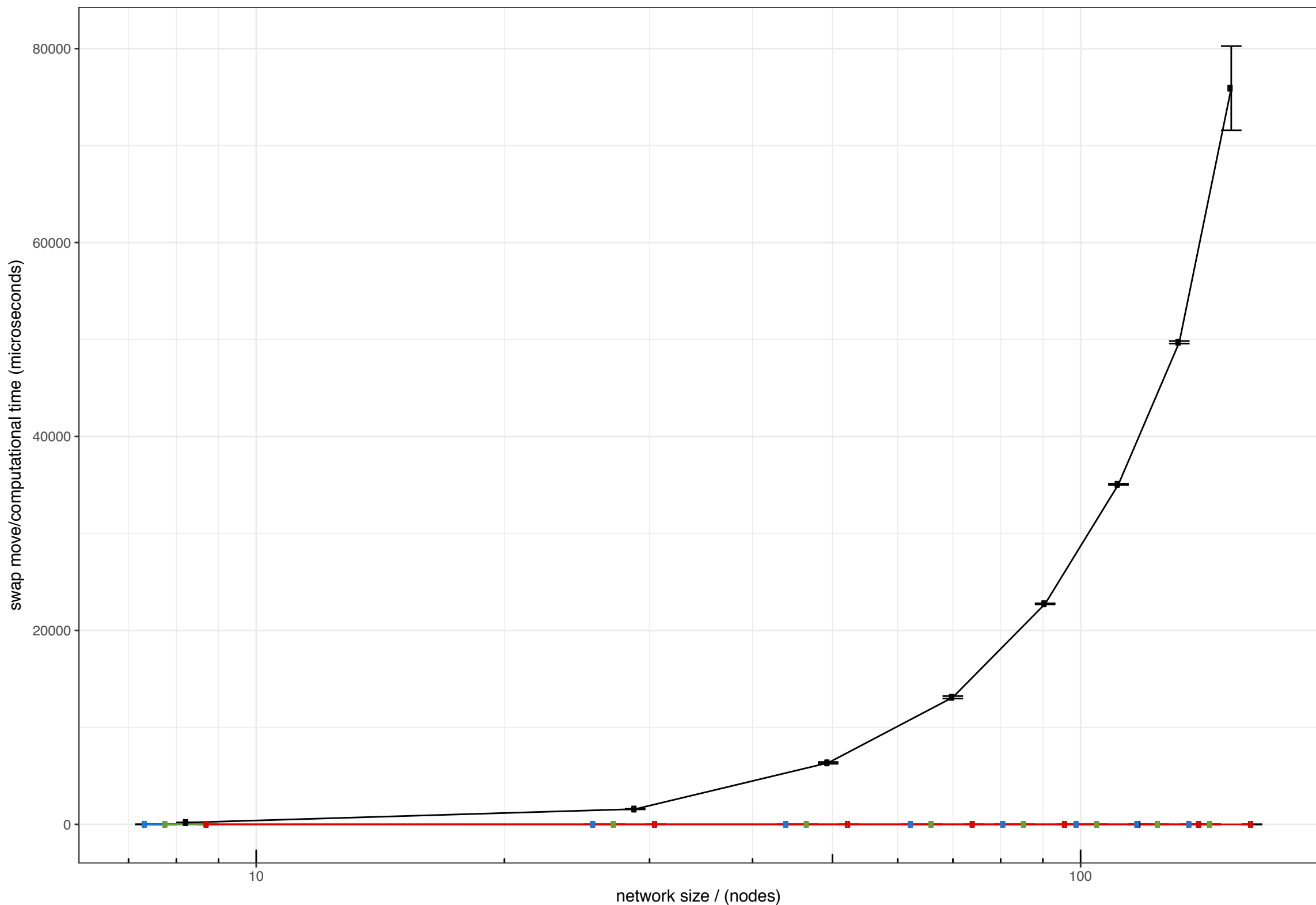


TSHLIB – Benchmark 4: Rearrangement operation performances w.r.t PLL ($d>3$, $n=10$)

Benchmark performed on 2.9 GHz Intel Core i5 with 8 GB 2133 MHz LPDDR3 using a random generated set of hierarchical binary networks



Optimisation scheme — debug — intel — release — pll