

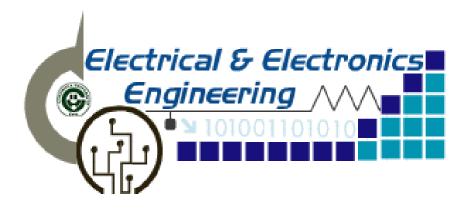


T.C. ÇUKUROVA ÜNİVERSİTESİ MÜHENDİSLİK-MİMARLIK FAKÜLTESİ ELEKTRİK-ELEKTRONİK MÜHENDİSLİĞİ BÖLÜMÜ STAJ DEFTERİ



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING SUMMER PRACTICE REPORT

ALİ CAN GÖK

Internship Company and Department:

Cukurova University Electrical Electronics

Engineering Department

SUMMER PRACTICE REPORT (YAZ STAJI RAPORU) Student Name-Surname: Ali Can Gök Öğrencinin Adı-Soyadı **Starting Date :** 14.07.2020 Staja Başladığı Tarih Completion Date: 14.08.2020 Stajı Tamamladığı Tarih **Total Working Days: 20** Toplam İş Günü Sayısı **Company:** Cukurova University Sirket **Department :** Electrical Electronics Engineering Department Bölüm **Address:** Çukurova Üniversitesi Mühendislik Fakültesi, Elektrik - Elektronik Mühendisliği Bölümü, 01330, Balcalı, Sarıçam, ADANA Adres Electrical-Electronics Engineering Responsible (Name, Department, Phone, Fax, etc.): Dr. Öğretim Üyesi Oğuzhan TİMUR **Diploma Number of Electrical-Electronics Engineering Responsible:** Sorumlu Elektrik-Elektronik Mühendisinin Diploma Numarası

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Not: Diploma numarası olmayanlar oda sicil numaralarını da yazabilirler.

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WORK / PROJECT SUMMARY (İŞ ÖZETİ)

Work Day (İş Günü)	Date (tarih)	Work / Project Description (İş Tanımı)
1	14/07/2019	Introduction
2	16/07/2019	The Design Problem Generally
3	17/07/2019	Power-Handling Ability
4	20/07/2019	Output Power, P0, Versus Apparent Power, Pt, Capability
5	21/07/2019	Output Power, P0, Versus Apparent Power, Pt, Capability
6	22/08/2019	Output Power, P0, Versus Apparent Power, Pt, Capability
7	23/07/2019	Transformers with Multiple Outputs
8	24/07/2019	Regulation
9	27/07/2019	Relationship, Kg, to Power Transformer Regulation Capability
10	28/07/2019	Relationship, Ap, to Transformer Power Handling Capability
11	29/07/2019	Different Cores Same Area Product
12	30/07/2019	Transformer Design, Using the Core Geometry, Kg, Approach

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15	06/08/2019	Transformer Design
16	07/08/2019	Transformer Design
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1. Introduction

The conversion process in power electronics requires the use of transformers and components that are frequently the heaviest and bulkiest item in the conversion circuit. They also have a significant effect upon the overall performance and efficiency of the system. Accordingly, the design of such transformers has an important influence on the overall system weight, power conversion efficiency and cost. Because of the interdependence and interaction of parameters, judicious tradeoffs are necessary to achieve design optimization.

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2. Company Description



The department of Electrical and Electronics engineering was established in the year 1987 with an intention to provide graduate and undergraduate programs in various disciplines. The graduate and undergraduate programs were started in the fall semester of 1989 and 1990, respectively. The language of teaching is English for both undergraduate and graduate programs.

The program has been designed to provide a contemporary Electrical and Electronic engineering education, and is composed of four years. The courses in the first year mainly consist of general physics, chemistry, and mathematics to bring the knowledge level of students with different backgrounds to a standard level. Compulsory courses are given in the second and third years (two non-technical elective courses have been added to each semesters of the third year). The fourth year curriculum includes elective courses, and a graduation thesis that is compulsory (two non-technical elective courses have been added to each semesters of the fourth year). In addition, students must conduct 2 summer practices each composed of at least 20 work days after 4th, and 6th semesters.

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3. POWER TRANSFORMER DESIGN

3.1. The Design Problem Generally

The designer is faced with a set of constraints that must be observed in the design on any transformer. One of these constraints is the output power, P0, (operating voltage multiplied by maximum current demand). The secondary winding must be capable of delivering to the load within specified regulation limits. Another constraint relates to the minimum efficiency of operation, which is dependent upon the maximum power loss that can be allowed in the transformer. Still another defines the maximum permissible temperature rise for the transformer when it is used in a specified temperature environment.

One of the basic steps in transformer design is the selection of proper core material. Magnetic materials used to design low and high frequency transformers are shown in Table 7-1. Each one of these materials has its own optimum point in the cost, size, frequency and efficiency spectrum. The designer should be aware of the cost difference between silicon-iron, nickel-iron, amorphous and ferrite materials. Other constraints relate to the volume occupied by the transformer and, particularly in aerospace applications, the

weight, since weight minimization is an important goal in today's electronics. Finally, cost effectiveness is always an important consideration.

Depending upon the application, certain ones of these constraints will dominate. Parameters affecting others may then be traded off as necessary to achieve the most desirable design. It is not possible to optimize all parameters in a single design because of their interaction and interdependence. For example, if volume and weight are of great significance, reductions in both can often be affected, by operating the transformer at a higher frequency, but, at a penalty in efficiency. When the frequency cannot be increased, reduction in weight and volume may still be possible by selecting a more efficient core material, but, at the penalty of increased cost. Thus, judicious trade-offs must be affected to achieve the design goals.

Transformer designers have used various approaches in arriving at suitable designs. For example, in many cases, a rule of thumb is used for dealing with current density. Typically, an assumption is made that a good working level is 200 amps-per-cm2 (1000 circular mils-per-ampere). This will work in many instances, but the wire size needed to meet this requirement may produce a heavier and bulkier transformer than desired or required. The information presented in this volume makes it possible to avoid the use of this assumption and other rules of thumb, and to develop a more economical design with great accuracy.

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Table 7-1 Magnetic Materials

Magnetic Material Properties				
Material Name	Trade Name Composition	Initial Permeability	Flux Density Tesla B _s	Typical Operating Frequency
		$\mu_{\rm i}$		
Silicon	3-97 SiFe	1500	1.5-1.8	50-2k
Orthonol	50-50 NiFe	2000	1.42-1.58	50-2k
Permalloy	80-20 NiFe	25000	0.66-0.82	1k-25k
Amorphous	2605SC	1500	1.5-1.6	250k
Amorphous	2714A	20,000	0.5-6.5	250k
Amorphous	Nanocrystalline	30,000	1.0-1.2	250k
Ferrite	MnZn	0.75-15k	0.3-0.5	10k-2M
Ferrite	NiZn	0.20-1.5k	0.3-0.4	0.2M-100M

3.2. Power-Handling Ability

For years manufacturers have assigned numeric codes to their cores; these codes represent the powerhandling ability. This method assigns to each core a number that is the product of its window area, Wa, and core cross-section area, Ac, and is called the area product, Ap.

These numbers are used by core suppliers to summarize dimensional and electrical properties in their catalogs. They are available for laminations, C-cores, pot cores, powder cores, ferrite toroids, and toroidal tape-wound cores.

The regulation and power-handling ability of a core is related to the core geometry, Kg. Every core has its own inherent, Kg. The core geometry is relatively new, and magnetic core manufacturers do not list this coefficient.

Because of their significance, the area product, Ap, and core geometry, Kg, are treated extensively in this book. A great deal of other information is also presented for the convenience of the designer. Much of the material is in tabular form to assist the designer in making trade-offs, best-suited for his particular application in a minimum amount of time.

These relationships can now be used as new tools to simplify and standardize the process of transformer design. They make it possible to design transformers of lighter weight and smaller volume, or to optimize efficiency, without going through a cut-and-try, design procedure. While developed especially for aerospace applications, the information has wider utility, and can be used for the design of non-aerospace, as well.

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3.3. Output Power, Po, Versus Apparent Power, Pt, Capability

Output power, Po, is of the greatest interest to the user. To the transformer designer, the apparent power, Pt, which is associated with the geometry of the transformer, is of greater importance. Assume, for the sake of simplicity, that the core of an isolation transformer has only two windings in the window area, a primary and a secondary. Also, assume that the window area, Wa, is divided up in proportion to the power-handling capability of the windings, using equal current density. The primary winding handles, Pin, and the secondary handles, Po, to the load. Since the power transformer has to be designed to accommodate the primary, Pin, and, Po, then,

By definition:

$$P_{i} = P_{in} + P_{o}, \text{ [watts]}$$

$$P_{in} = \frac{P_{o}}{\eta}, \text{ [watts]}$$
[3.1]

The primary turns can be expressed using Faraday's Law:

$$N_p = \frac{V_p \left(10^4\right)}{A_c B_{ac} f K_f}, \quad \text{[turns]}$$
[3.2]

The winding area of a transformer is fully utilized when:

$$K_u W_a = N_p A_{wp} + N_s A_{ws}$$
 [3.3]

By definition the wire area is:

$$A_{w} = \frac{I}{J}, \quad [\text{cm}^2]$$
 [3.4]

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Rearranging the equation shows:

$$K_u W_a = N_p \left(\frac{I_p}{J}\right) + N_s \left(\frac{I_s}{J}\right)$$
 [3.5]

Now, substitute in Faraday's Equation:

$$K_u W_a = \frac{V_p \left(10^4\right)}{A_c B_{ac} f K_f} \left(\frac{I_p}{J}\right) + \frac{V_s \left(10^4\right)}{A_c B_{ac} f K_f} \left(\frac{I_s}{J}\right)$$
[3.6]

Rearranging shows:

$$W_a A_c = \frac{\left[\left(V_p I_p \right) + \left(V_s I_s \right) \right] \left(10^4 \right)}{B_{ac} f J K_f K_u}, \quad [\text{cm}^4]$$
 [3.7]

The output power, P0, is:

$$P_o = V_s I_s$$
, [watts] [3.8]

The input power, Pj,,, is:

$$P_{in} = V_p I_p$$
, [watts] [3.9]

Then:

$$P_{t} = P_{in} + P_{o}$$
, [watts] [3.10]

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Substitute in, Pt:

$$W_a A_c = \frac{P_t(10^4)}{B_{ac} f J K_f K_u}, \text{ [cm}^4]$$
[3.11]

By definition, Ap, equals:

$$A_p = W_a A_c$$
, [cm⁴] [3.12]

Then:

$$A_{p} = \frac{P_{f}(10^{4})}{B_{ac} f J K_{f} K_{u}}, \quad [\text{cm}^{4}]$$
[3.13]

The designer must be concerned with the apparent power, Pt, and power handling capability of the transformer core and windings. P, may vary by a factor, ranging from 2 to 2.828 times the input power, Pin, depending upon the type of circuit in which the transformer is used. If the current in the rectifier transformer becomes interrupted, its effective RMS value changes. Thus, transformer size is not only determined by the load demand, but also, by application, because of the different copper losses incurred, due to the current waveform.

For example, for a load of one watt, compare the power handling capabilities required for each winding, (neglecting transformer and diode losses, so that Pin = P0) for the full-wave bridge circuit of Figure 3-1, the full-wave center-tapped secondary circuit of Figure 3-2, and the push-pull, center-tapped full-wave circuit in Figure 3-3, where all the windings have the same number of turns, (N).

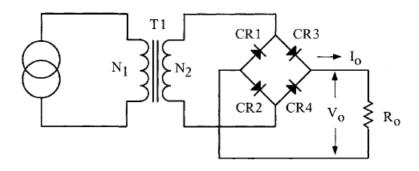


Figure 3-1. Full-Wave Bridge Secondary.

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The total apparent power, Pt, for the circuit shown in Figure 3-1 is 2 watts.

This is shown in the following equation:

$$P_{t} = P_{in} + P_{o}$$
, [watts]

$$P_t = 2P_{in}$$
, [watts] [3.14],[3.15]

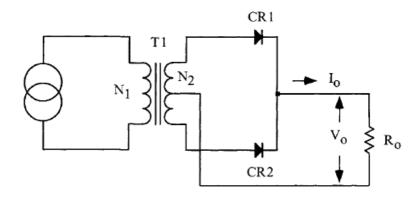


Figure 3-2. Full-Wave, Center-Tapped Secondary.

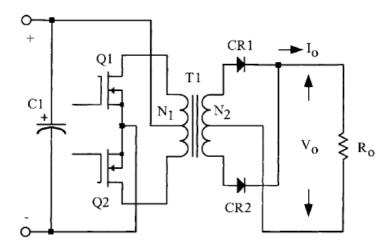


Figure 3-3. Push-Pull Primary, Full-Wave, Center-Tapped Secondary.

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The total power, Pt, for the circuit shown in Figure 3-2, increased 20.7%, due to the distorted wave form of the interrupted current flowing in the secondary winding. This is shown in the following equation:

$$P_t = P_{in} + P_o \sqrt{2}$$
, [watts]

$$P_{in} = P_{in} (1 + \sqrt{2}), \text{ [watts]}$$
 [3.16],[3.17]

The total power, Pt, for the circuit is shown in Figure 3-3, which is typical of a dc to dc converter. It increases to 2.828 times, Pin, because of the interrupted current flowing in both the primary and secondary windings.

$$P_i = P_{in}\sqrt{2} + P_o\sqrt{2}$$
, [watts]

$$P_{t} = 2P_{in}\sqrt{2}$$
, [watts] [3.18],[3.19]

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3.4. Transformers with Multiple Outputs

This example shows how the apparent power, Pt, changes with a multiple output transformers.

Output Circuit 5 V @ 10A center-tapped V_d = diode drop = 1 V 15 V @ 1A full-wave bridge V_d = diode drop = 2 V

Efficiency = 0.95

The output power seen by the transformer in Figure 3-4 is:

$$P_{o1} = (V_{o1} + V_d)(I_{o1}), \text{ [watts]}$$

 $P_{o1} = (5+1)(10), \text{ [watts]}$
 $P_{o1} = 60, \text{ [watts]}$
[3.20]

And:

$$P_{o2} = (V_{o2} + V_d)(I_{o2}),$$
 [watts]
 $P_{o2} = (15 + 2)(1.0),$ [watts]
 $P_{o2} = 17,$ [watts]
[3.21]

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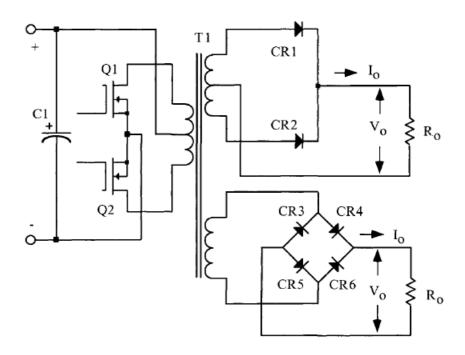


Figure 3-4. Multiple Output Converter.

Because of the different winding configurations, the apparent power, Pt, the transformer outputs will have to be summed to reflect this. When a winding has a center-tap and produces a discontinuous current, then, the power in that winding, be it primary or secondary, has to be multiplied by the factor, U. The factor, U, corrects for the rms current in that winding. If the winding has a center-tap, then the factor, U, is equal to 1.41. If not, the factor, U, is equal to 1.

For an example, summing up the output power of a multiple output transformer, would be:

$$P_{\Sigma} = P_{o1}(U) + P_{o2}(U) + P_{n}(U) + \cdots$$
 [3.22]

Then:

$$P_{\Sigma} = P_{o1}(U) + P_{o2}(U)$$
, [watts]
 $P_{\Sigma} = 60(1.41) + 17(1)$, [watts]
 $P_{\Sigma} = 101.6$, [watts]
[3.23]

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After the secondary has been totaled, then the primary power can be calculated.

$$P_{in} = \frac{P_{o1} + P_{o2}}{\eta}$$
, [watts]
 $P_{in} = \frac{(60) + (17)}{(0.95)}$, [watts]
 $P_{in} = 81$, [watts]

[3.24]

Then, the apparent power, Pt, equals:

$$P_t = P_{in}(U) + P_{\Sigma}$$
, [watts]
 $P_t = (81)(1.41) + (101.6)$, [watts]
 $P_t = 215.8$, [watts]
[3.25]

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3.6. Regulation

The minimum size of a transformer is usually determined either by a temperature rise limit, or by allowable voltage regulation, assuming that size and weight are to be minimized. Figure 3-5 shows a circuit diagram of a transformer with one secondary.

Note that a = regulation (%).

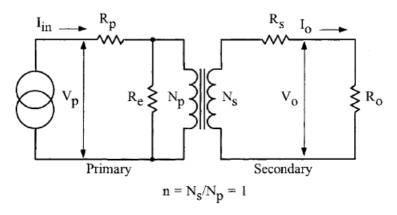


Figure 3-5. Transformer Circuit Diagram.

The assumption is that distributed capacitance in the secondary can be neglected because the frequency an secondary voltage are not excessively high. Also, the winding geometry is designed to limit the leakage inductance to a level, low enough, to be neglected under most operating conditions.

Transformer voltage regulation can now be expressed as:

$$\alpha = \frac{V_o(N.L.) - V_o(F.L.)}{V_o(F.L.)} (100), \quad [\%]$$
[3.26]

In which, Vo(N.L.), is the no load voltage and, Vo(F.L.), is the full load voltage. For the sake of simplicity, assume the transformer in Figure 3-5, is an isolation transformer, with a 1:1 turns ratio, and the core impedance, Re, is infinite.

If the transformer has a 1 : 1 turns ratio, and the core impedance is infinite, then:

$$I_{in} = I_o$$
, [amps]

$$R_p = R_s$$
, [ohms] [3.27]

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With equal window areas allocated for the primary and secondary windings, and using the same current density, J:

$$\Delta V_p = I_{in} R_p = \Delta V_s = I_o R_s, \quad \text{[volts]}$$
[3.28]

Regulation is then:

$$\alpha = \frac{\Delta V_p}{V_p} (100) + \frac{\Delta V_s}{V_s} (100), \quad [\%]$$
[3.29]

Multiply the equation by currents, I:

$$\alpha = \frac{\Delta V_p I_{in}}{V_p I_{in}} (100) + \frac{\Delta V_s I_o}{V_s I_o} (100), \quad [\%]$$
[3.30]

Primary copper loss is:

$$P_p = \Delta V_p I_{in}, \text{ [watts]}$$
 [3.31]

Secondary copper loss is:

$$P_s = \Delta V_s I_o$$
, [watts] [3.32]

Total copper loss is:

$$P_{cu} = P_p + P_s$$
, [watts] [3.33]

Then, the regulation equation can be rewritten to:

$$\alpha = \frac{P_{cu}}{P_o} (100), \quad [\%]$$
[3.34]

Regulation can be expressed as the power lost in the copper. A transformer, with an output power of 100 watts and a regulation of 2%, will have a 2 watt loss in the copper:

$$P_{cu} = \frac{P_o \alpha}{100}, \quad [\text{watts}]$$
[3.35]

$$P_{cu} = \frac{(100)(2)}{100}$$
, [watts] [3.36]

$$P_{cu} = 2$$
, [watts] [3.37]

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3.7. Relationship, Kg, to Power Transformer Regulation Capability

Although most transformers are designed for a given temperature rise, they can also be designed for a given regulation. The regulation and power-handling ability of a core is related to two constants:

$$\alpha = \frac{P_t}{2K_g K_e}, \quad [\%]$$

$$\alpha = \text{Regulation (\%)}$$
[3.39]

The constant, Kg, is determined by the core geometry, which may be related by the following equations:

$$K_{\rm g} = \frac{W_{\rm u} A_{\rm c}^2 K_{\rm u}}{\rm MLT}, \quad [\rm cm^5]$$

The constant, Ke, is determined by the magnetic and electric operating conditions, which may be related by the following equation:

$$K_{c} = 0.145 K_{f}^{2} f^{2} B_{m}^{2} \left(10^{-4}\right)$$
 [3.41]

Where:

$$K_f$$
 = waveform coefficient
4.0 square wave
4.44 sine wave

From the above, it can be seen that factors such as flux density, frequency of operation, and the waveform coefficient have an influence on the transformer size.

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Work Done (Yapılan İş)	Relationship, Ap, to Transformer Power Handling Capability	Date (Tarih)	28/07/2019

3.8. Relationship, Ap, to Transformer Power Handling Capability

Transformers

According to the newly developed approach, the power handling capability of a core is related to its area product, Ap, by an equation which may be stated as:

$$A_{p} = \frac{P_{t}(10^{4})}{K_{f} K_{u} B_{m} J f}, \quad [\text{cm}^{4}]$$
[3.42]

Where:

$$K_f$$
 = waveform coefficient
4.0 square wave
4.44 sine wave

From the above, it can be seen that factors such as flux density, frequency of operation, and the window utilization factor, Ku, define the maximum space which may be occupied by the copper in the window.

3.9.Different Cores Same Area Product

The area product, Ap, of a core is the product of the available window area, Wa, of the core in square centimeters, (cm2), multiplied by the effective, cross-sectional area, Ac, in square centimeters, (cm2), which may be stated as:

$$A_p = W_a A_c$$
, [cm⁴] [3.43]

Figures 3-6 through Figure 3-9 show, in outline form, three transformer core types that are typical of those shown in the catalogs of suppliers.

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Work Done (Yapılan İş)	Different Cores Same Area Product	Date (Tarih)	29/07/2019	

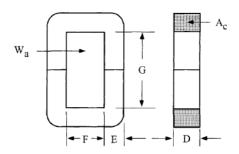


Figure 3-6. Dimensional Outline of a C Core.

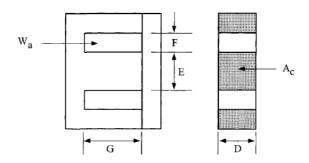


Figure 3-7. Dimensional Outline of a El Lamination.

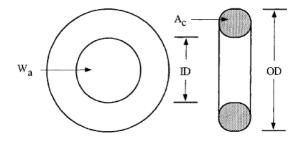


Figure 3-8. Dimensional Outline of a Toroidal Core.

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Work Done (Yapılan İş)	Different Cores Same Area Product	Date (Tarih)	29/07/2020

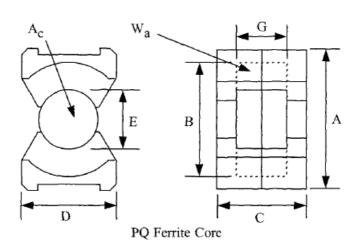


Figure 3-9. Dimensional Outline of a PQ Ferrite Core

3.10 .1 KWatt 100kHz Transformer Design, Using the Core Geometry, Kg, Approach

Input voltage, V(min)	310V dc
Output voltage V(o)	110V dc
Output current I(o)	9.09A
Frequency, f	100kHz
Efficiency, n	%98
Regulation, α	%0.4
Diode voltage drop, Vd	1
Operating flux density, Bac	0.05T
Core Material	3C95 FERROXCUBE
Window utilization, Ku	0.3
Temperature rise goal, Tr	35
Pcu	4W
Using a center-tapped winding,	U=1.41
Using a single winding,	U=1.00

At this point, select a wire so that the relationship between the ac resistance and the dc resistance is 1:

$$\frac{R_{ac}}{R_{dc}} = 1$$

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The skin depth, E, in centimeters, is:

$$\varepsilon = \frac{6.62}{\sqrt{f}}, \quad [\text{cm}]$$

$$\varepsilon = \frac{6.62}{\sqrt{100,000}}, \quad [\text{cm}]$$

$$\varepsilon = 0.0209, \quad [\text{cm}]$$

Then, the wire diameter, DAWG, is:

$$D_{AWG} = 2(\varepsilon)$$
, [cm] $D_{AWG} = 2(0.0209)$, [cm] $D_{AWG} = 0.0418$, [cm]

Then, the bare wire area, Aw, is:

$$A_{w} = \frac{\pi (D_{AWG})^{2}}{4}, \text{ [cm}^{2}]$$

$$A_{w} = \frac{(3.1416)(0.0418)^{2}}{4}, \text{ [cm}^{2}]$$

$$A_{w} = 0.00137, \text{ [cm}^{2}]$$

Wire AWG	Bare Area	Area Ins.	Bare/Ins.	$\mu\Omega/cm$
#26	0.001280	0.001603	0.798	1345
#27	0.001021	0.001313	0.778	1687
#28	0.0008046	0.0010515	0.765	2142

We use AWG #26 Coated copper wire.

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Work Done (Yapılan İş)	Transformer Design	Date (Tarih)	04/08/2020

Step No. 1 Calculate the transformer output power, Po

$$Po=Io*(Vo+Vd)$$

Po=1008.99W

Step No. 2 Calculate the total secondary apparent power, Ps.

Ps=1008.99*1

Ps=1008.99W

Step No. 3 Calculate the total apparent power, Pt.

Pin=(Pout/efficiency)W

Pin=(1008.99/0.98)W

Pin=1029.6W

Pt=Pin*U+Ps

Pt=1029.6*1+1008.99

Pt=2038.50W

Step No. 4 Calculate the electrical conditions, Ke

$$K_e = 0.145 (K_f)^2 (f)^2 (B_m)^2 (10^{-4})$$

 $K_f = 4.0$, [square wave]

Ke=5800.

Step No. 5 Calculate the core geometry, Kg.

$$K_g = \frac{P_t}{2 K_e \alpha}$$
, [cm⁵]

Kg=0.44 cm^5

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Step No. 6 Select a PQ core, comparable in core geometry Kg.

Core number	PQ40/40
Manufacturer	ferroxcube
Magnetic material	3c95
Magnetic path length, MPL	10.2
Core weight, Wtfe	95.0
Copper weight, Wtcu	97.2
Mean length turn, MLT	8.4
Iron area, Ac	2.01
Window area, Wa	3.260
Area product, Ap	6.553
Core geometry, Kg	0.627
Surface area, At	77.1
AL	6100 nH/turns^2

Step No. 7 Calculate the number of primary turns, Np, using Faraday's Law.

$$N_p = \frac{V_p \left(10^4\right)}{K_f B_{ac} f A_c}, \quad \text{[turns]}$$

Step No. 8 Calculate the current density, J, using a window utilization, Ku = 0.3.

$$J = \frac{P_t \left(10^4\right)}{K_f K_u B_{ac} f A_p}, \quad [\text{amps } /\text{cm}^2]$$

J=432.3 amps/cm^2

Step No. 9 Calculate the input current, Iin

$$I_{in} = \frac{P_o}{V_{in} \eta}$$
, [amps]

Step No. 10 Calculate the primary bare wire area, Awp(B).

$$A_{wp(B)} = \frac{I_{in} \sqrt{D_{\text{max}}}}{J}, \quad [\text{cm}^2]$$

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$$Awp(B)=0.00538 cm^2$$

Step No. 11 Calculate the required number of primary strands, Snp

$$S_{np} = \frac{A_{wp (B)}}{\#26}$$

Step No. 12 Calculate the primary new μOHM per centimeter.

$$(\text{new})\mu\Omega/\text{cm} = \frac{\mu\Omega/\text{cm}}{S_{np}}$$

$$(\text{new})\mu\Omega/\text{cm} = (1345/4)$$

$$(\text{new})\mu\Omega/\text{cm} = 336.25$$

Step No. 13 Calculate the primary resistance, Rp.

$$R_p = \text{MLT}\left(N_p\right)\left(\frac{\mu\Omega}{\text{cm}}\right)\left(10^{-6}\right)$$
 [ohms]

Step No. 14 Calculate the primary copper loss, Pp.

$$P_p = I_p^2 R_p$$
, [watts]
Pp=1.956W

Step No. 15 Calculate the secondary turns, Ns.

$$N_{s1} = \frac{N_p V_{s1}}{V_{in}} \left(1 + \frac{\alpha}{100} \right), \text{ [turns]}$$

$$Vs1=Vo+Vd=110+1=111V$$

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Step No. 16 Calculate the secondary bare wire area, Aws.

$$A_{\text{ws 1}} = \frac{I_o \sqrt{D_{\text{max}}}}{J}, \quad [\text{cm}^2]$$

Aws=0.01487 cm²

Step No. 17 Calculate the required number of secondary strands, Sn.

$$S_{ns1} = \frac{A_{ws1(B)}}{\#26}$$

Sn=0.01487/0.00128

Sn=12

Step No. 18 Calculate the secondary, S new μQ per centimeter.

(new)
$$\mu\Omega$$
/cm = $\frac{\mu\Omega$ /cm}{S_{ns 1}}

(new)µohm/cm=112.08 ohms

Step No. 19 Calculate the secondary S resistance, Rs.

$$R_{s1} = \text{MLT}(N_{s1}) \left(\frac{\mu\Omega}{\text{cm}}\right) (10^{-6}), \text{ [ohms]}$$

Rs=0.02165 ohms

Step No. 20 Calculate the secondary copper loss, Ps.

Step No. 21 Calculate the total primary and secondary copper loss, Pcu.

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Step No. 22 Calculate the transformer regulation, α .

$$\alpha = \frac{P_{cu}}{P_o} (100), \quad [\%]$$

$$\alpha = (3.744/1000)*100=0.374\%$$

Step No. 23 Calculate the core loss, Pfe.

Pfe =
$$12KW/m^3$$
 at $100Khz~0.06T$ from [1]
PQ40/40 effective volume $20500mm^3$
Pfe = $0.246W$

Step No. 24 Calculate the total loss, Ptotal.

Step No. 25 Calculate the watts per unit area, Ÿ

$$\psi = \frac{P_{\Sigma}}{A_t}$$
, [watts / cm²]

Step No. 26 Calculate the temperature rise, Tr.

$$T_r = 450 (\psi)^{(0.826)}, [^{\circ}C]$$

$$Tr=39.22$$

Step No. 27 Calculate the total window utilization, Ku

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Work Done (Yapılan İş) Simulation Results		Date (Tarih)	12/08/2020
DC-DC Full Bridge		L2 -nvin	<u></u>

Figure 2: 1Kw 310dc to 110v dc full bride converter

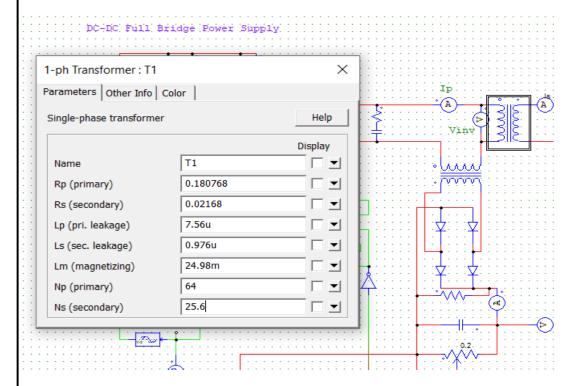
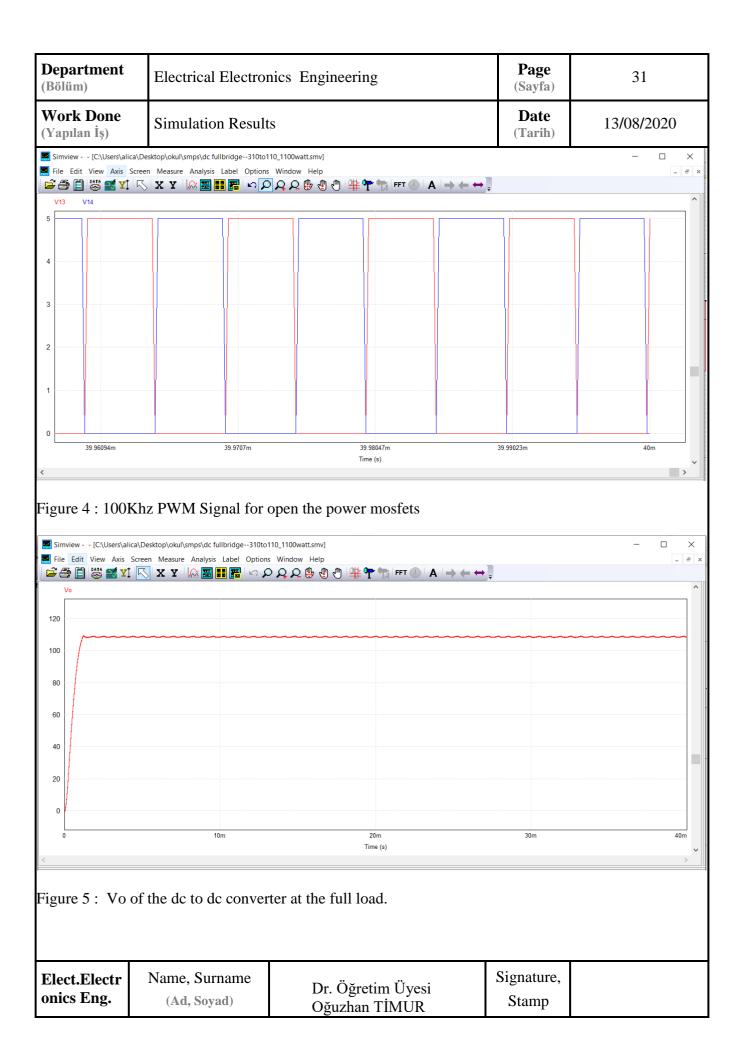
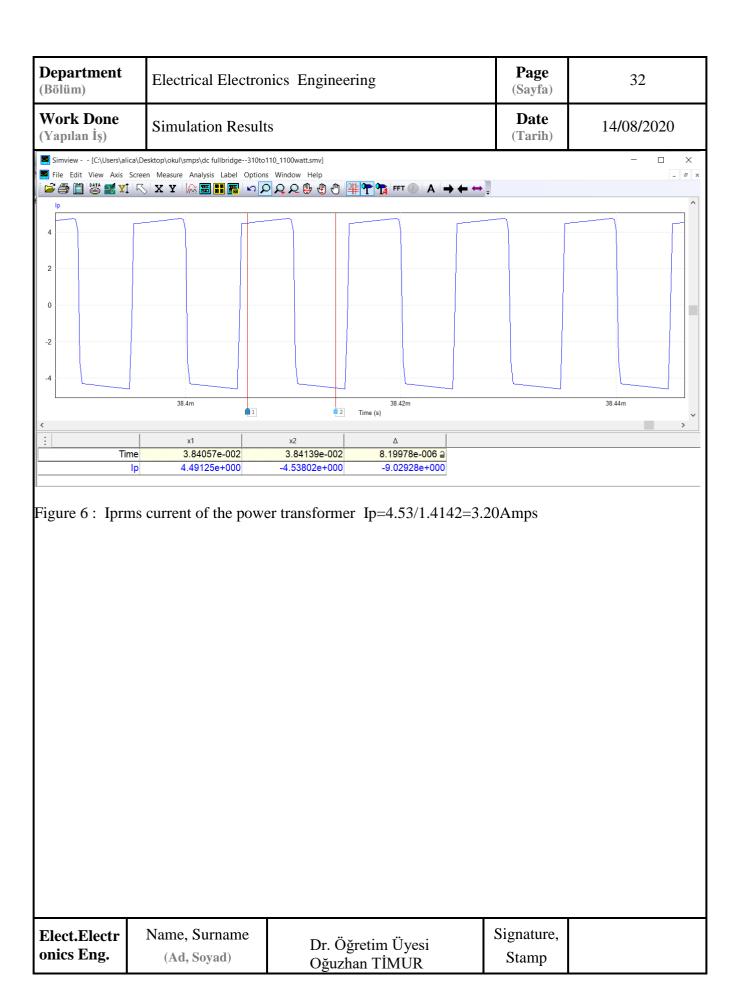


Figure 3: 1Kw %98efficiency power transformer data

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7.	CULLU	IUSIUII

I designed 1Kw power transformer that have %98 effciency. I simulated the PSIM software. and I calculated Rp,Rs,Lm,Ns,Np,Leakage inductance by using books and web sites at the references.

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[4] Transformer and Inductor Design Handbook

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