

## Intro to Computer System :: Project 3 :: Sequential Chips

Student name: \_\_\_\_\_

**Grading method:** The implementation of some chips was described in the book, and some chips are simpler than others. The different weights assigned to the chips below reflect this variance. If the chip passes *all* the tests specified in the supplied test script, it receives two thirds of its allotted points. The remaining third reflects our evaluation of the way the chip is built.

Generally speaking, we prefer implementations that *use as few chip parts as possible*, even if it implies a less efficient chip design.

Packaging		5 / 5	Directory (folder) with your name on it, containing only .hdl files and a README with comments on things that don't work and how you tried to fix them
<b>Chip</b>	<b>Working?</b>	<b>Well built?</b>	<b>Comments</b>
Bit	7 / 7	3 / 3	
Register	7 / 7	3 / 3	
RAM8	13 / 13	6 / 6	
RAM64	13 / 13	6 / 6	
RAM512	5 / 5	3 / 3	
RAM4K	5 / 5	3 / 3	
RAM16K	5 / 5	3 / 3	
PC	12 / 12	6 / 6	
Total	67 / 67	33 / 33	

Total grade: \_\_\_\_\_