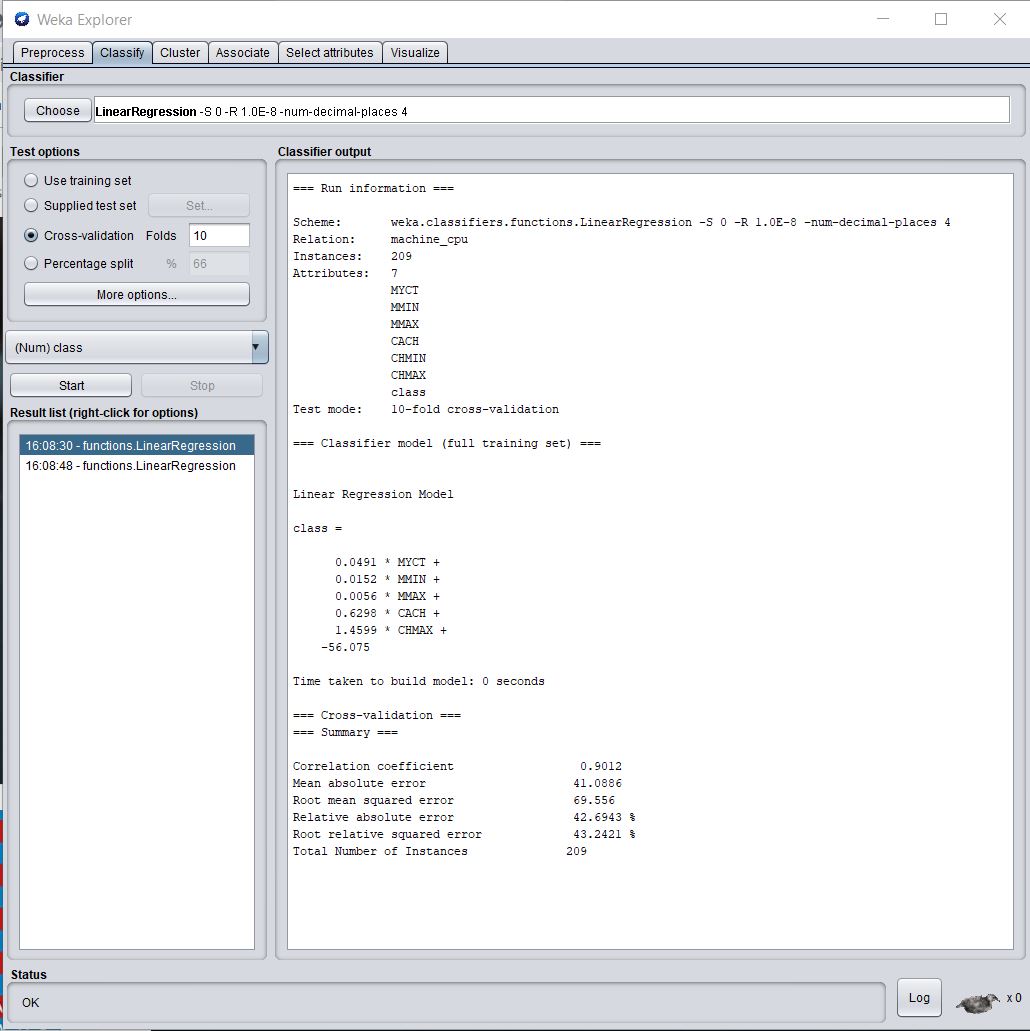
Alan Chen

ECE 331 Project 2

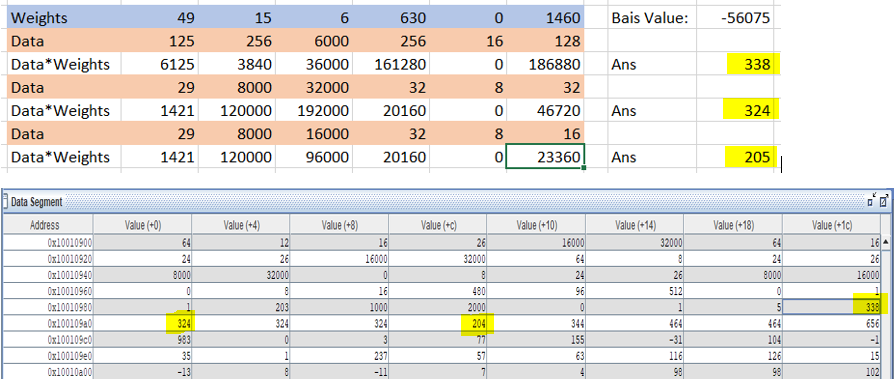
Machine Learning (Linear Regression), Matrix Multiplication, and Cache Simulation in MIPS assembly language

11/22/2019

**WEKA OUTPUT (for weights and bias value)**

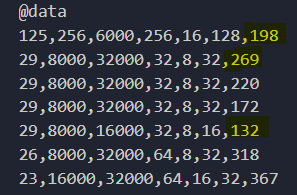
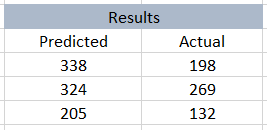


**MIPS Results vs Hand Calculated Results**

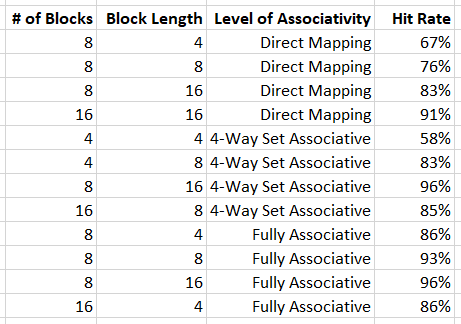
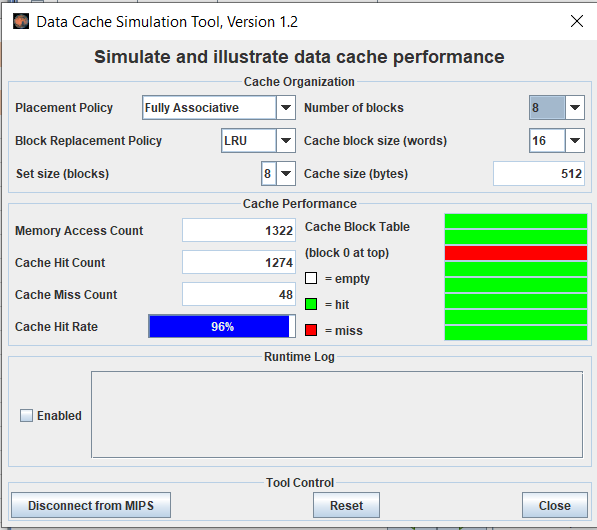


There was one error introduced by our fixed point approximation (204 vs 205) because for hand counted result, I rounded 204.98 to 205 whereas in MIPS, I just ignore the remainder.

**MIPS(Predicted) Results vs Actual Results**



The linear regression function use for predictions are not that accurate. I think this is because the points that I have chosen are far from the line of best fit, since linear regression considers all values.

**Optimizing Cache Performance Using Cache Simulator**

Fully Associative would have the best cache performance for level of associativity because you can put data anywhere within the cache whereas in Direct Mapping and 4-Way Set Associativity, you can only put data in certain blocks. Also the longer the block length is, the better the cache hit rate will be. This is because the you are bringing in more data from main memory which will increase the hit rate. From a bit level perspective, longer block length will increase the offset bit which will decrease the tag bits, therefore the tag bits will more likely be the same within the same block.