Anthony Chen

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Education

University of California, San Diego, B.S. Computer Science — Jun 2024

- GPA: 3.9, *Magna Cum Laude*
- **Coursework:** Data Structures & Algorithms, Computer Architecture, Operating Systems, Networks

Experience

Full Stack Engineer — Jun 2024 – Present
Avert Inc — Irvine, CA

- Developed Rust software to control motors over network using TCP/IP protocols
- Built RF communication systems for UDP video transmissions up to 3 km
- Integrating video stream to AWS cloud and frontend React application (in progress)
- Utilized: Rust, NATS.io, AWS, Docker, GStreamer, WebRTC, Git, Raspbian, Arduino, React, Typescript

Software Engineering Intern — Jun 2023 – Sep 2023
Horizon Med Innovation — Laguna Hills, CA

- Introduced filtering techniques to achieve 63% noise reduction from ECG signals
- Designed algorithm to identify R-wave peaks in arrhythmia ECG readings with 94% accuracy
- Accelerated Bluetooth client app performance by 9% through runtime optimizations
- Utilized: C#, .Net, Kotlin, Java, Gradle, DSP, Bluetooth LE protocol, MATLAB

Data Analyst Intern — Jun 2022 – Sep 2022
Southern California Edison — Pomona, CA

- Applied ML & NLP techniques to filter large datasets for relevant work orders
- Refined pipeline to identify key notification entries using TF-IDF for K-means classification
- Developed a cost budgeting program with 80% accuracy by training statistical models for time-series analysis
- Utilized: Python, PyTorch, SAS, Machine Learning, NLP, Forecasting

Machine Learning Intern — Jun 2019 – Aug 2019
Delta Micro Tech — Lake Forest, CA

- Filtered over 2000 data points for training and testing datasets for facial recognition
- Collaborated with senior engineers to produce facial recognition software, reaching 83% accuracy
- Utilized: C++, Python, Neural Networks, Deep Learning, Feature Extraction

Projects

Expense Tracker

- Directed group of 10 in web-app development by leading meetings, assigning roles, and partitioning tasks
- Automated linting, unit testing, and deployment, decreasing refactor/debug related pull requests by 33%
- Utilized: JavaScript, HTML/CSS, indexedDB, ESLint, Jest, Codacy, Figma, CI/CD, GitHub, Agile, Scrum

Hamming Parity Calculator

- Created RISC-like 9-bit instruction set architecture and designed single-cycle, load-store processor hardware
- Customized assembler to convert assembly into machine code
- Utilized SystemVerilog, Modelsim, Quartus, FPGA design, Python

Technologies

Languages: C, C#, C++, Java, Python, Kotlin, Rust, JavaScript, TS, SQL, CSS, HTML
Technologies: Git, .NET, AWS, NodeJS, React, Docker, Bash, Unix, Jest, Vercel, NATS