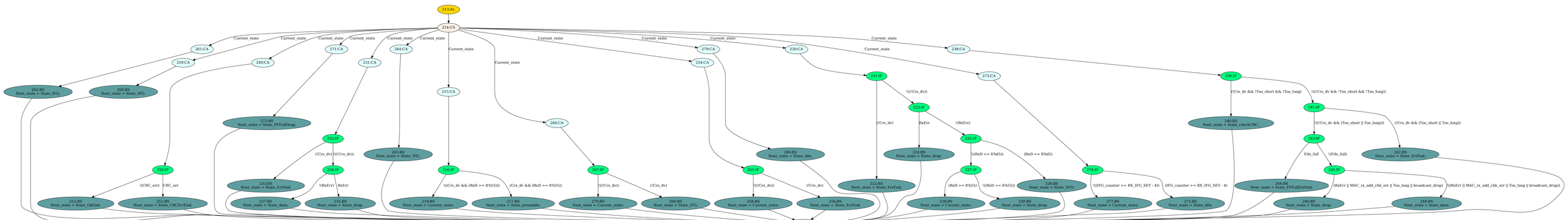
213:AL
214:CS
Current_state
261:CA
259:CA
249:CA
271:CA
231:CA
264:CA
215:CA
266:CA
254:CA
279:CA
220:CA
221:IF
!(!Crs_dv)
223:IF
!(Crs_dv)
RxErr
!(RxErr)
224:BS
Next_state = State_drop;
225:IF
!((RxD == 8'hd5))
(RxD == 8'hd5)
227:IF
(RxD == 8'h55)
!((RxD == 8'h55))
228:BS
Next_state = Current_state;
230:BS
Next_state = State_drop;
226:BS
Next_state = State_SFD;
222:BS
Next_state = State_ErrEnd;
273:CA
274:IF
!((IFG_counter == RX_IFG_SET - 4))
(IFG_counter == RX_IFG_SET - 4)
277:BS
Next_state = Current_state;
275:BS
Next_state = State_idle;
238:CA
239:IF
!Crs_dv && !Too_short && !Too_long)
!(!Crs_dv && !Too_short && !Too_long))
240:BS
Next_state = State_checkCRC;
241:IF
!(!(Crs_dv && (Too_short || Too_long)))
(!Crs_dv && (Too_short || Too_long))
242:BS
Next_state = State_ErrEnd;
243:IF
!Fifo_full
!(Fifo_full)
244:BS
Next_state = State_FFFullErrEnd;
245:IF
(RxErr || MAC_rx_add_chk_err || Too_long || broadcast_drop)
!(RxErr || MAC_rx_add_chk_err || Too_long || broadcast_drop))
246:BS
Next_state = State_drop;
248:BS
Next_state = State_data;
262:BS
Next_state = State_IFG;
260:BS
Next_state = State_IFG;
272:BS
Next_state = State_FFFullDrop;
232:IF
!(Crs_dv)
!(!Crs_dv)
234:IF
!(RxErr)
RxErr
250:IF
!(CRC_err)
CRC_err
233:BS
Next_state = State_ErrEnd;
253:BS
Next_state = State_OkEnd;
251:BS
Next_state = State_CRCErrEnd;
237:BS
Next_state = State_data;
235:BS
Next_state = State_drop;
265:BS
Next_state = State_IFG;
216:IF
!((Crs_dv && (RxD == 8'h55)))
(Crs_dv && (RxD == 8'h55))
219:BS
Next_state = Current_state;
217:BS
Next_state = State_preamble;
267:IF
!(!Crs_dv)
(!Crs_dv)
270:BS
Next_state = Current_state;
268:BS
Next_state = State_IFG;
280:BS
Next_state = State_idle;
255:IF
!(!Crs_dv)
(!Crs_dv)
258:BS
Next_state = Current_state;
256:BS
Next_state = State_ErrEnd;
Leaf_213:AL