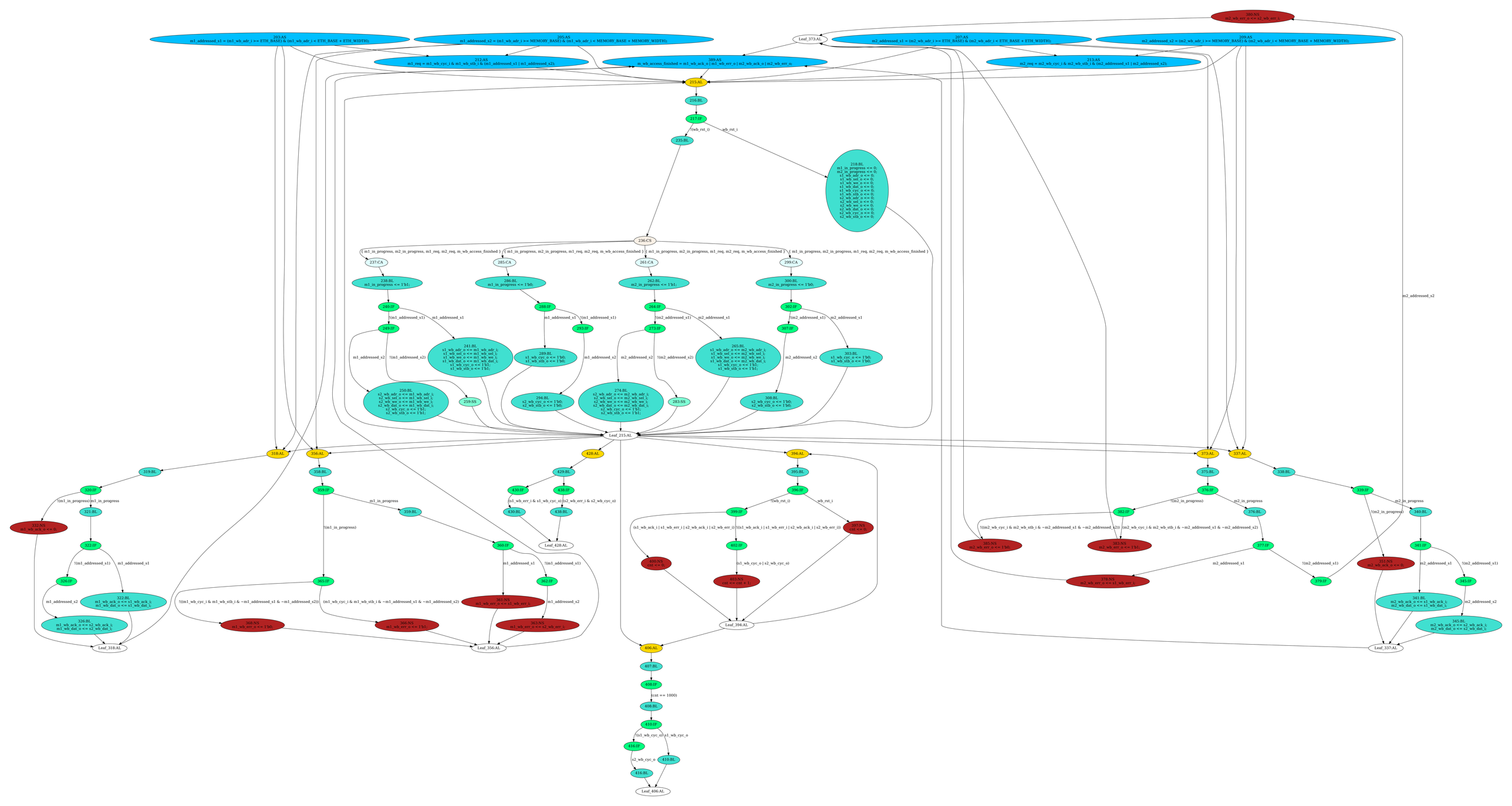
390:NS
m2_wb_err_o <= s2_wb_err_i;
Leaf_373:AL
203:AS
m1_addressed_s1 = (m1_wb_adr_i >= ETH_BASE) & (m1_wb_adr_i < ETH_BASE + ETH_WIDTH);
205:AS
m1_addressed_s2 = (m1_wb_adr_i >= MEMORY_BASE) & (m1_wb_adr_i < MEMORY_BASE + MEMORY_WIDTH);
207:AS
m2_addressed_s1 = (m2_wb_adr_i >= ETH_BASE) & (m2_wb_adr_i < ETH_BASE + ETH_WIDTH);
209:AS
m2_addressed_s2 = (m2_wb_adr_i >= MEMORY_BASE) & (m2_wb_adr_i < MEMORY_BASE + MEMORY_WIDTH);
212:AS
m1_req = m1_wb_cyc_i & m1_wb_stb_i & (m1_addressed_s1 | m1_addressed_s2);
389:AS
m_wb_access_finished = m1_wb_ack_o | m1_wb_err_o | m2_wb_ack_o | m2_wb_err_o;
211:AS
m2_req = m2_wb_cyc_i & m2_wb_stb_i & (m2_addressed_s1 | m2_addressed_s2);
215:AL
216:BL
217:IF
!(wb_rst_i)
wb_rst_i
235:BL
218:BL
m1_in_progress <= 0;
m2_in_progress <= 0;
s1_wb_adr_o <= 0;
s1_wb_sel_o <= 0;
s1_wb_we_o <= 0;
s1_wb_dat_o <= 0;
s1_wb_cyc_o <= 0;
s1_wb_stb_o <= 0;
s2_wb_adr_o <= 0;
s2_wb_sel_o <= 0;
s2_wb_we_o <= 0;
s2_wb_dat_o <= 0;
s2_wb_cyc_o <= 0;
s2_wb_stb_o <= 0;
236:CS
{ m1_in_progress, m2_in_progress, m1_req, m2_req, m_wb_access_finished }
237:CA
238:BL
m1_in_progress <= 1'b1;
240:IF
!(m1_addressed_s1)
m1_addressed_s1
249:IF
m1_addressed_s2
!(m1_addressed_s2)
241:BL
s1_wb_adr_o <= m1_wb_adr_i;
s1_wb_sel_o <= m1_wb_sel_i;
s1_wb_we_o <= m1_wb_we_i;
s1_wb_dat_o <= m1_wb_dat_i;
s1_wb_cyc_o <= 1'b1;
s1_wb_stb_o <= 1'b1;
250:BL
s2_wb_adr_o <= m1_wb_adr_i;
s2_wb_sel_o <= m1_wb_sel_i;
s2_wb_we_o <= m1_wb_we_i;
s2_wb_dat_o <= m1_wb_dat_i;
s2_wb_cyc_o <= 1'b1;
s2_wb_stb_o <= 1'b1;
259:SS
285:CA
286:BL
m1_in_progress <= 1'b0;
288:IF
m1_addressed_s1
!(m1_addressed_s1)
293:IF
289:BL
s1_wb_cyc_o <= 1'b0;
s1_wb_stb_o <= 1'b0;
294:BL
s2_wb_cyc_o <= 1'b0;
s2_wb_stb_o <= 1'b0;
261:CA
262:BL
m2_in_progress <= 1'b1;
264:IF
!(m2_addressed_s1)
m2_addressed_s1
273:IF
m2_addressed_s2
!(m2_addressed_s2)
265:BL
s1_wb_adr_o <= m2_wb_adr_i;
s1_wb_sel_o <= m2_wb_sel_i;
s1_wb_we_o <= m2_wb_we_i;
s1_wb_dat_o <= m2_wb_dat_i;
s1_wb_cyc_o <= 1'b1;
s1_wb_stb_o <= 1'b1;
274:BL
s2_wb_adr_o <= m2_wb_adr_i;
s2_wb_sel_o <= m2_wb_sel_i;
s2_wb_we_o <= m2_wb_we_i;
s2_wb_dat_o <= m2_wb_dat_i;
s2_wb_cyc_o <= 1'b1;
s2_wb_stb_o <= 1'b1;
283:SS
299:CA
300:BL
m2_in_progress <= 1'b0;
302:IF
!(m2_addressed_s1)
m2_addressed_s1
307:IF
m2_addressed_s2
303:BL
s1_wb_cyc_o <= 1'b0;
s1_wb_stb_o <= 1'b0;
308:BL
s2_wb_cyc_o <= 1'b0;
s2_wb_stb_o <= 1'b0;
Leaf_215:AL
318:AL
356:AL
428:AL
394:AL
373:AL
337:AL
319:BL
320:IF
!(m1_in_progress)
m1_in_progress
321:BL
322:IF
!(m1_addressed_s1)
m1_addressed_s1
326:IF
m1_addressed_s2
322:BL
m1_wb_ack_o <= s1_wb_ack_i;
m1_wb_dat_o <= s1_wb_dat_i;
326:BL
m1_wb_ack_o <= s2_wb_ack_i;
m1_wb_dat_o <= s2_wb_dat_i;
332:NS
m1_wb_ack_o <= 0;
Leaf_318:AL
358:BL
359:IF
m1_in_progress
!(m1_in_progress)
359:BL
360:IF
m1_addressed_s1
!(m1_addressed_s1)
361:IF
!((m1_wb_cyc_i & m1_wb_stb_i & ~m1_addressed_s1 & ~m1_addressed_s2))
(m1_wb_cyc_i & m1_wb_stb_i & ~m1_addressed_s1 & ~m1_addressed_s2)
362:IF
m1_addressed_s2
368:NS
m1_wb_err_o <= 1'b0;
366:NS
m1_wb_err_o <= 1'b1;
369:NS
m1_wb_err_o <= s1_wb_err_i;
363:NS
m1_wb_err_o <= s2_wb_err_i;
Leaf_356:AL
429:BL
430:IF
438:IF
(s1_wb_err_i & s1_wb_cyc_o)
(s2_wb_err_i & s2_wb_cyc_o)
430:BL
438:BL
Leaf_428:AL
395:BL
396:IF
!(wb_rst_i)
wb_rst_i
399:IF
(s1_wb_ack_i | s1_wb_err_i | s2_wb_ack_i | s2_wb_err_i)
!(s1_wb_ack_i | s1_wb_err_i | s2_wb_ack_i | s2_wb_err_i)
397:NS
cnt <= 0;
400:NS
cnt <= 0;
402:IF
(s1_wb_cyc_o | s2_wb_cyc_o)
403:NS
cnt <= cnt + 1;
Leaf_394:AL
406:AL
407:BL
408:IF
(cnt == 1000)
408:BL
410:IF
!(s1_wb_cyc_o)
s1_wb_cyc_o
416:IF
s2_wb_cyc_o
410:BL
416:BL
Leaf_406:AL
375:BL
376:IF
!(m2_in_progress)
m2_in_progress
382:IF
!((m2_wb_cyc_i & m2_wb_stb_i & ~m2_addressed_s1 & ~m2_addressed_s2))
(m2_wb_cyc_i & m2_wb_stb_i & ~m2_addressed_s1 & ~m2_addressed_s2)
376:BL
385:NS
m2_wb_err_o <= 1'b0;
383:NS
m2_wb_err_o <= 1'b1;
377:IF
m2_addressed_s1
!(m2_addressed_s1)
178:NS
m2_wb_err_o <= s1_wb_err_i;
379:IF
338:BL
339:IF
!(m2_in_progress)
m2_in_progress
340:BL
351:NS
m2_wb_ack_o <= 0;
341:IF
m2_addressed_s1
!(m2_addressed_s1)
343:IF
341:BL
m2_wb_ack_o <= s1_wb_ack_i;
m2_wb_dat_o <= s1_wb_dat_i;
345:BL
m2_wb_ack_o <= s2_wb_ack_i;
m2_wb_dat_o <= s2_wb_dat_i;
m2_addressed_s2
Leaf_337:AL