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Proj\_2B\_watch\_dog\_timer

SNOW STORM DISPLAY: Uses the watch dog timer to prevent the display from getting jammed in a way that sometimes affected earlier programs

PROVIDES MORE ON

1. Random number generation: Project subroutine PRN\_16bit\_GEN (0) uses a number stored in EEPROM to generate another one which it saves in the same EEPROM location. In this way repeated calls to PRN\_16bit\_GEN (0) generate a sequence of random numbers.

Note:

- a. EEPROM memory is not affected by POR or a WD timeout.
- b. PRN\_16bit\_GEN (Num) generates a random number based on Num and it is up to the project to increment Num in some way.

2. The Watch Dog Timer Once this has been set running it will reset the program after a predetermined time interval (250mS in this case) unless it is first reset itself using command wdr().

OPERATION

Pressing sw\_2 calls subroutine ISR(PCINT2\_vect) which contain the statement while(1); This halts program flow because there are no active interrupts at this point and therefore the watch dog timer cannot be reset and will time out.

Note: Interrupts are automatically disabled when program execution enters an ISR. They can however be re-enabled using sei() in which case one ISR can be interrupted by another interrupt.

Note

1. The display pauses but continues in sequence, because the random number generator uses the EEPROM for its data storage.
2. In practice the watch dog delay is made as short as possible without interfering with normal program operation so that any pause is insignificant.
3. The switch is used to allow us to test the operation of the WDT. Normally we only want it to timeout when a real glitch in the system causes the program to crash.