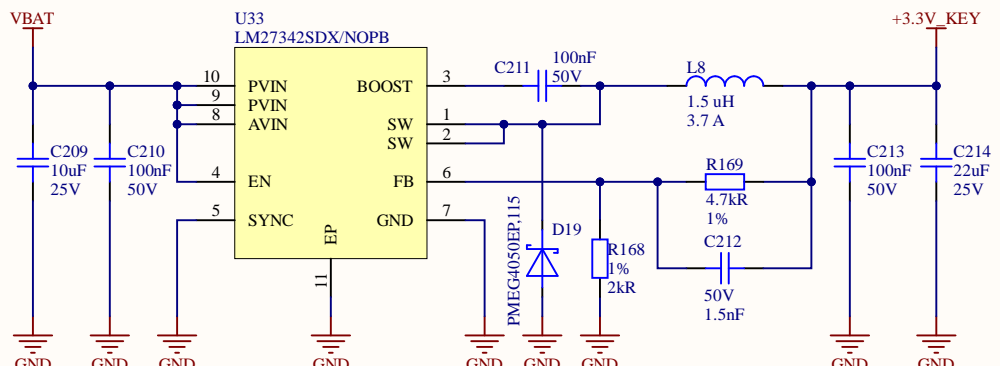
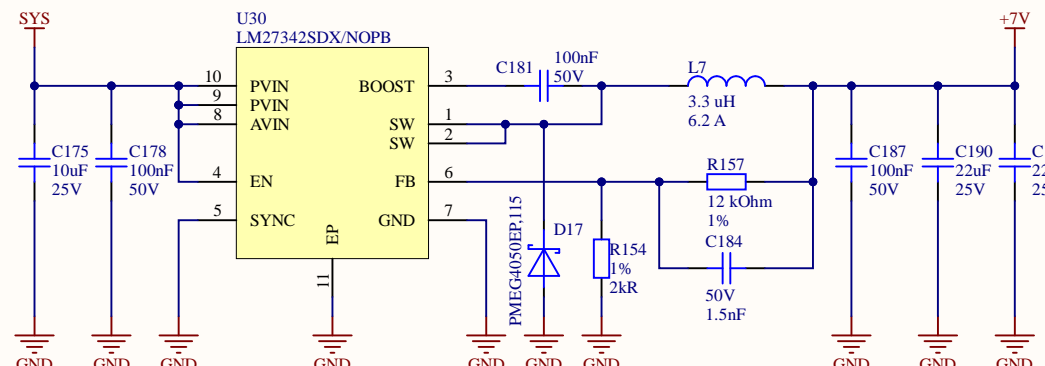
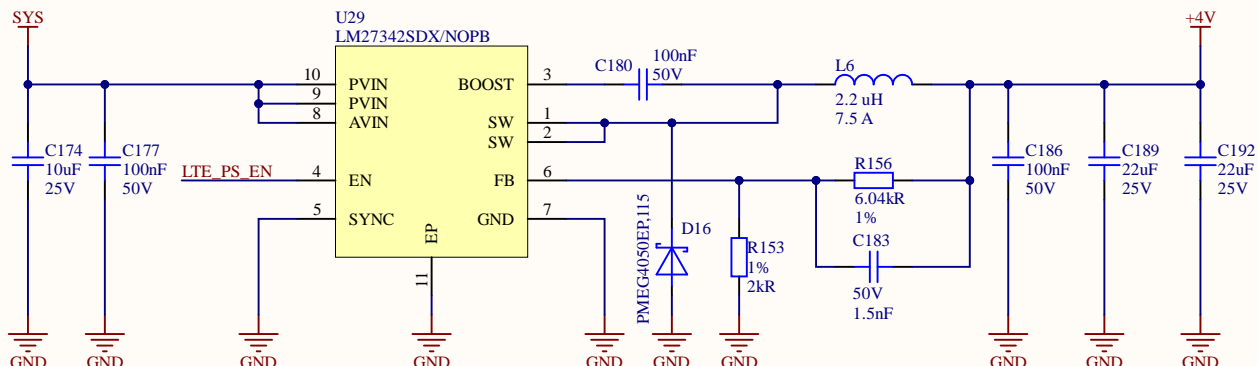
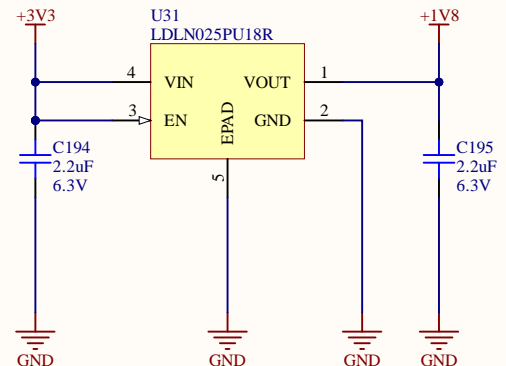
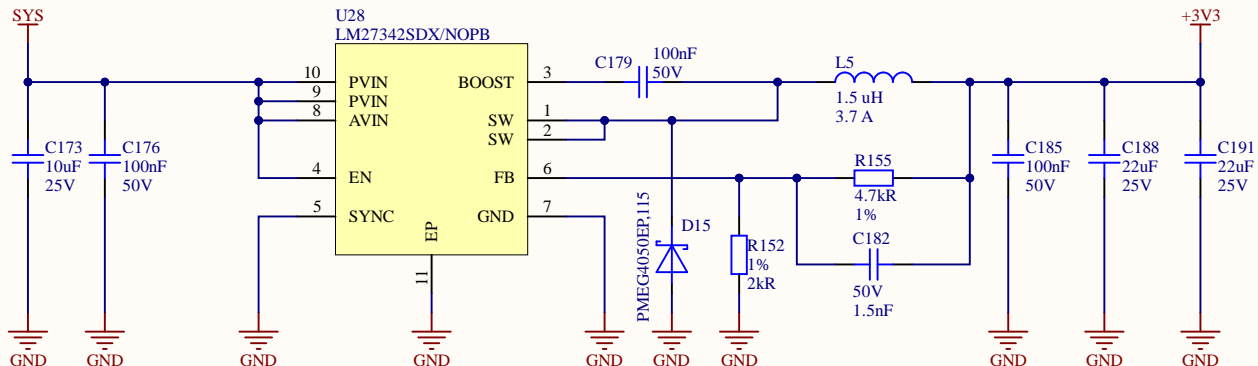
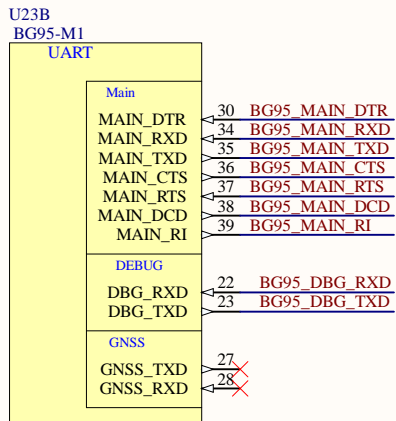
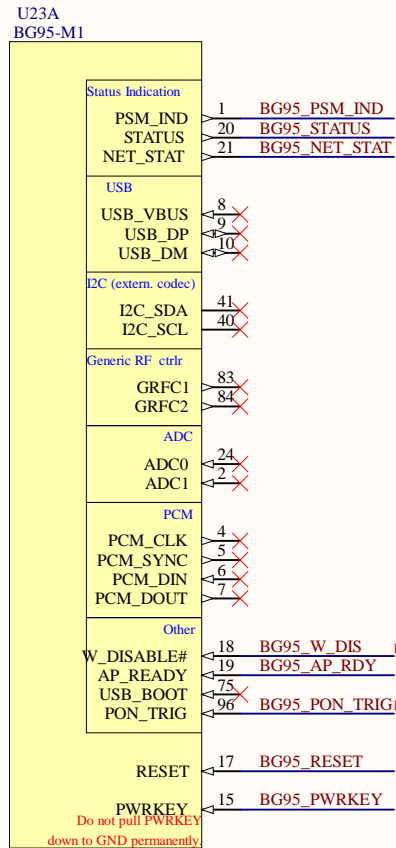


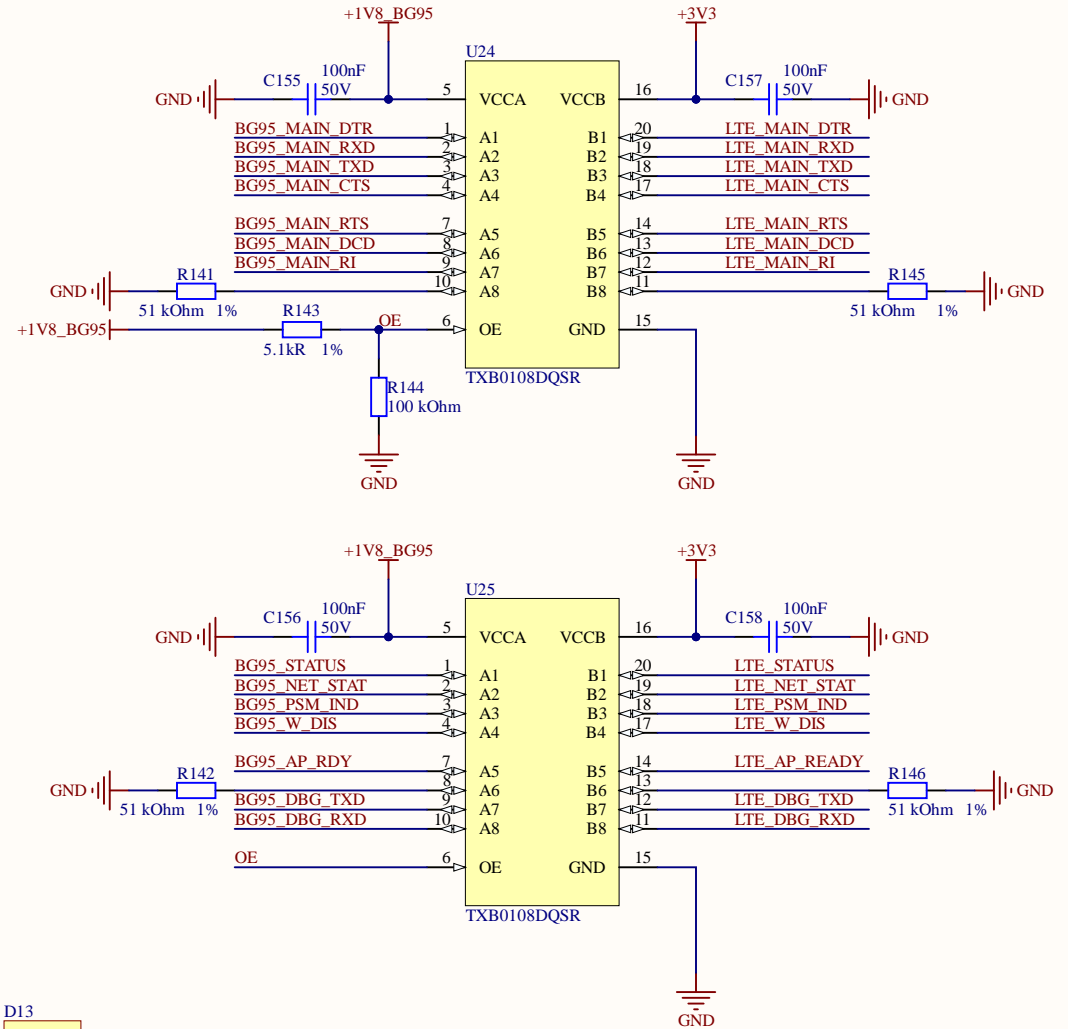
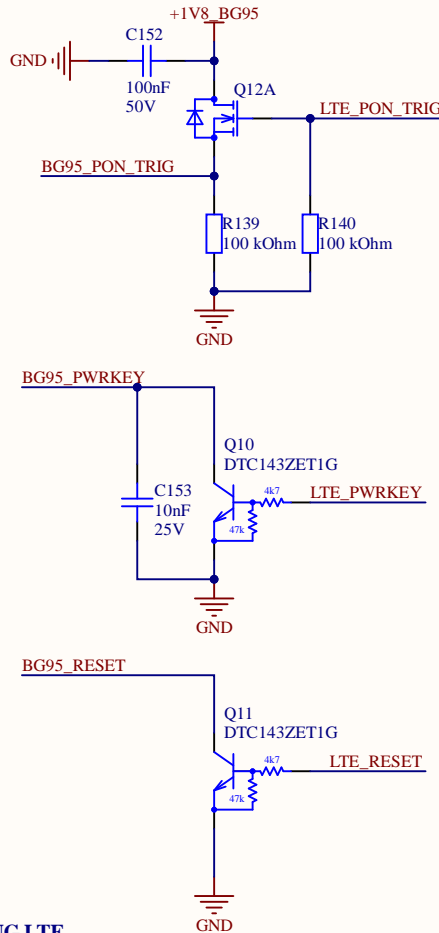
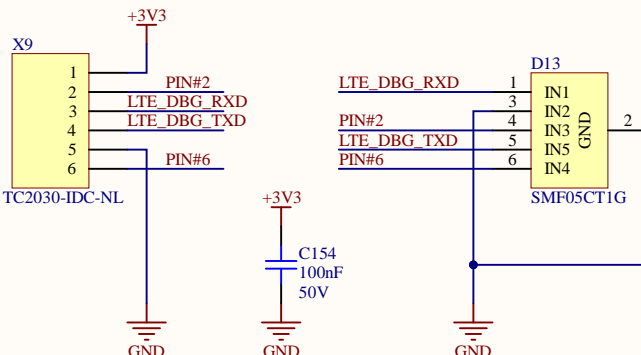
Title 16. Power Disp			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801
Size: A4	Number: 14	Revision: 0.1	
Date: 17.03.2022	Time: 21:10:51	Sheet 14 of 51	
File: 16. Power Disp.SchDoc			



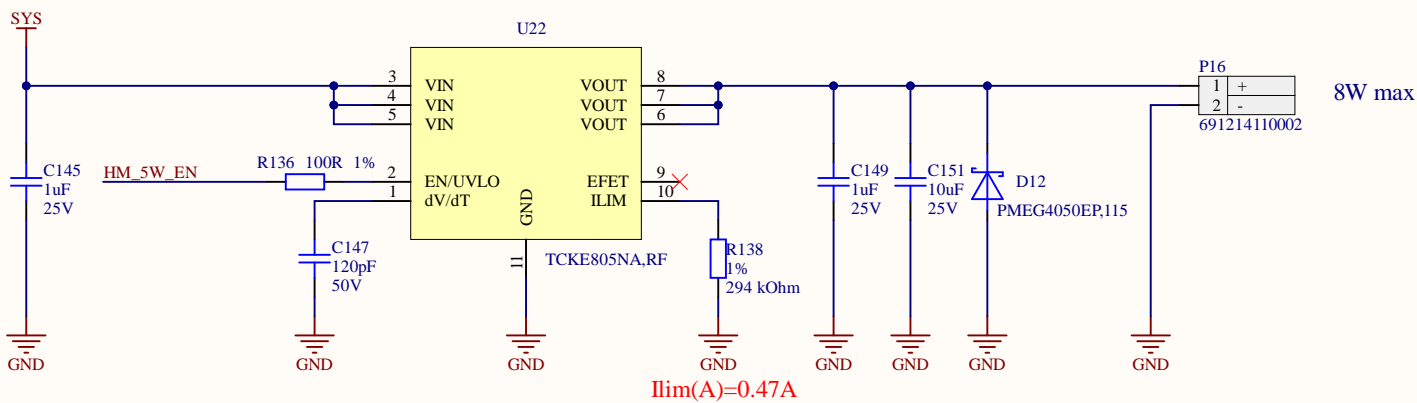
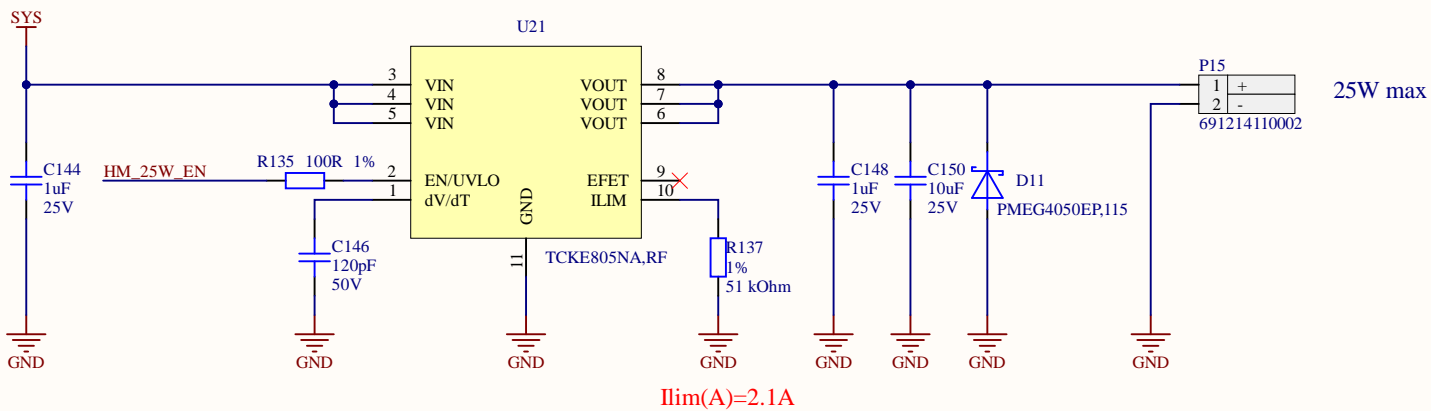
Title 15. Power			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:52	Sheet 14 of 51		
File: 15.Power.SchDoc				



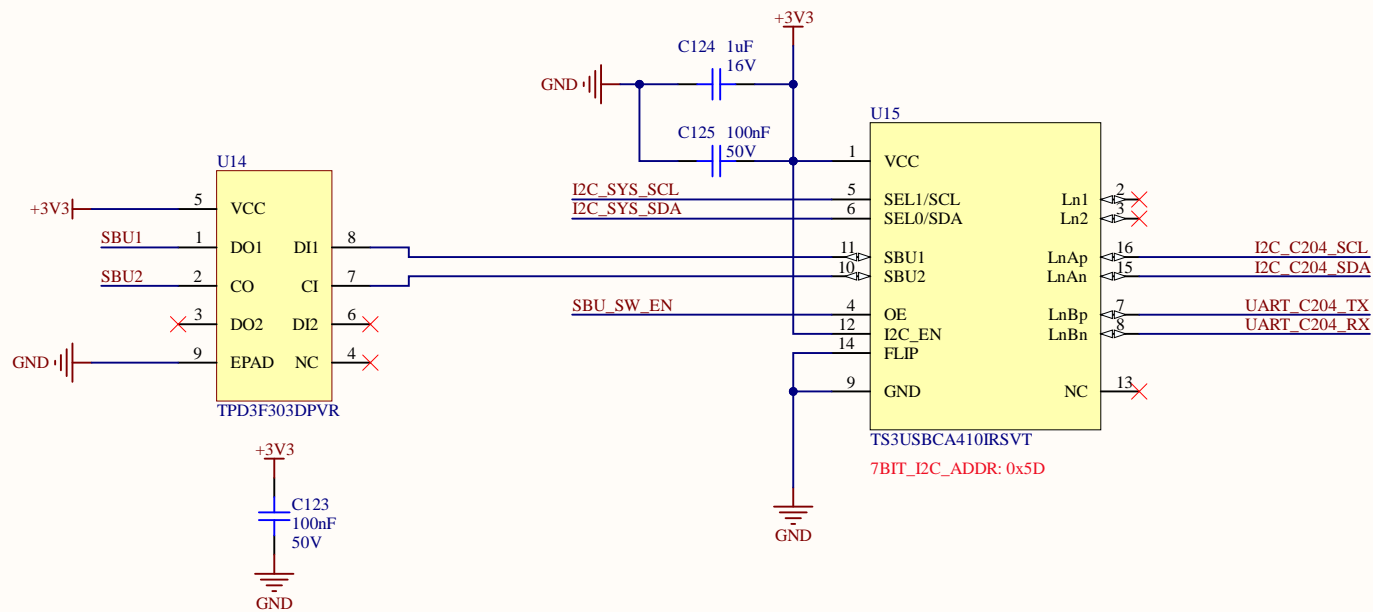
DEBUG LTE



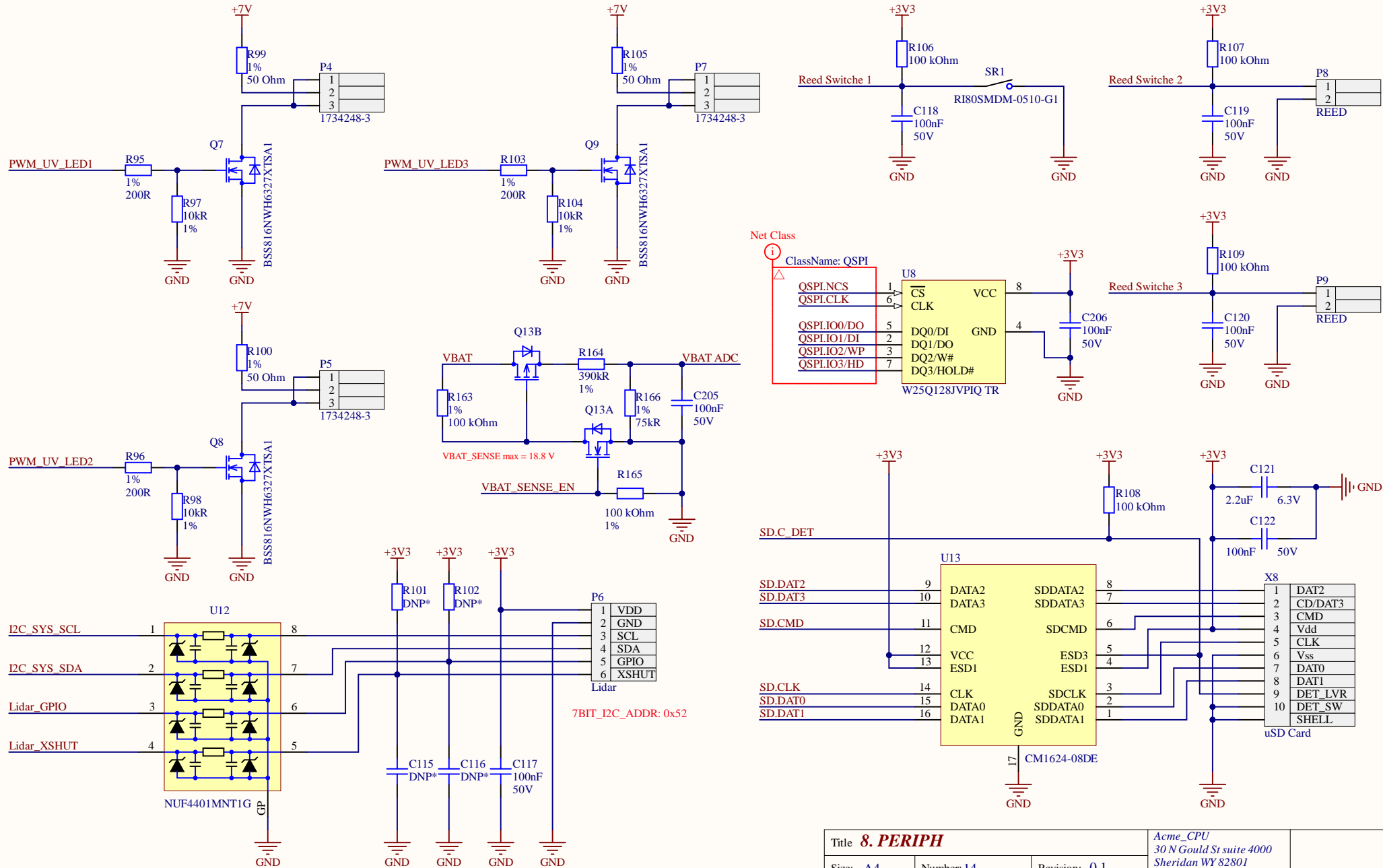
Title 13. LTE			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:52	Sheet 14 of 51		
File: 13.LTE.SchDoc				



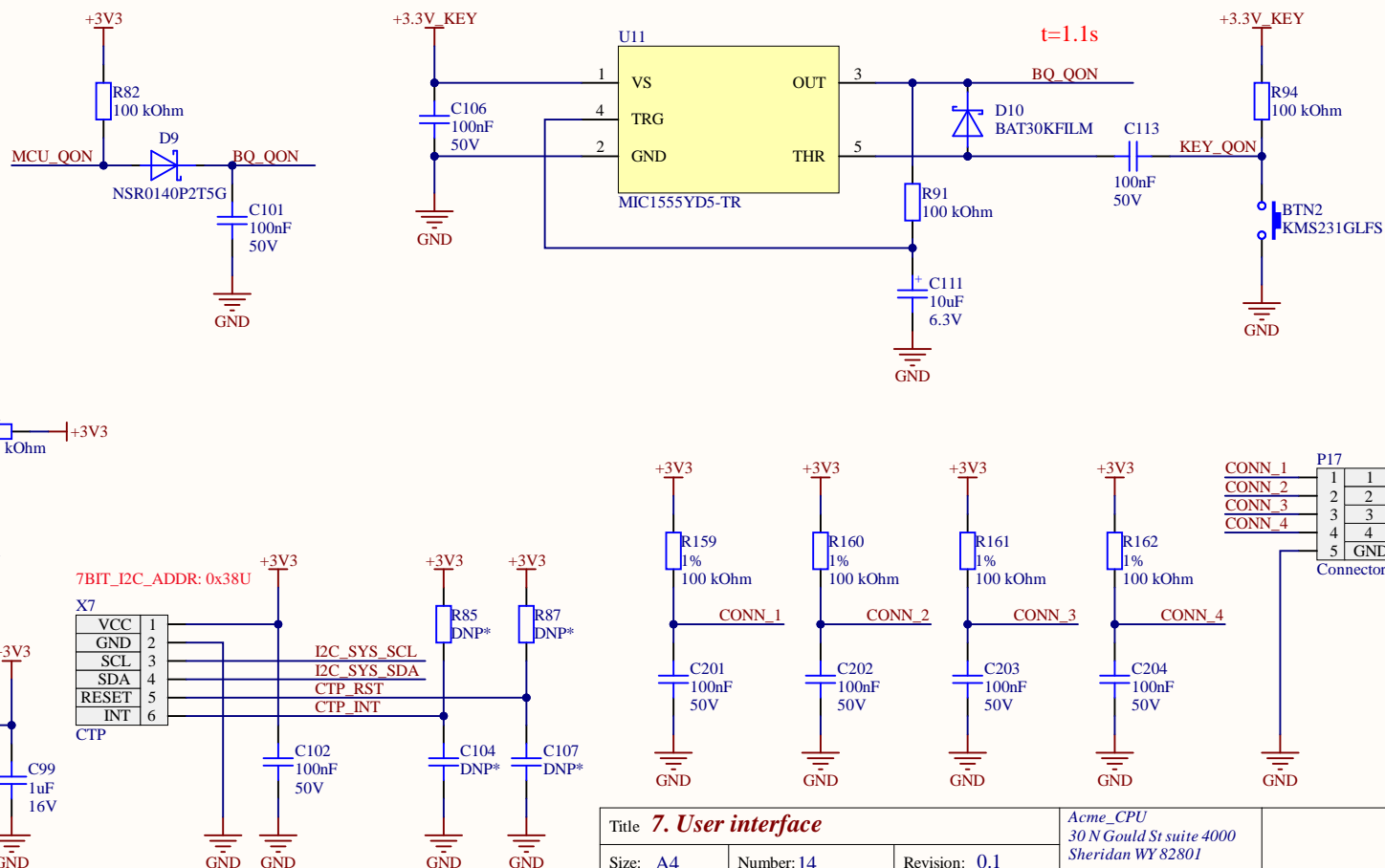
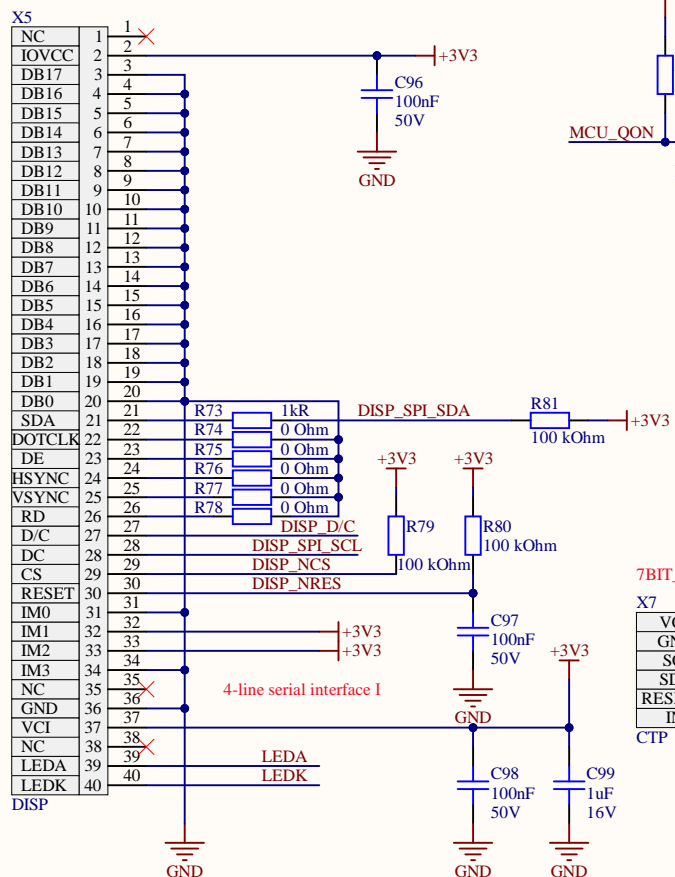
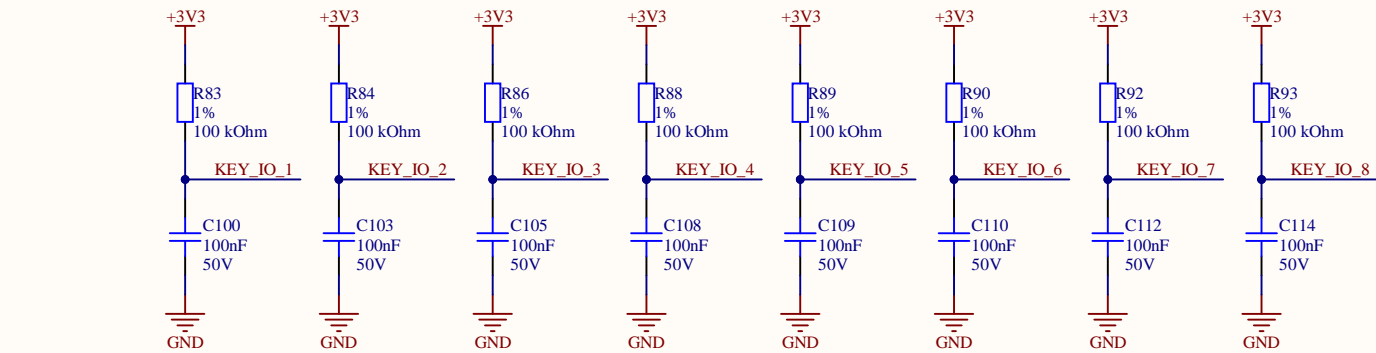
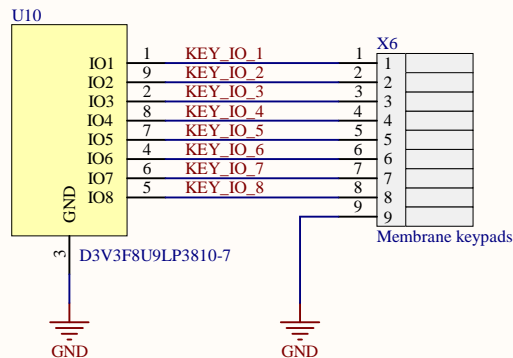
Title 12. Heating modules			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:52	Sheet 14 of 51		
File: 12. Heating modules.SchDoc				



Title 9. MUX			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:53	Sheet 14 of 51		
File: 9. MUX.SchDoc				



Title 8. PERIPH			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:53	Sheet 14 of 51		
File: 8.PERIPH.SchDoc				



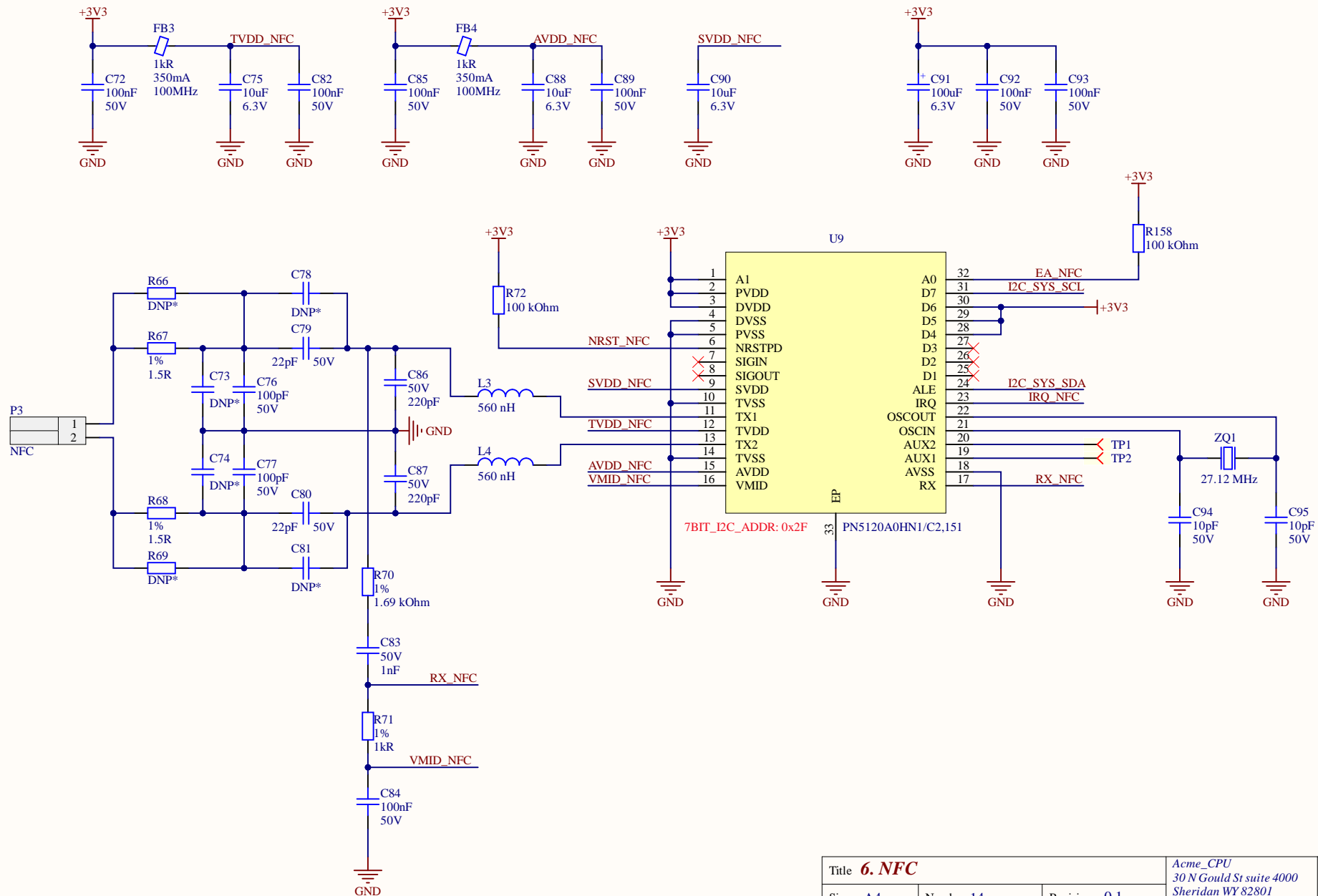
7. User interface

Size: A4 Number: 14 Revision: 0.1

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File: 7. User interface.SchDoc

Acme_CPU
30 N Gould St suite 4000
Sheridan WY 82801



Title 6. NFC			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:53	Sheet 14 of 51		
File: 6. NFC.SchDoc				

A

B

C

D

2

3

4

A

B

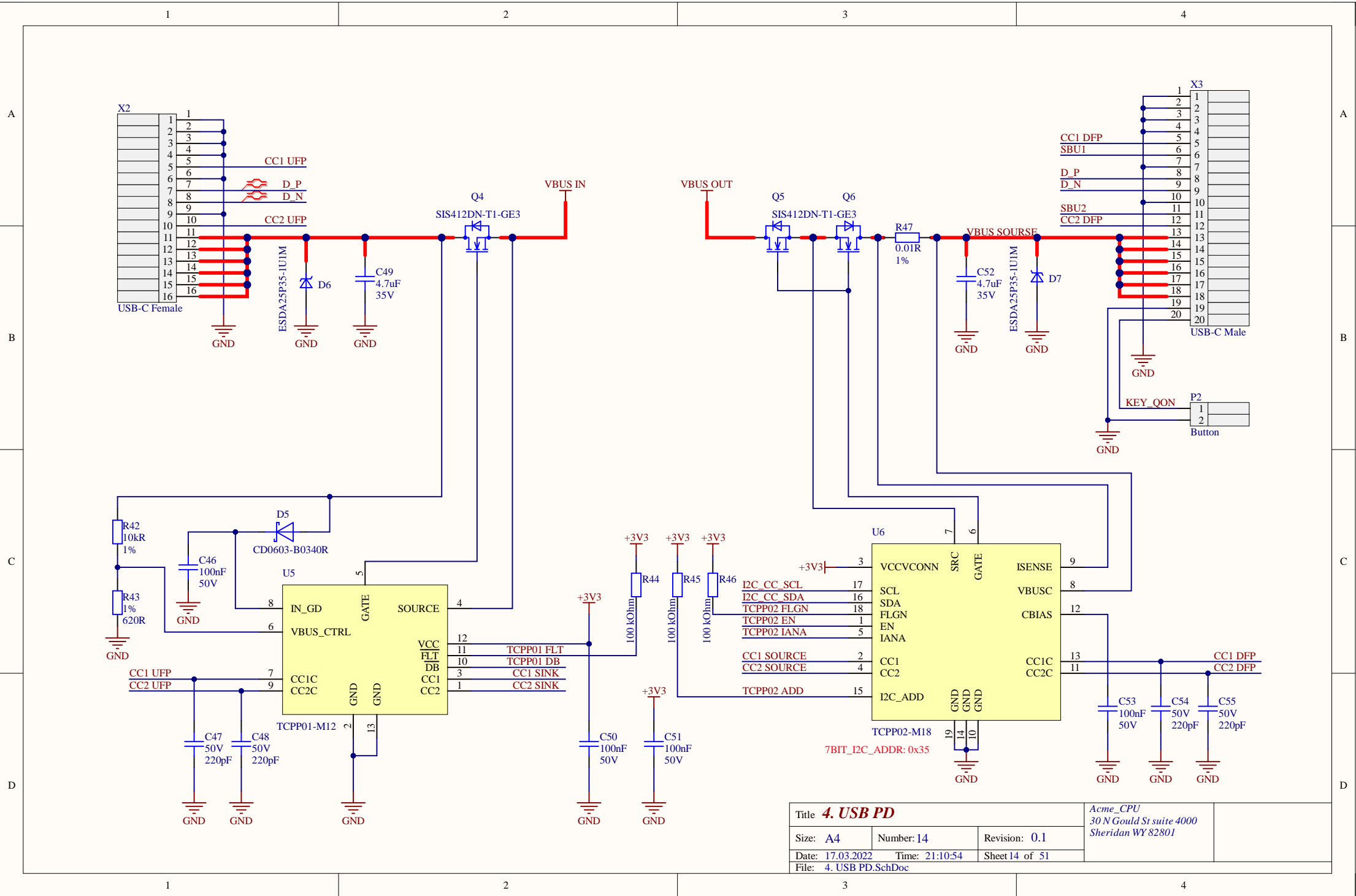
C

D

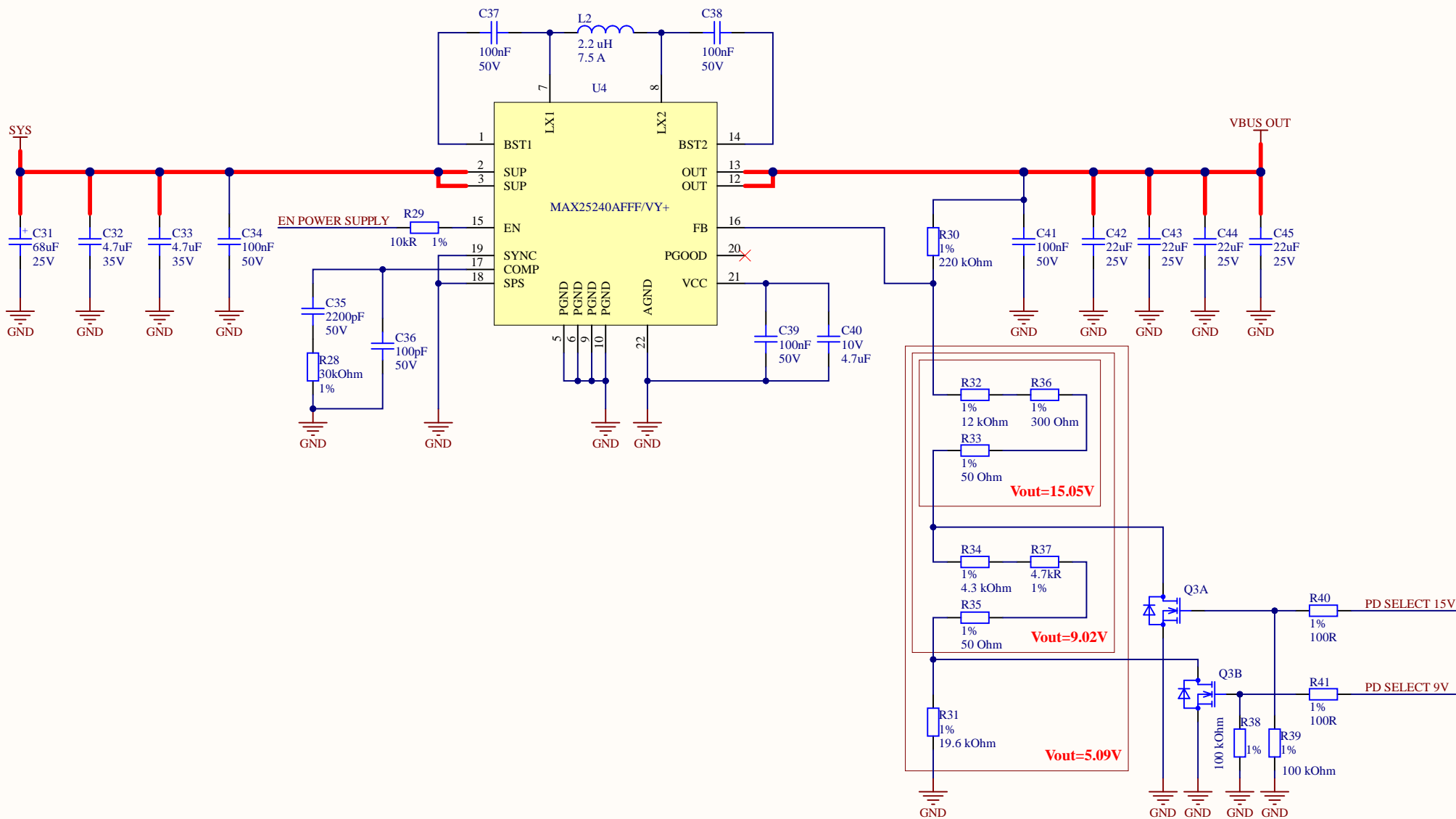
U7A		U7B		
STP_MODE2	1	PE2	142	STP_STCK/MODE3
STP_PWM	2	PE3	141	STP_DIR/MODE4
STP_MODE1	3	PE4	140	STP_STBY/RESET
STP_EN/FAULT	4	PE5	139	
STP_EN	5	PE6	138	BOOT0
MCU_QON	7	PC13	137	
	8	OSC32_IN	136	LTE_PS_EN
	9	OSC32_OUT	135	LTE_PON_TRIG
I2C_TS_SDA	10	PF0	134	LTE_PWRKEY
I2C_TS_SCL	11	PF1	133	LTE_RESET
TS1_INT	12	PF2	132	LTE_PSM_IND
TS2_INT	13	PF3	129	I2C_SYS_SCL
	14	PF4	128	I2C_SYS_SDA
	15	PF5	127	LTE_AP_READY
QSPIIO3/HD	18	PF6	126	LTE_W_DIS
QSPIIO2/WP	19	PF7	125	LTE_MAIN_RI
QSPIIO0/DO	20	PF8	124	LTE_MAIN_DCD
QSPIIO1/DI	21	PF9	123	LTE_MAIN_DTR
QSPICLK	22	PF10	122	LTE_MAIN_TXD
	23	PH0-OSC_IN	119	LTE_MAIN_RXD
	24	PH1-OSC_OUT	118	LTE_MAIN_RTS
NRST	25	NRST	117	LTE_MAIN_CTS
I2C_HS_SCL	26	PC0	116	SD.CMD
I2C_HS_SDA	27	PC1	115	LTE_NET_STAT
HS1_INT	28	PC2	114	LTE_STATUS
HS2_INT	29	PC3	113	SD.CLK
UART_DBG_TX	34	PA0	112	SD.DAT3
UART_DBG_RX	35	PA1	111	SD.DAT2
QSPINCS	36	PA2	110	PWM_LED backlight
DISP_NRES	37	PA3	109	SWCLK
DISP_NCS	40	PA4	105	SWDIO
DISP_SPI_SCL	41	PA5	104	
DISP_D/C	42	PA6	103	
DISP_SPI_SDA	43	PA7	102	UART_C204_RX
UART_MCU_PD_RX	44	PC4	101	UART_C204_TX
UART_MCU_PD_TX	45	PC5	100	SD.C_DET
VBAT_ADC	46	PC6	99	SD.DAT1
VBAT_SENSE_EN	47	PC9	98	SD.DAT0
CTP_INT	48	PC8	97	
CTP_RST	49	PC7	96	
KEY_IO_8	50	PC6	93	ACCEL_INT
KEY_IO_7	53	PC8	92	Lidar GPIO
KEY_IO_6	54	PG7	91	EN POWER SUPPLY
KEY_IO_5	55	PG6	90	PD SELECT 15V
KEY_IO_4	56	PG5	89	PD SELECT 9V
KEY_IO_3	57	PG4	88	Lidar_XSHUT
KEY_IO_2	58	PG3	87	HM_25W_EN
KEY_IO_1	59	PG2	86	BQ_CE
PWM_UV_LED1	60	PD15	85	HM_5W_EN
	63	PD14	82	AL/CC
PWM_UV_LED2	64	PD13	81	BQ_INT
	65	PD12	80	SBU_SW_EN
PWM_UV_LED3	66	PD11	79	IRQ_NFC
CONN_1	67	PD10	78	NRST_NFC
CONN_2	68	PD9	77	EA_NFC
CONN_3	69	PD8	76	Reed Switch 1
CONN_4	70	PB15	75	Reed Switch 2
	73	PB14	74	Reed Switch 3
		PB13		

Signals mapping will be updated at routing stage

Title 5. MCU			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:54	Sheet 14 of 51		
File: 5. MCU.SchDoc				



Title 4. USB PD			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:54	Sheet 14 of 51		
File: 4. USB PD.SchDoc				



Title **3. Power PD**

Size: **A4**

Number: **14**

Revision: **0.1**

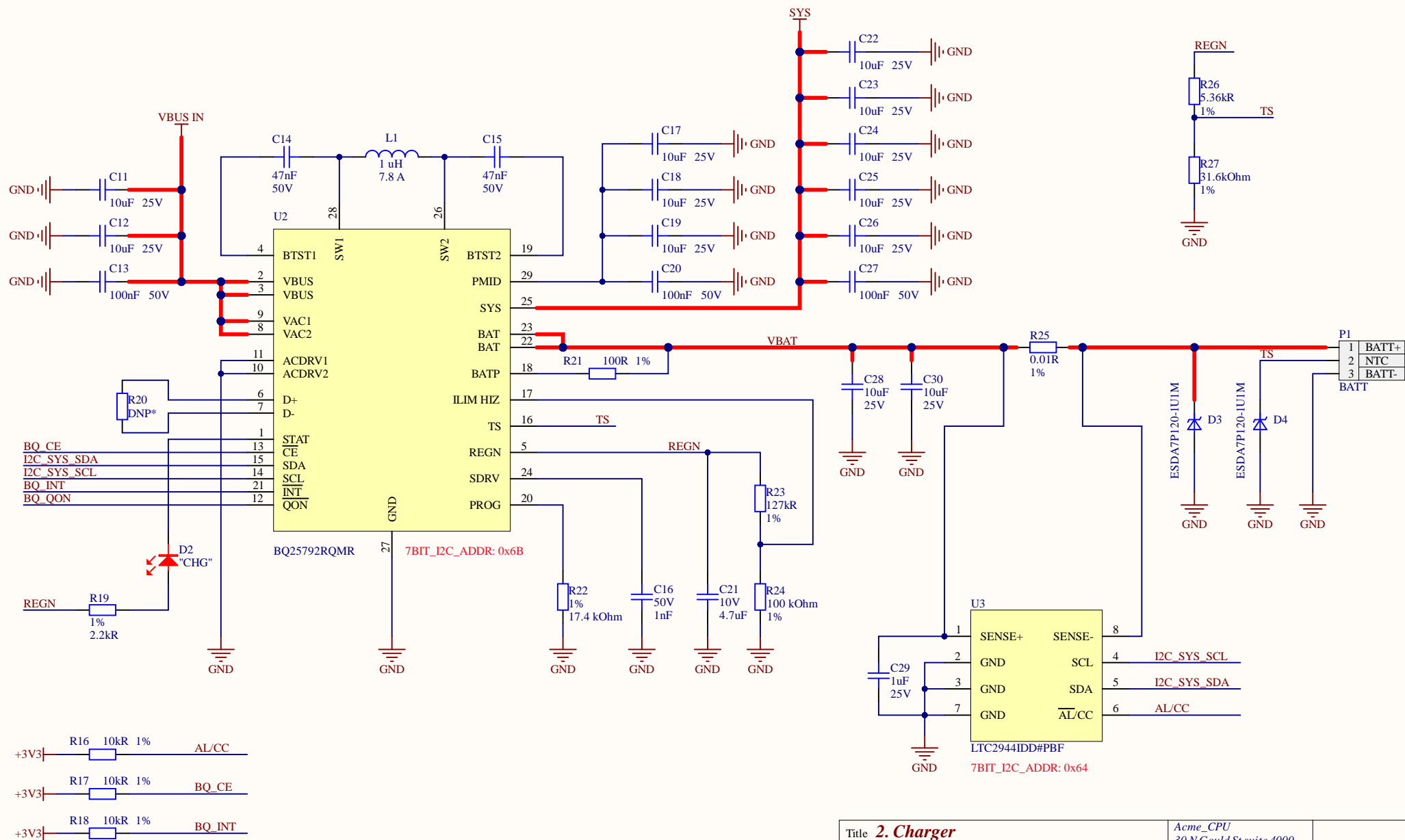
Date: **17.03.2022**

Time: **21:10:54**

Sheet **14** of **51**

File: **3. Power PD.SchDoc**

Acme_CPU
30 N Gould St suite 4000
Sheridan WY 82801



Title 2. Charger			Acme_CPU 30 N Gould St suite 4000 Sheridan WY 82801	
Size: A4	Number: 14	Revision: 0.1		
Date: 17.03.2022	Time: 21:10:54	Sheet 14 of 51		
File: 2. Charger.SchDoc				

