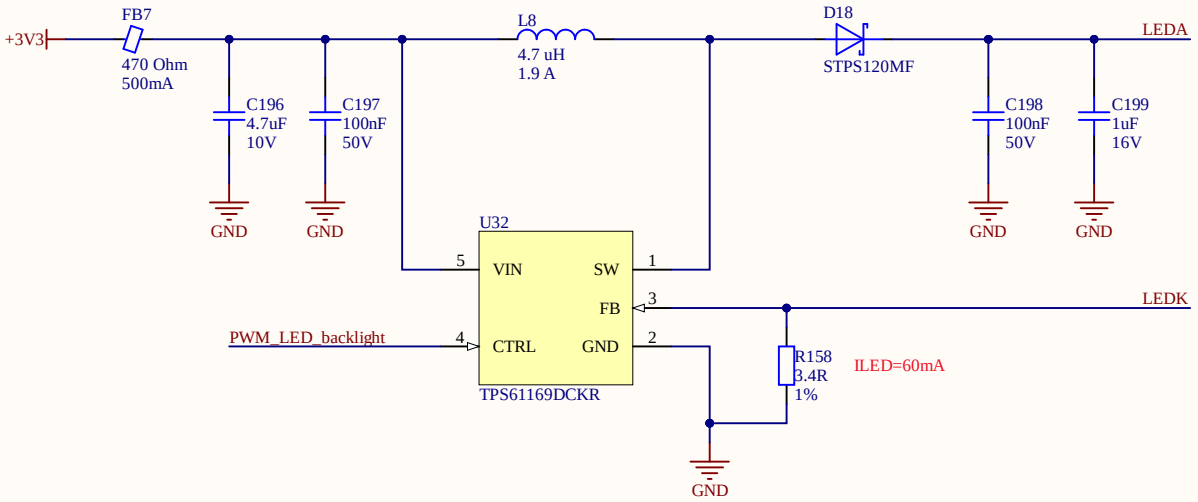
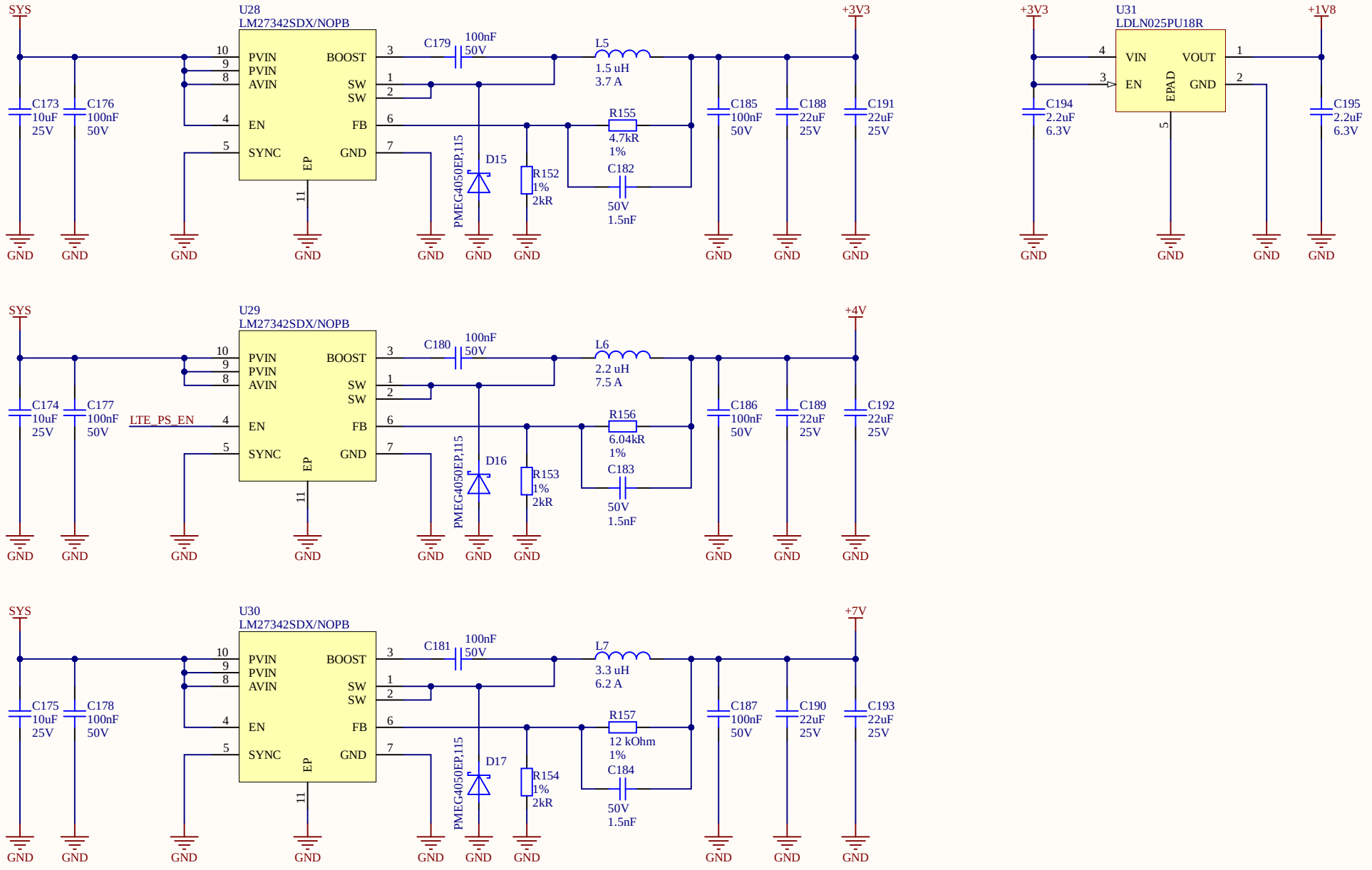
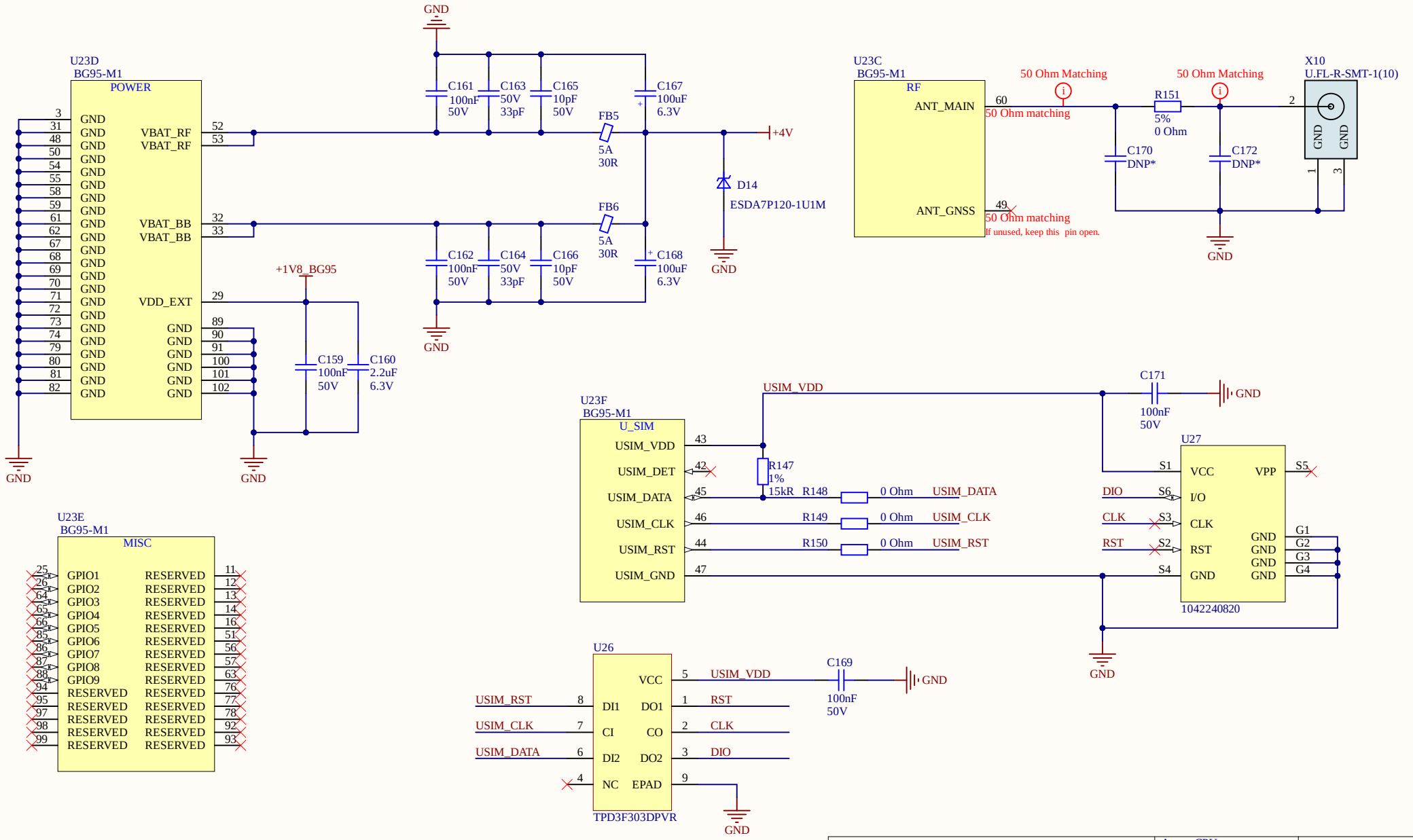


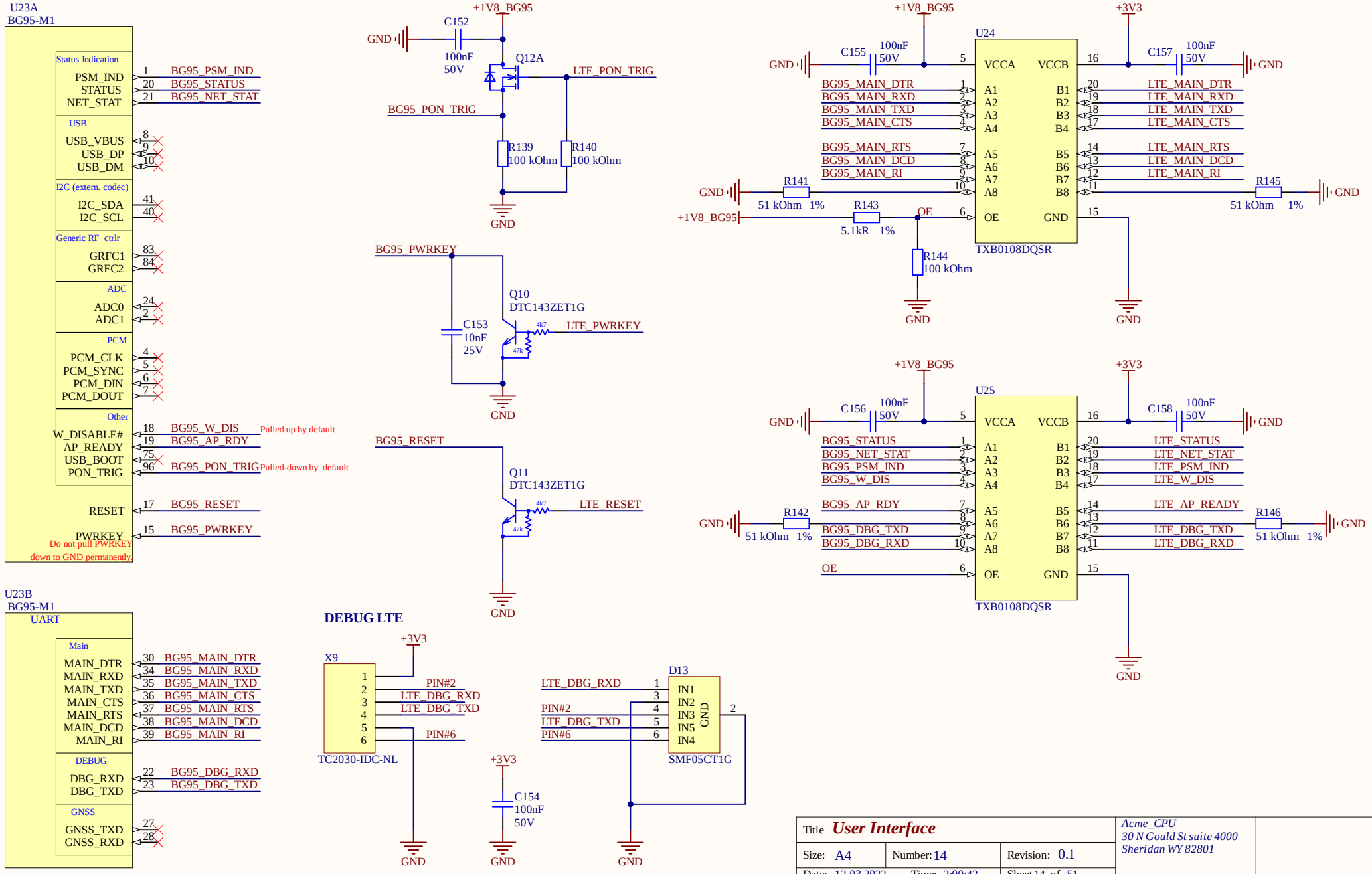
Title **User Interface**			*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022 Time: 2:00:41		Sheet 14 of 51	
File: 16. Power Disp.SchDoc			

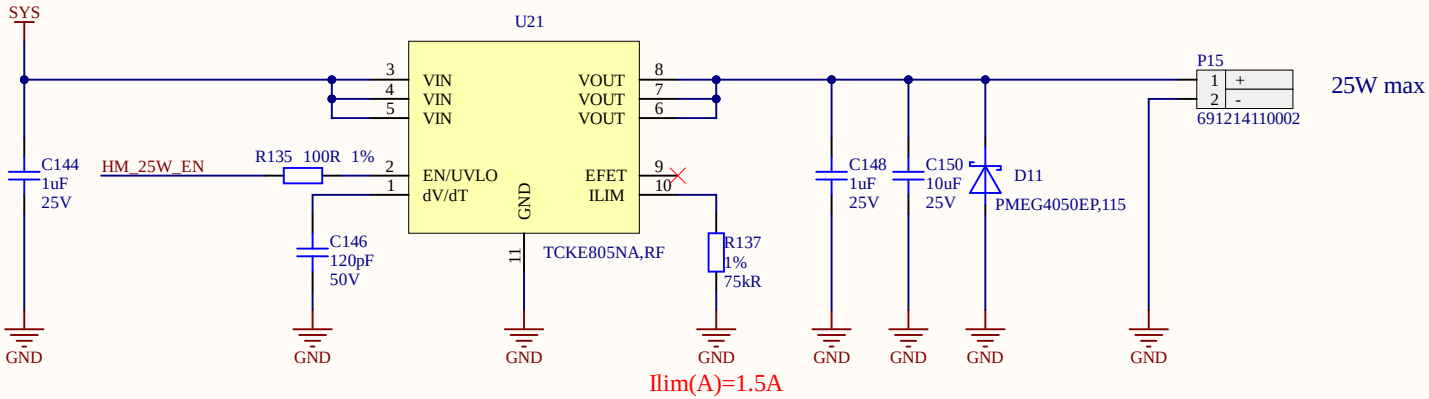


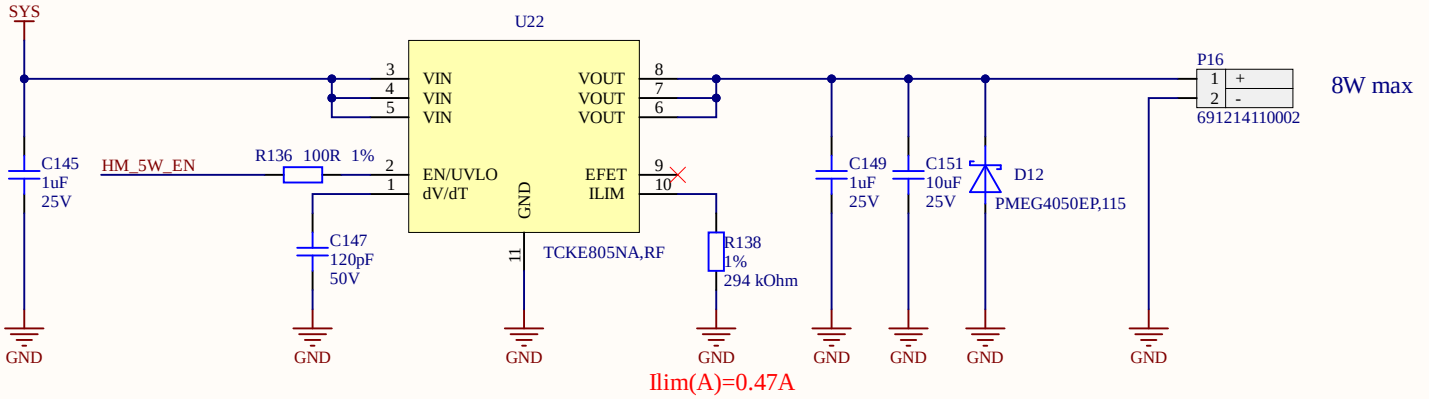
Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:42	Sheet 14 of 51	
File: 15. Power.SchDoc			



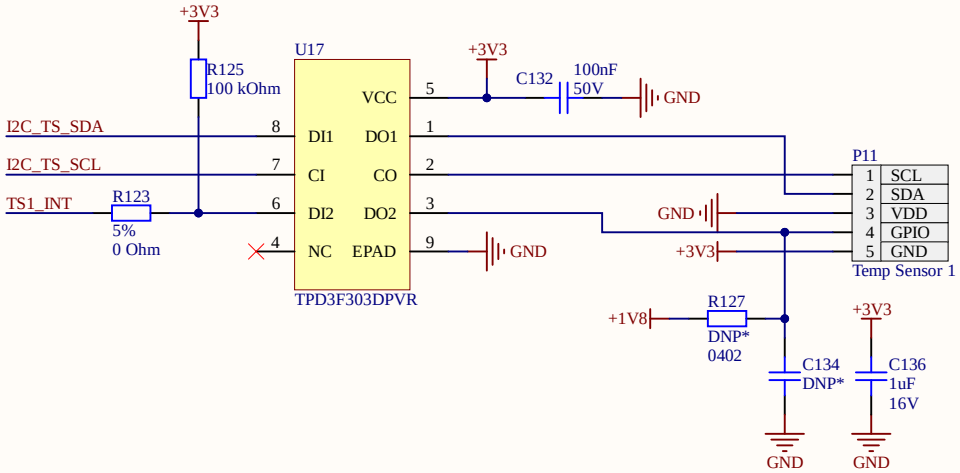
Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:42	Sheet 14 of 51	
File: 14. LTE Periph.SchDoc			

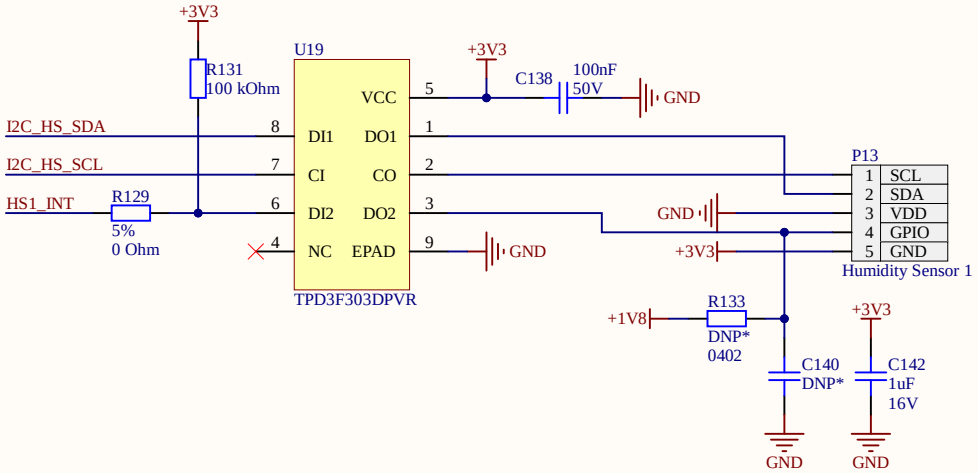

U23A
BG95-M1
Status Indication
PSM_IND
STATUS
NET_STAT
USB
USB_VBUS
USB_DP
USB_DM
I2C (extern. codec)
I2C_SDA
I2C_SCL
Generic RF ctrlr
GRFC1
GRFC2
ADC
ADC0
ADC1
PCM
PCM_CLK
PCM_SYNC
PCM_DIN
PCM_DOUT
Other
W_DISABLE#
AP_READY
USB_BOOT
PON_TRIG
RESET
PWRKEY
Do not pull PWRKEY down to GND permanently
BG95_PSM_IND
BG95_STATUS
BG95_NET_STAT
BG95_W_DIS
Pulled up by default
BG95_AP_RDY
BG95_PON_TRIG
Pulled-down by default
BG95_RESET
BG95_PWRKEY
U23B
BG95-M1
UART
Main
MAIN_DTR
MAIN_RXD
MAIN_TXD
MAIN_CTS
MAIN_RTS
MAIN_DCD
MAIN_RI
DEBUG
DBG_RXD
DBG_TXD
GNSS
GNSS_TXD
GNSS_RXD
+1V8_BG95
C152
100nF
50V
Q12A
LTE_PON_TRIG
R139
100 kOhm
R140
100 kOhm
Q10
DTC143ZET1G
C153
10nF
25V
LTE_PWRKEY
Q11
DTC143ZET1G
LTE_RESET
DEBUG LTE
X9
TC2030-IDC-NL
PIN#2
PIN#6
C154
100nF
50V
D13
SMF05CT1G
U24
TXB0108DQSR
VCCA
VCCB
OE
GND
C155
C157
R141
R143
R144
R145
51 kOhm 1%
5.1kR 1%
100 kOhm
BG95_MAIN_DTR
BG95_MAIN_RXD
BG95_MAIN_TXD
BG95_MAIN_CTS
BG95_MAIN_RTS
BG95_MAIN_DCD
BG95_MAIN_RI
LTE_MAIN_DTR
LTE_MAIN_RXD
LTE_MAIN_TXD
LTE_MAIN_CTS
LTE_MAIN_RTS
LTE_MAIN_DCD
LTE_MAIN_RI
U25
C156
C158
R142
R146
BG95_STATUS
BG95_NET_STAT
BG95_PSM_IND
BG95_W_DIS
BG95_AP_RDY
BG95_DBG_TXD
BG95_DBG_RXD
LTE_STATUS
LTE_NET_STAT
LTE_PSM_IND
LTE_W_DIS
LTE_AP_READY
LTE_DBG_TXD
LTE_DBG_RXD
+3V3
Title User Interface
Size: A4
Number: 14
Revision: 0.1
Date: 12.03.2022
Time: 2:00:42
Sheet 14 of 51
File: 13. LTE.SchDoc
Acme_CPU
30 N Gould St suite 4000
Sheridan WY 82801

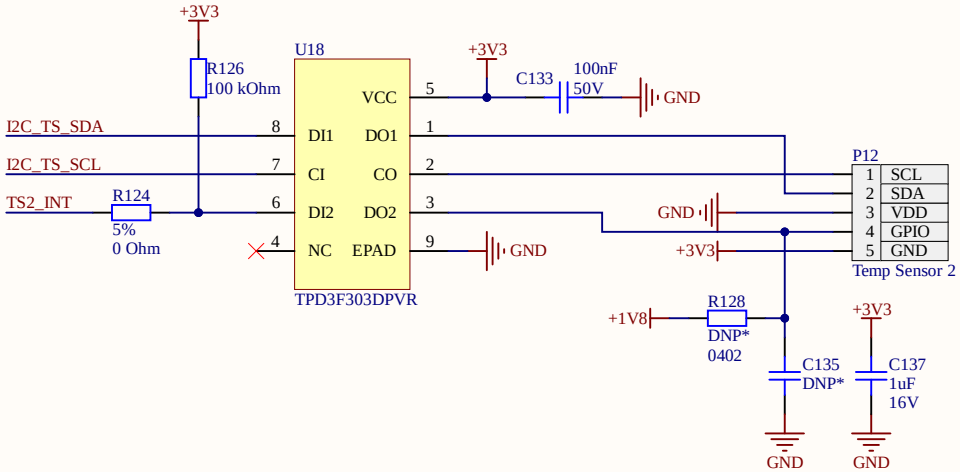


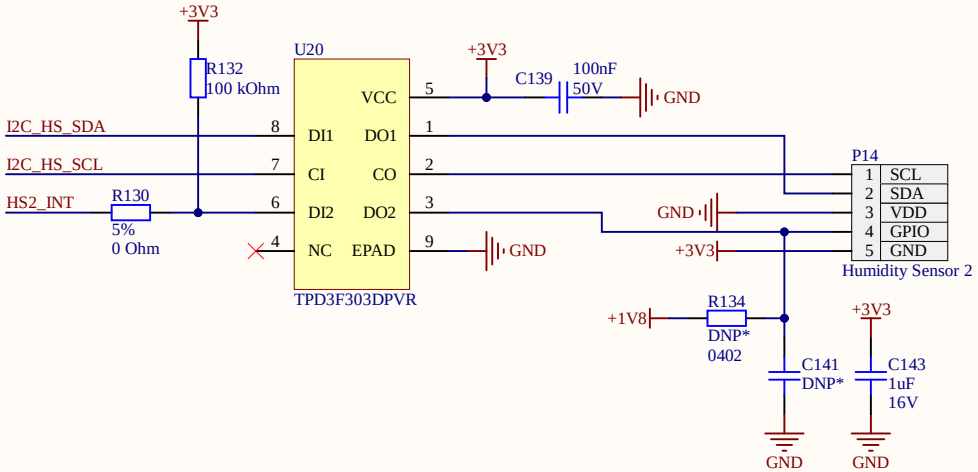


Title **User Interface**			*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:42	Sheet 14 of 51	
File: 12. Heating modules.SchDoc			

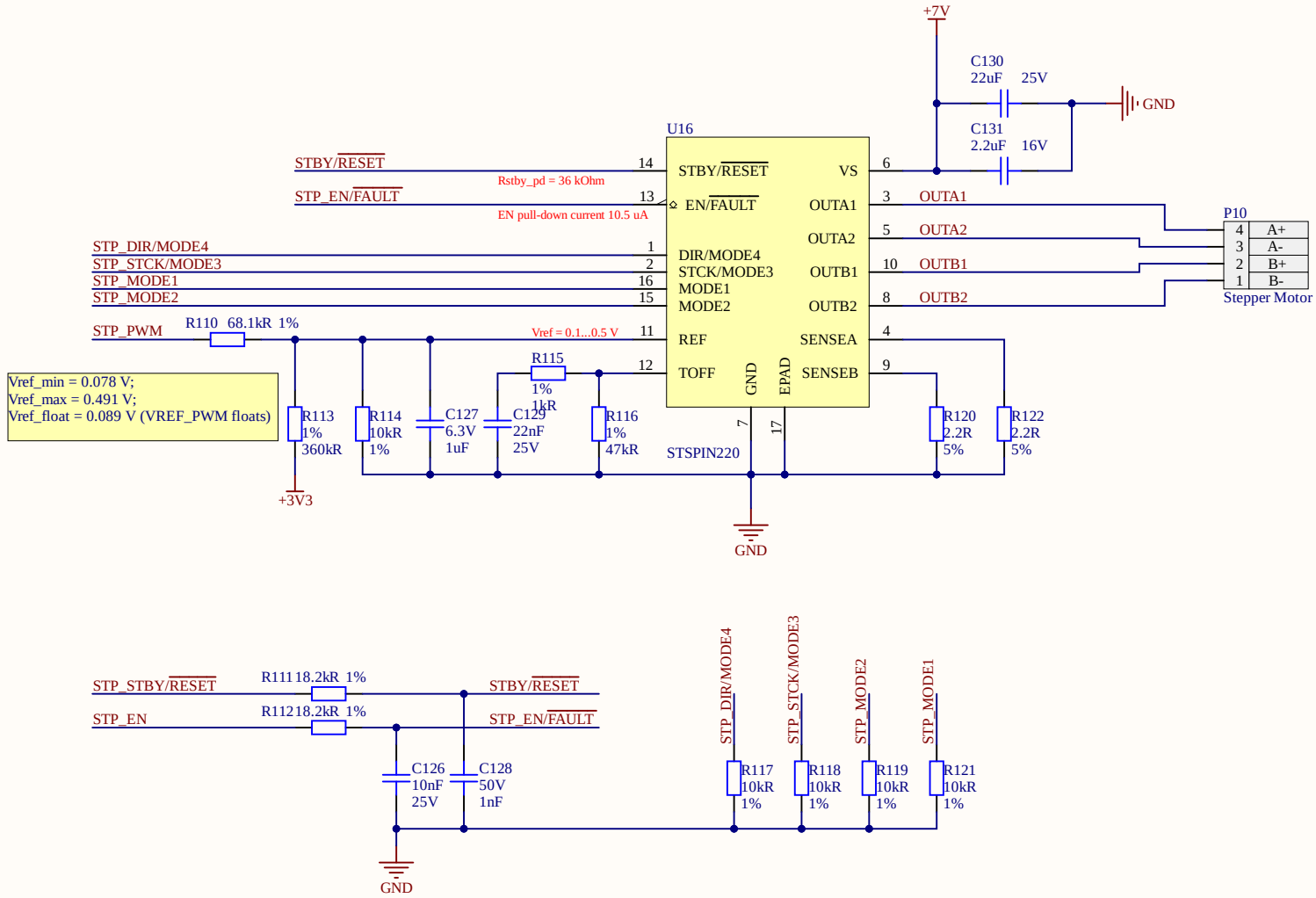




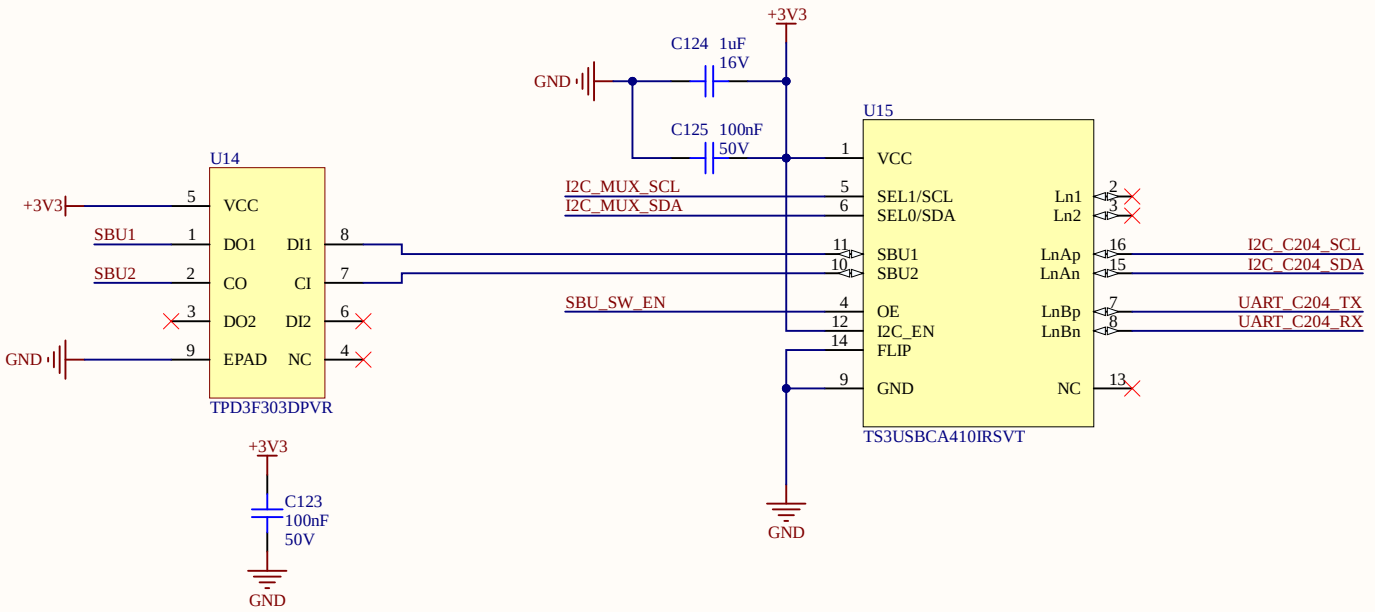




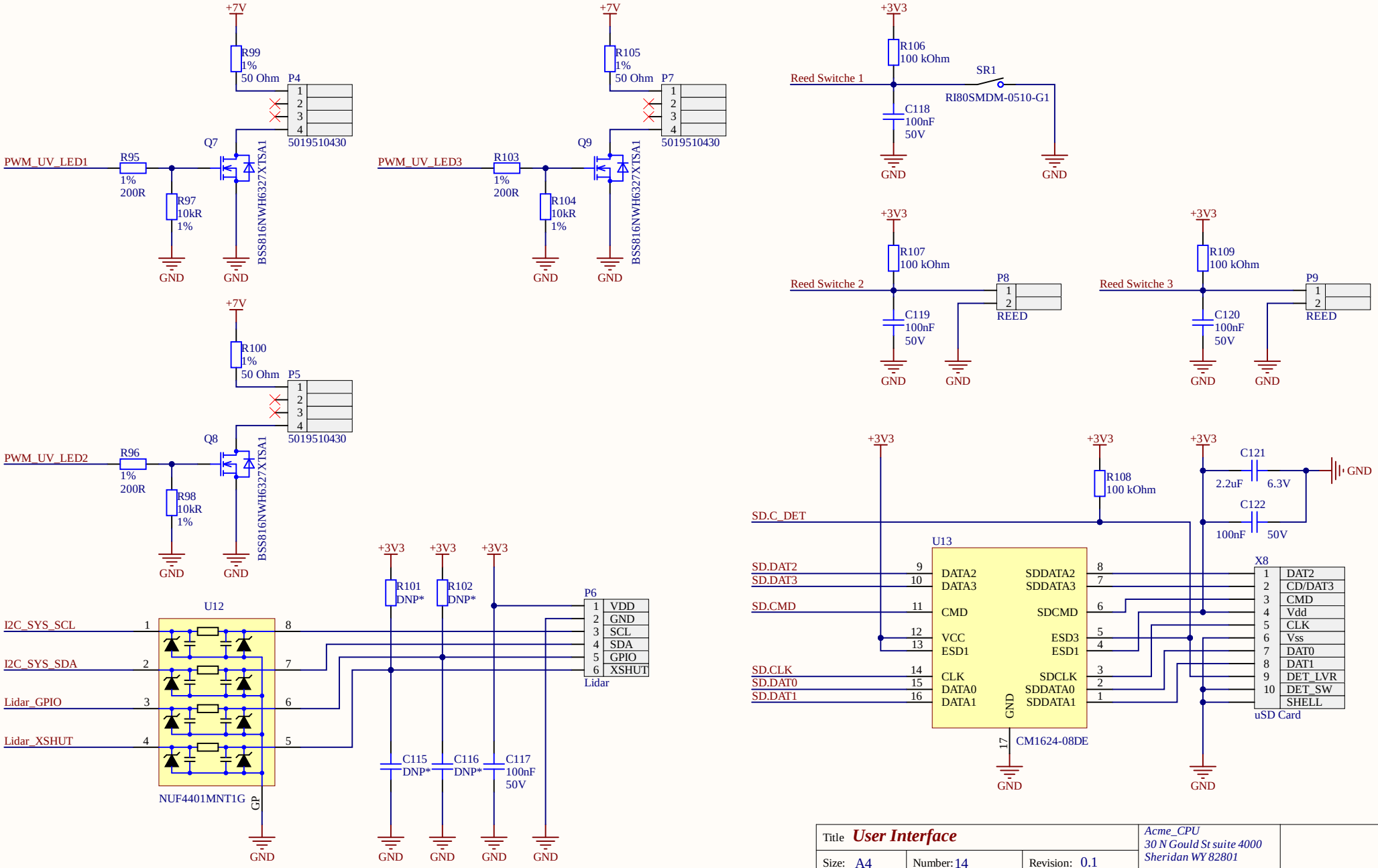
Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:42	Sheet 14 of 51	
File: 11. Sensors.SchDoc			



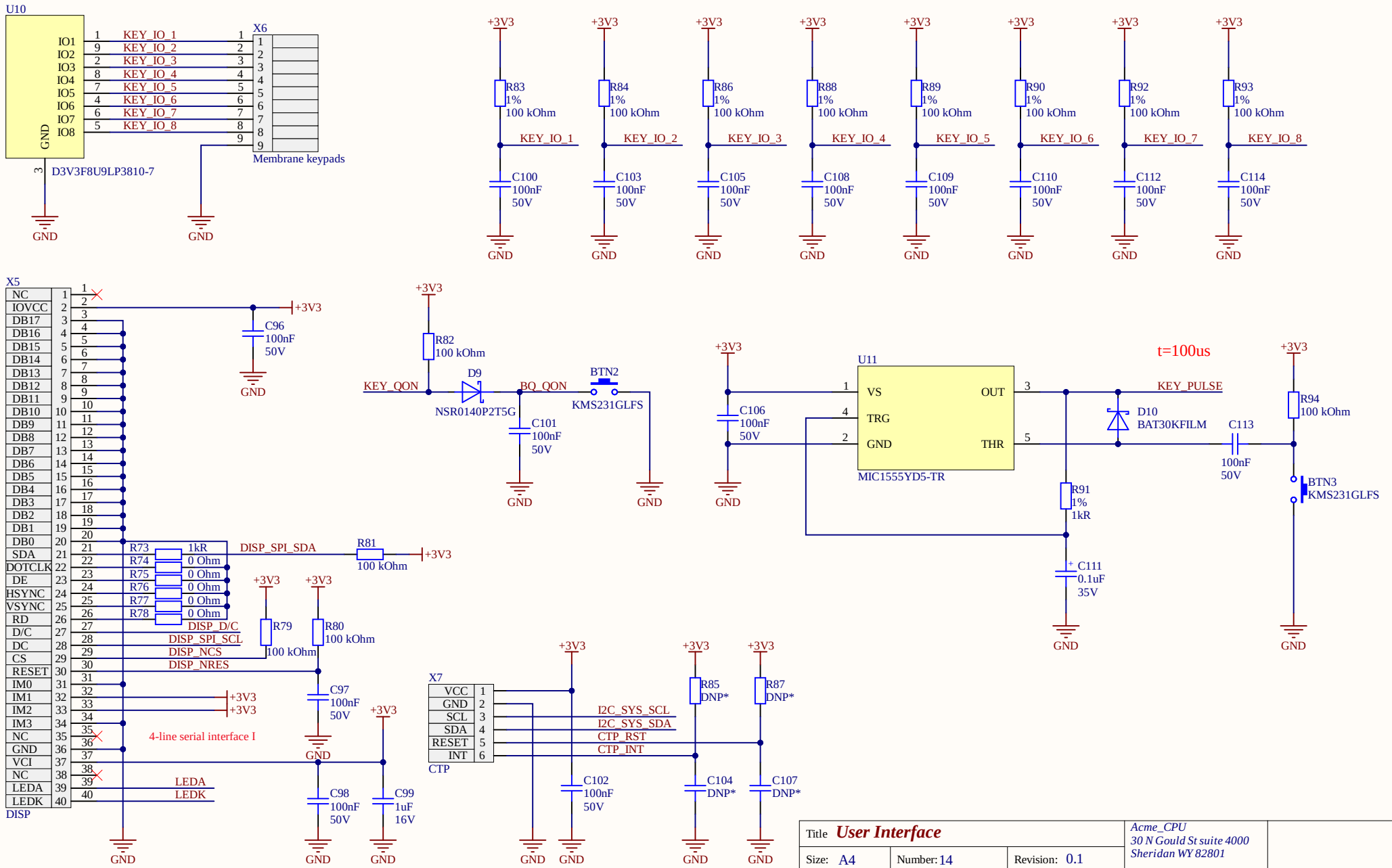
Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:43	Sheet 14 of 51	
File: 10. Stepper Motor Driver.SchDoc			



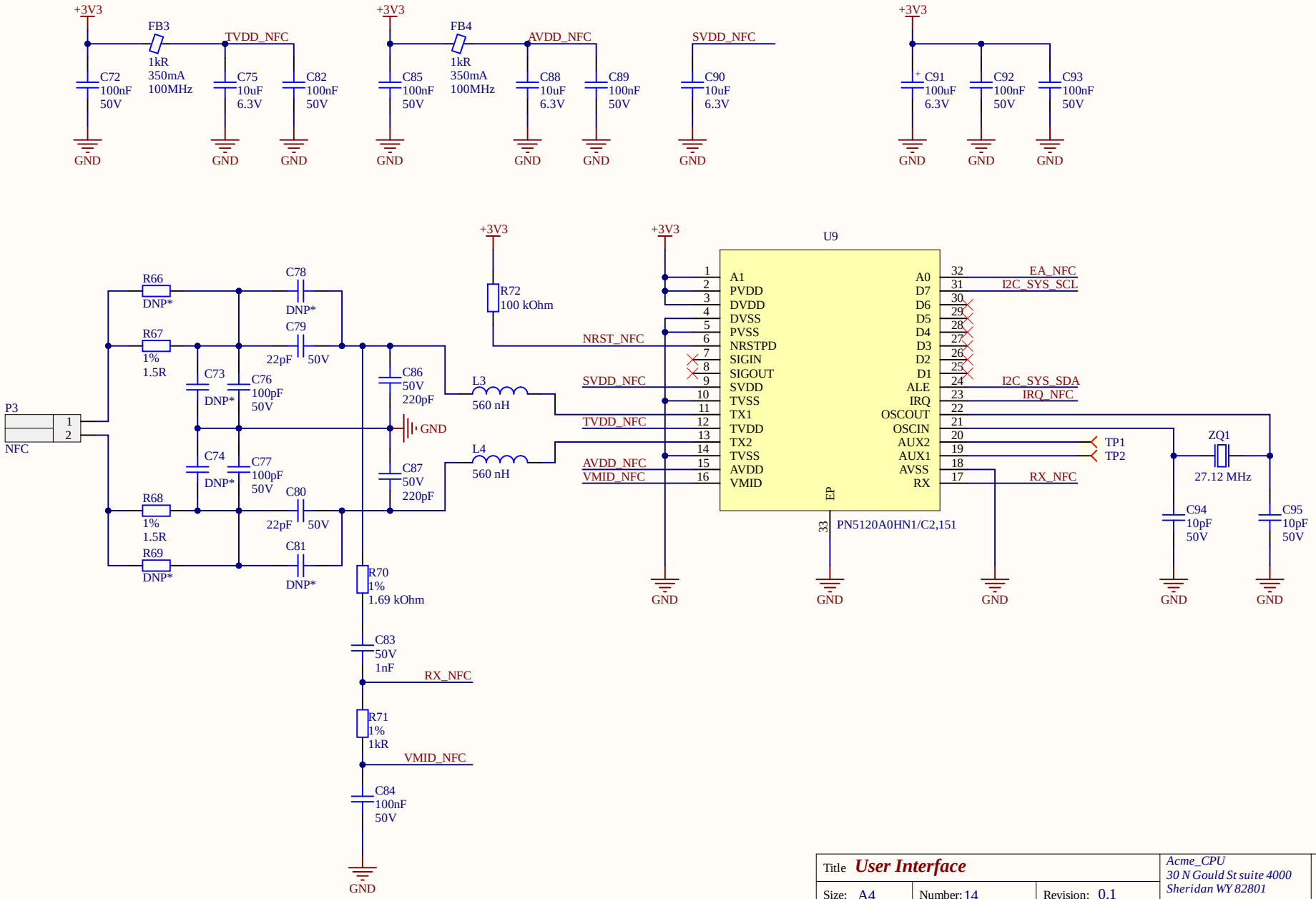
Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:43	Sheet 14 of 51	
File: 9. MUX.SchDoc			



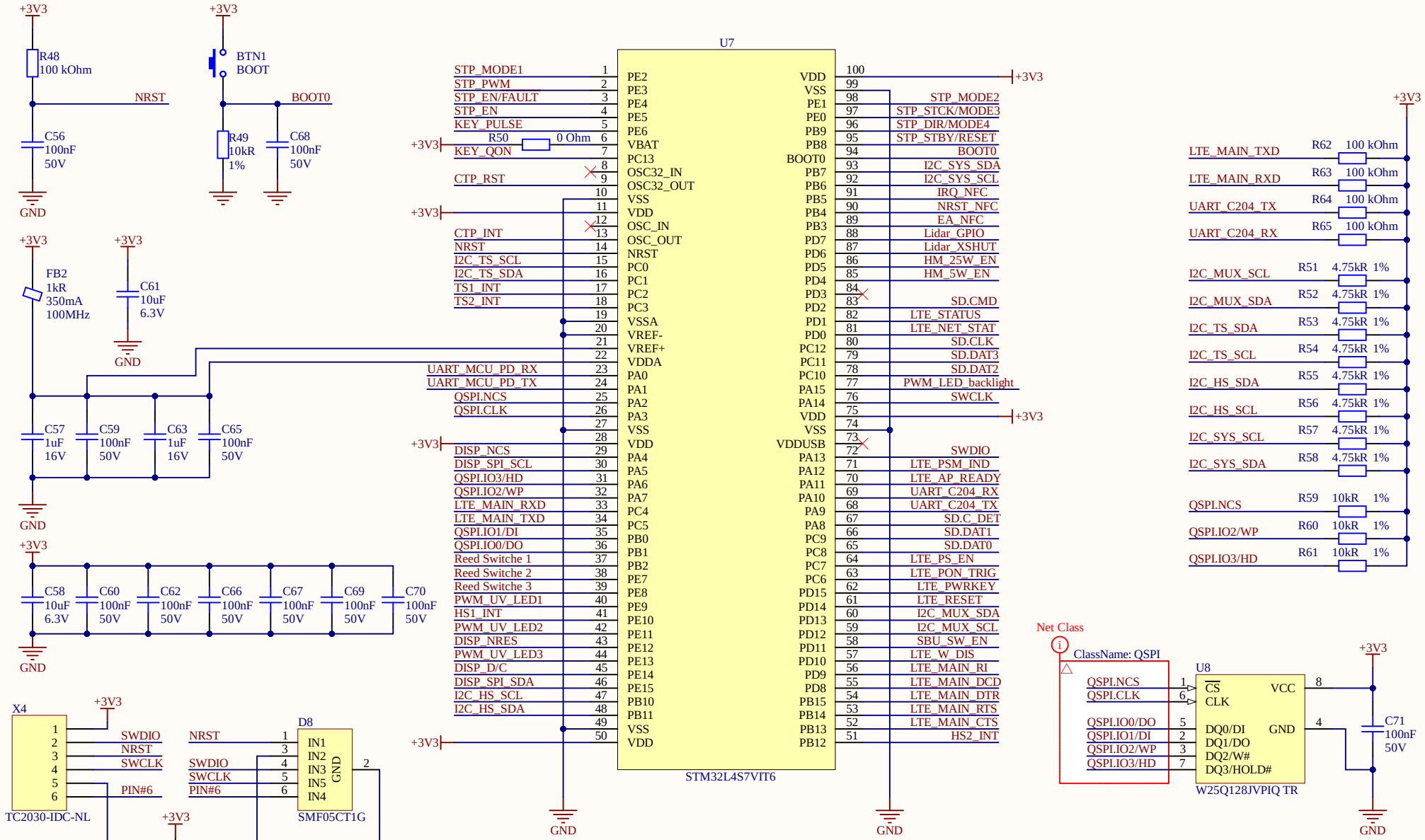
Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:43	Sheet 14 of 51	
File: 8. PERIPH.SchDoc			



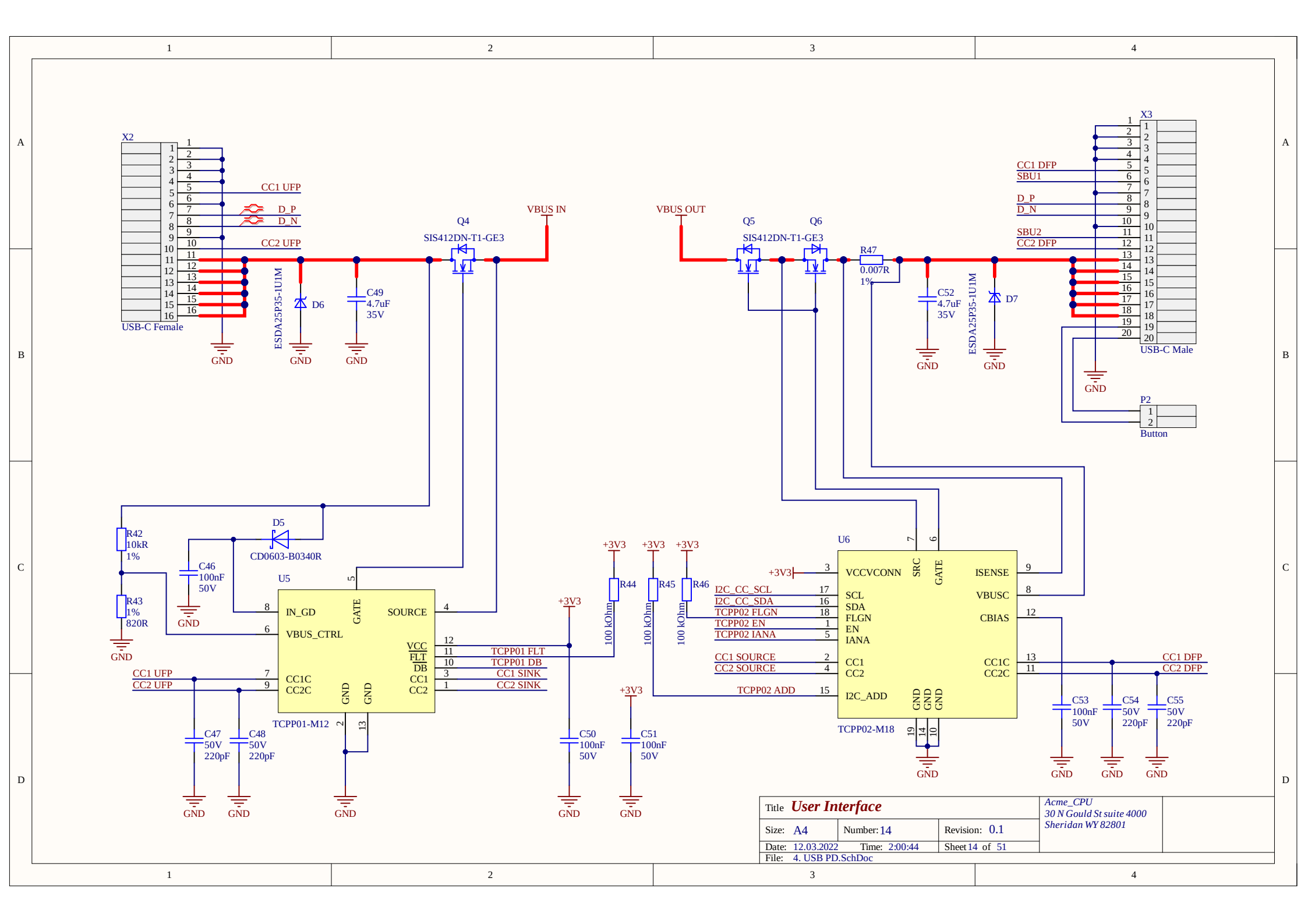
Title	***User Interface***		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:43	Sheet 14 of 51	
File: 7. User interface.SchDoc			

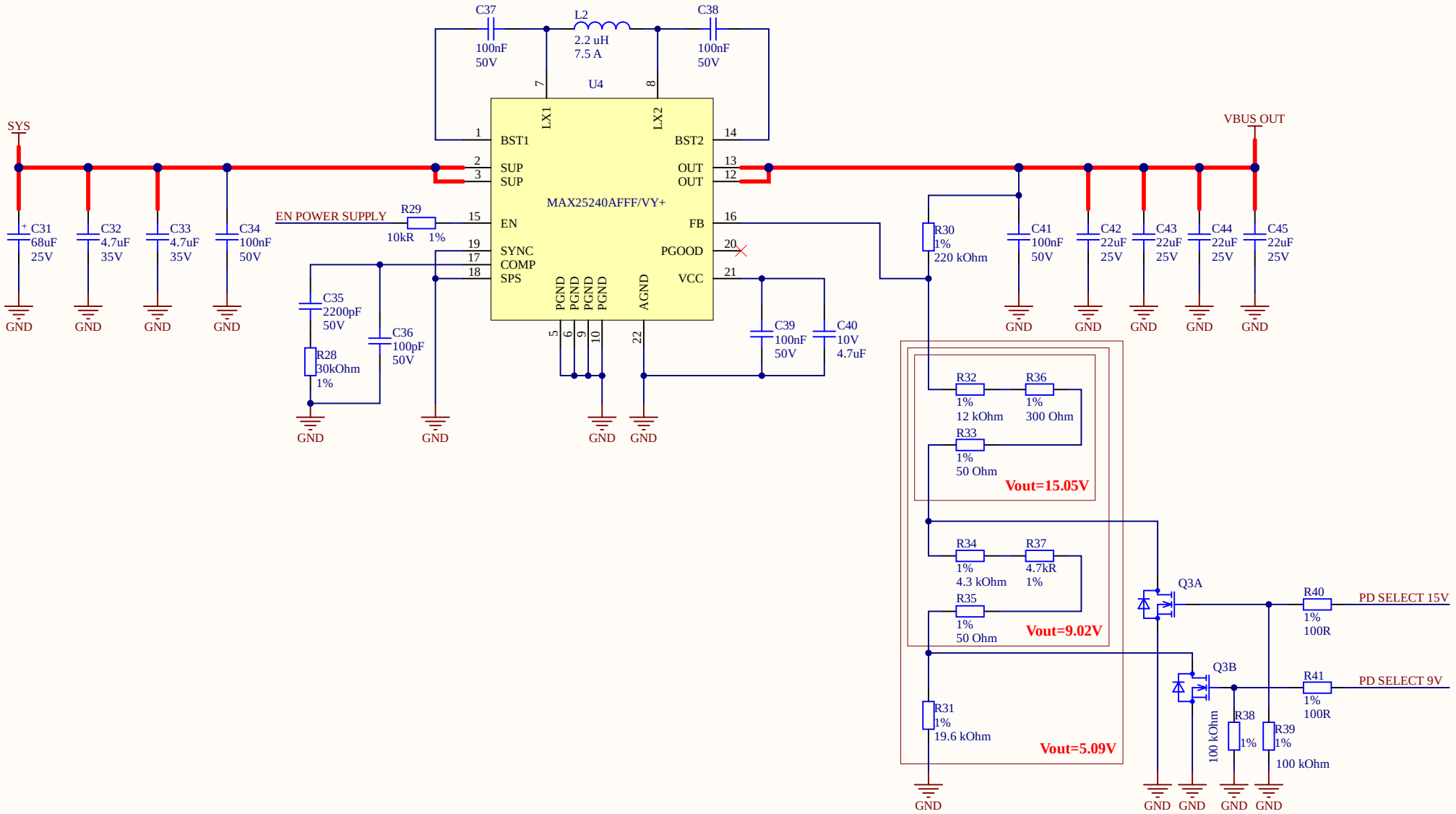


Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:43	Sheet 14 of 51	
File: 6. NFC.SchDoc			

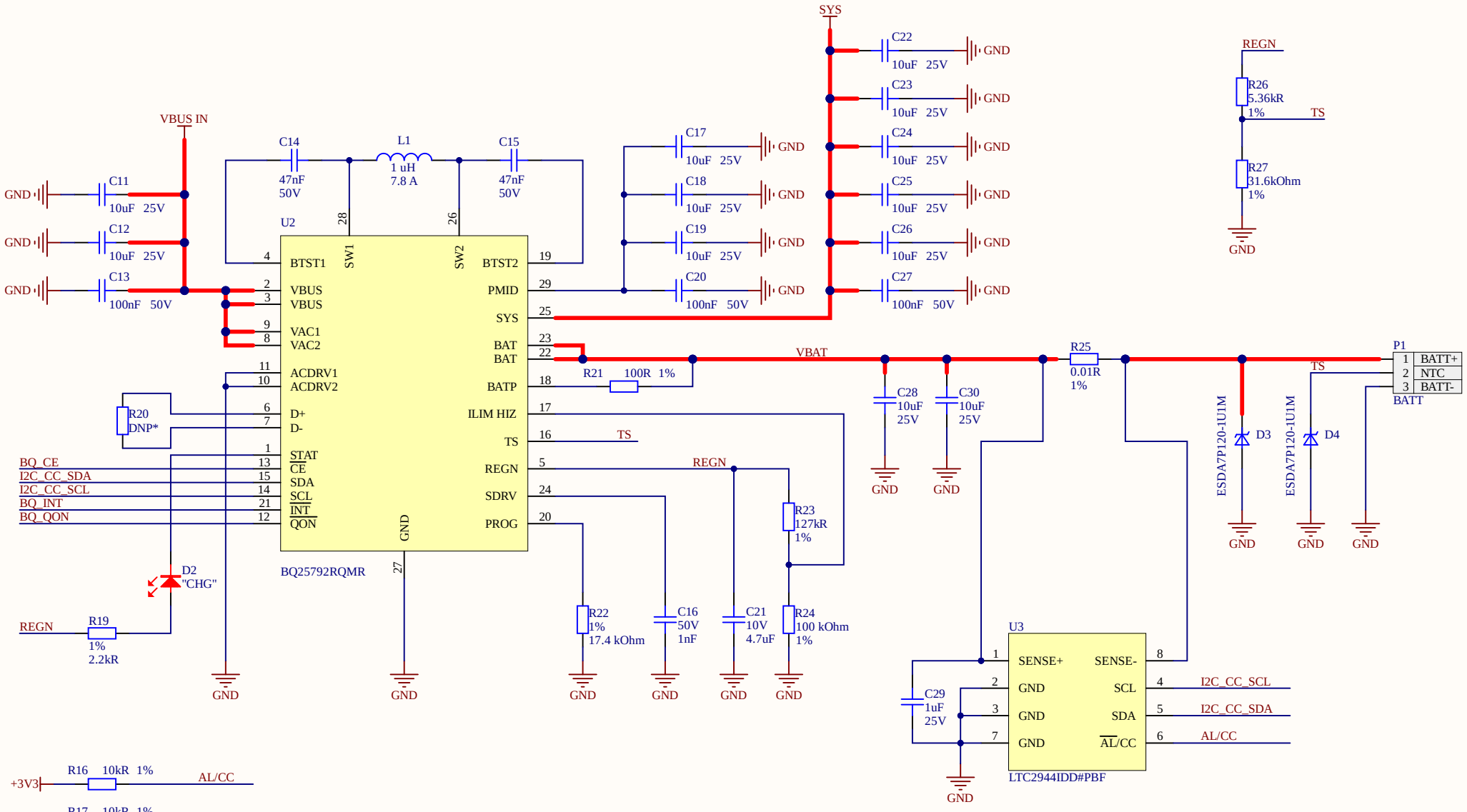


Title	***User Interface***		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:44	Sheet 14 of 51	
File: 5. MCU.SchDoc			

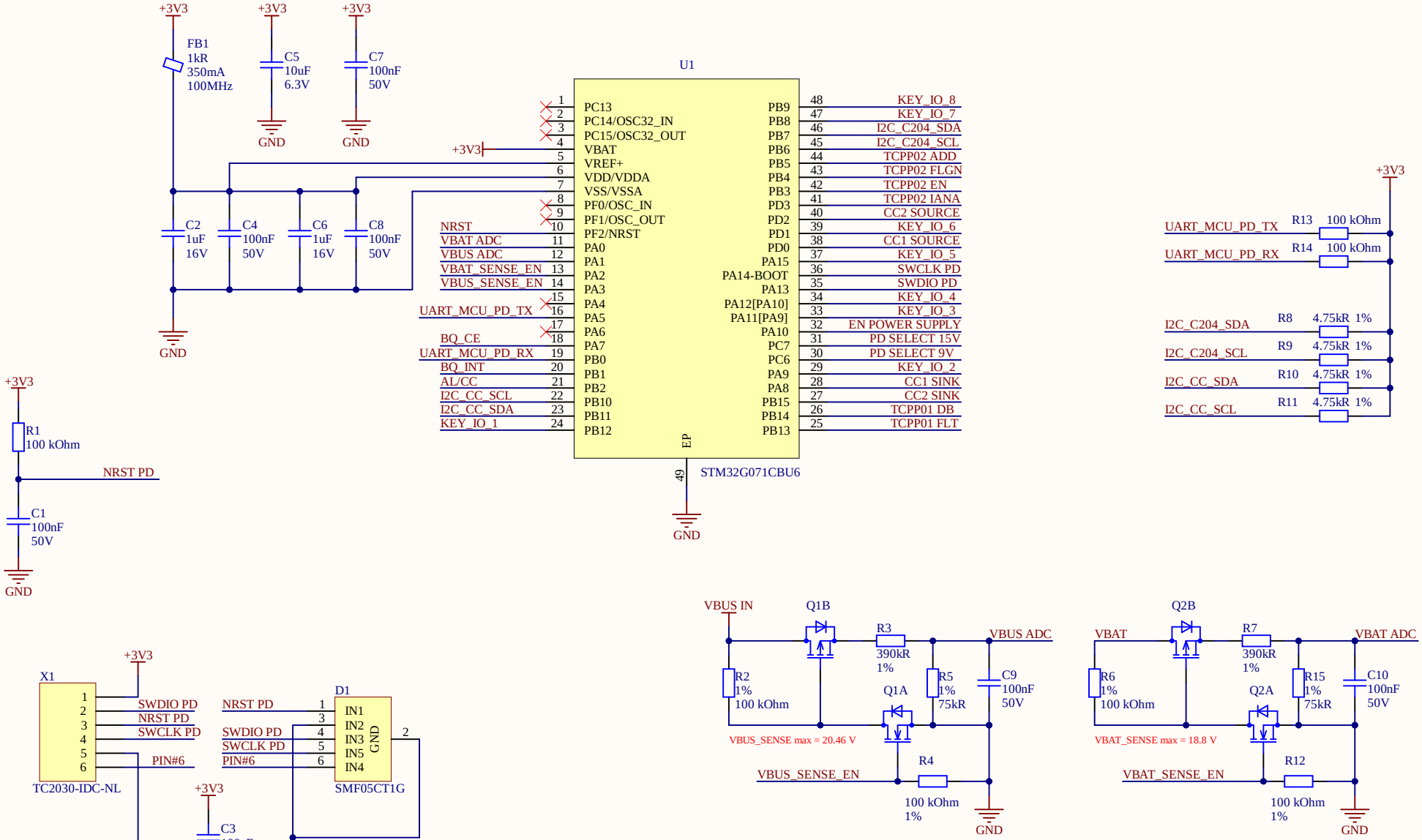

X2
USB-C Female
CC1 UFP
CC2 UFP
D_P
D_N
GND
ESDA25P35-1U1M
D6
C49
4.7uF
35V
Q4
SIS412DN-T1-GE3
VBUS IN
VBUS OUT
Q5
Q6
SIS412DN-T1-GE3
R47
0.007R
1%
C52
4.7uF
35V
D7
X3
USB-C Male
CC1 DFP
SBU1
SBU2
CC2 DFP
P2
Button
R42
10kR
1%
R43
1%
820R
C46
100nF
50V
D5
CD0603-B0340R
U5
IN_GD
GATE
SOURCE
VBUS_CTRL
VCC
FLT
DB
CC1
CC2
CC1C
CC2C
TCPP01-M12
TCPP01 FLT
TCPP01 DB
CC1 SINK
CC2 SINK
CC1 UFP
CC2 UFP
C47
50V
220pF
C48
50V
220pF
+3V3
R44
R45
R46
100 kOhm
C50
100nF
50V
C51
100nF
50V
U6
VCCVCONN
SRC
GATE
ISENSE
VBUSC
CBIAS
SCL
SDA
FLGN
EN
IANA
CC1
CC2
I2C_ADD
CC1C
CC2C
TCPP02-M18
I2C_CC_SCL
I2C_CC_SDA
TCPP02 FLGN
TCPP02 EN
TCPP02 IANA
CC1 SOURCE
CC2 SOURCE
TCPP02 ADD
C53
100nF
50V
C54
50V
220pF
C55
50V
220pF
Title User Interface
Size: A4
Number: 14
Revision: 0.1
Date: 12.03.2022
Time: 2:00:44
Sheet 14 of 51
File: 4. USB PD.SchDoc
Acme_CPU
30 N Gould St suite 4000
Sheridan WY 82801



Title **User Interface**			*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:44	Sheet 14 of 51	
File: 3. Power PD.SchDoc			



Title **User Interface**			*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:44	Sheet 14 of 51	
File: 2. Charger.SchDoc			



Title	**User Interface**		*Acme_CPU* *30 N Gould St suite 4000* *Sheridan WY 82801*
Size: A4	Number: 14	Revision: 0.1	
Date: 12.03.2022	Time: 2:00:45	Sheet 14 of 51	
File: 1. MCU PD.SchDoc			