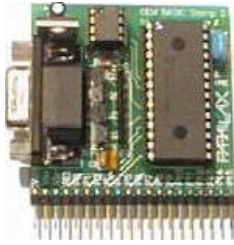

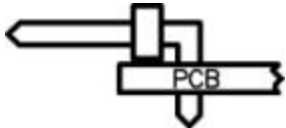
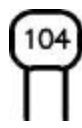
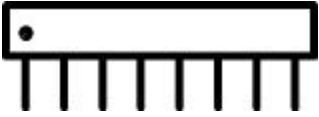
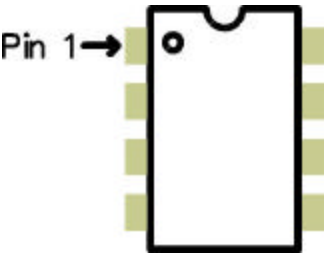


# OEM BS2 - Rev A1 Assembly Instructions

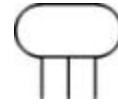


This document is a guide that will aid you in the assembly of your OEM-BS2 Rev A1. It is assumed that you have the proper equipment, and possess the skills necessary to safely assemble electronic components.

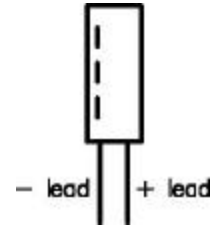
Generally, it is best to start with the lower profile components, and then work your way to the tallest components. This is often true because, in tight places, lower profile components are more difficult to get at with taller components in the way.

1. The three lowest profile components on the board are the 4.7K Ohm resistors and the 10K Ohm resistor. These resistors should have color bands around them that signify their value. The color band pattern for the 4.7K resistors should be: Yellow, Violet, Red, and Gold. The color band pattern for the 10K resistor should be: Brown, Black, Orange, and Gold. Resistors are polarity insensitive, which means that it doesn't matter which end goes where. Install the 4.7K resistors where designated by 'R1' and 'R2', and the 10K resistor where designated by 'R3'.
 
2. The next lowest profile component on the board is the 20-pin right angle strip header. It may seem ambiguous as to which side is inserted into the pcb. The side with the angle should be inserted into the pcb as shown in the drawing to the right. Install the strip header where designated by 'X1'.
 
3. Next, find the two 0.1 uF ceramic capacitors. Like, the resistors, it doesn't matter which way these are installed. Install the two capacitors where designated by 'C2' and 'C3'.
 
4. The next components to install are the resistor networks. A resistor network looks like a comb for a 'Barbie Doll'. On the resistor network (Rnet), there is a dot printed near one end to signify at which end Pin 1 is located. Find Pin 1 on the Rnet and correlate it to the square pad on the PCB that pertains to the 'RN1' part designator. Install the Rnets where designated by 'RN1' and 'RN2'.
 
5. The IC sockets are next in line. Nearly all ICs have a 'Pin 1' marker that denotes the location of Pin 1. It is important to install chips with the proper orientation since, more often than not, installing a chip backwards will result in damage to the chip and/or surrounding circuitry. For the IC sockets that you will be installing, there is a notch on the edge of the short sides. This notch denotes the 'top' of the chip. When oriented properly, Pin 1 is the top-left pin as depicted in the drawing to the right. Install both the 28-Pin and the 8-Pin DIP sockets into U1 and U2 respectively.
 

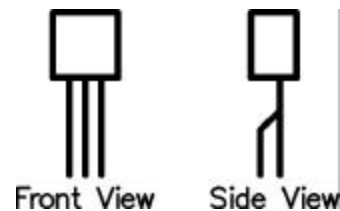
6. The next component to install is the ceramic resonator. The ceramic resonator looks like a coffee bean (although sometimes they are blue instead of brown) with three metal legs sticking out from one side and should be marked as “20.00”, (20 MHz). This component too is ‘polarity insensitive’. That is to say, it doesn’t matter which way it is installed, it will still work OK. Install the ceramic resonator where the ‘XTAL’ designator is.



7. Next, grab the electrolytic capacitor. It resembles a small cylinder with two long legs emerging from one end. Be particularly careful when installing this component. If it is installed backwards, it can pop like a miniature fire cracker and emit a horrible stench after it is powered up. The electrolytic capacitor will have the negative lead marked with one or more ‘-’ signs. Install the leg NOT marked with the ‘-’ signs in the hole closest to the ‘+’ sign in C1’s space on the pcb.



8. Now grab the Reset Supervisor; it looks like a black bead with three legs and bears the mark, “13811”. The white silkscreen on the pcb will guide you as to how to orient this component. There are three other components that closely resemble the Reset Supervisor that we will be installing in this step. You will notice that their middle legs must be bent slightly to accommodate the hole patterns for Q1, Q2, Q3, and U3. Please bend the middle leg of the Reset Supervisor, the PNP transistor (bears the mark, “3906”), and the NPN transistors, (they bear the mark, “3904”), as shown in the drawing above. Avoid excessive bending as this can damage the component. Install the reset supervisor into U3, install the PNP transistor into Q3, and install the NPN transistors into Q1, and Q2.



9. Next you will install the 9-pin D-Sub connector. This is the only component that looks like it will plug into a serial cable. Install it where designated by the “J1” label.
10. The last component to solder on is the voltage regulator, VR1. It is a relatively large plastic square with a metal tab protruding from the top. Install this with the metal tab closest to the 9-pin D-Sub connector. This component, like the electrolytic capacitor, IS polarity sensitive and MUST be installed correctly, lest ye suffer smoke and fire and the gnashing of teeth.
11. Be sure you clean the board before you stuff the Basic Interpreter IC and the eeprom IC into U1’s and U2’s respective sockets. That’s it ! You’ve done it! You’ve made your own stamp.
12. Visually inspect the board. Before applying power to it, repair all solder bridges and ensure that all components have been installed in the proper locations and with the correct orientation. Download and run the latest version of the program, STAMPW.exe, from either the CD or the website. Connect the OEM BS2 to a PC serial port and power it up. Please consult the Basic Stamp manual, also available from either the CD or our website, for guidance on using your BS2. Select the BS2 as the default stamp before attempting to identify or download a program to your new BS2. You must also name the stamp program you wish to download with a .bs2 extension.

## OEM BS2 – Rev A1      Design Notes

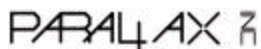
One of the main purposes behind the OEM BS2 is making it easier to embed the BS2 into an OEM design. When designing the BS2 into your product, please consider the following:

- ?? Power Supply - The Basic Interpreter requires a 'clean' 5 Vdc supply. The LM2940 was selected because of its wide operating range, built-in protection features, and ability to deliver up to 1 Amp of current. The Vin pin is the input to the voltage regulator and requires 6-24 Vdc for proper operation. The value of the output capacitor, C1, was based on the manufacturer's recommendation. Be sure to consult the manufacturer's recommendations for input and output capacitance selection for the regulator that you will be using in your design. Also, if the Basic Interpreter will be located more than a couple of inches away from the power supply, it may be a good idea to add a 0.1 uF ceramic capacitor across Vdd and Vss at the Basic Interpreter IC.
- ?? Resonator Location – Note that the resonator on the OEM BS2 board is located adjacent to the Basic Interpreter. When laying out your board, please choose a location for the resonator that is as close as possible to the Basic Interpreter, and has the shortest circuit traces possible to ensure reliable operation.
- ?? Regulatory Warnings – Stamps and their constituent components are not FCC approved. They are not in finished product form. If you wish to obtain FCC approval, you must first design the stamp into a product, then seek FCC approval of the whole product.
- ?? We use a minimum complement of components to effect an interface between the high levels of RS232 and the relatively low levels of TTL. Some customers may need or want to use a different approach. Regardless of how the line levels are interfaced, there are two important things to note: the idle state of the serial input line (Sin) at the interpreter, (RA.2), is high, so you may need to place a pull-up resistor on the RA.2 pin, also, there must exist some form of loop back from 'Sin' to 'Sout' so that the Stampw.exe software can communicate with the stamp.
- ?? Symbols – Various acronyms are used throughout this manual. Here's a brief glossary:

OEM - Original Equipment Manufacturer  
BS2 - Second generation Basic Stamp  
IC - Integrated Circuit  
DIP - Dual In-line Package  
PCB - Printed Circuit Board  
Vin - Input Voltage (Specifically, the positive side of the supply DC voltage)  
Vdd - 5Vdc (the positive side)  
Vss - Ground (the negative side of the supply DC voltage)

- ?? Powering up your OEMBS2 – There are two ways in which you can power your OEMBS2:
  - 1) You may apply 6-24 Vdc to the Vin pin (and ground to the Vss pin)
  - OR –
  - 2) You may apply 5Vdc to the Vdd pin (and ground to the Vss pin)

**Please note that these two methods are mutually exclusive: do one or the other, but never both!**



?? **WARNING!** Interpreter chips are available in a number of packages and the pinouts may be different than the DIP chip depicted in this document. When laying out your pcb board, be sure to specify the correct package and use the correct pinout. Specifically, the SOIC package has the same pinout as the DIP, but the SSOP is different. Ex: for the BS2:

U1 PBASIC2/P			U1 PBASIC2/SO			U1 PBASIC2/SS		
1	RTCC	MCLR	1	RTCC	MCLR	1	V <sub>ss</sub>	MCLR
2	V <sub>dd</sub>	OSC1	2	V <sub>dd</sub>	OSC1	2	TOCKI	OSC1
3	NC	OSC2	3	NC	OSC2	3	V <sub>dd</sub>	OSC2
4	V <sub>ss</sub>	RC7	4	V <sub>ss</sub>	RC7	4	V <sub>dd</sub>	RC7
5	NC	RC6	5	NC	RC6	5	RA0	RC6
6	RA0	RC5	6	RA0	RC5	6	RA1	RC5
7	RA1	RC4	7	RA1	RC4	7	RA2	RC4
8	RA2	RC3	8	RA2	RC3	8	RA3	RC3
9	RA3	RC2	9	RA3	RC2	9	RB0	RC2
10	RB0	RC1	10	RB0	RC1	10	RB1	RC1
11	RB1	RC0	11	RB1	RC0	11	RB2	RC0
12	RB2	RB7	12	RB2	RB7	12	RB3	RB7
13	RB3	RB6	13	RB3	RB6	13	RB4	RB6
14	RB4	RB5	14	RB4	RB5	14	V <sub>ss</sub>	RB5

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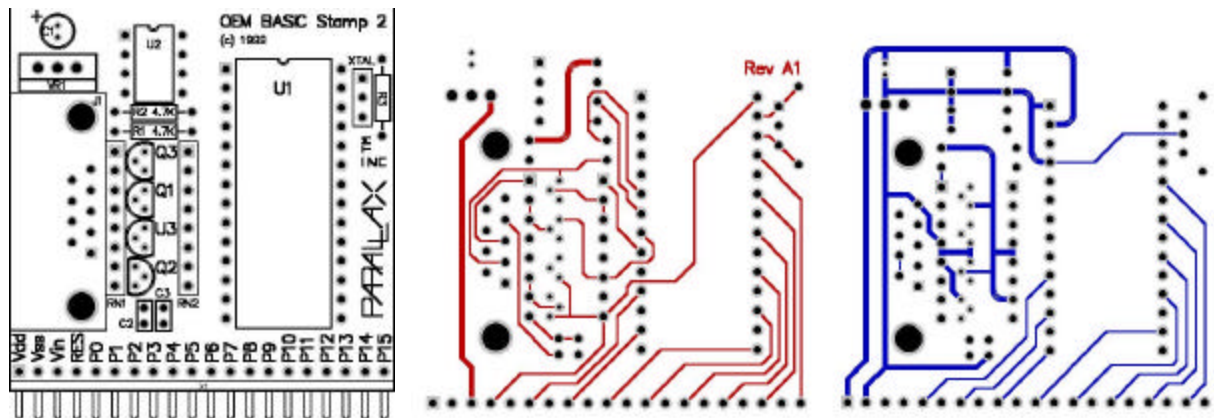
sales / technical support (916) 624-8333 ? fax (916) 624-8003

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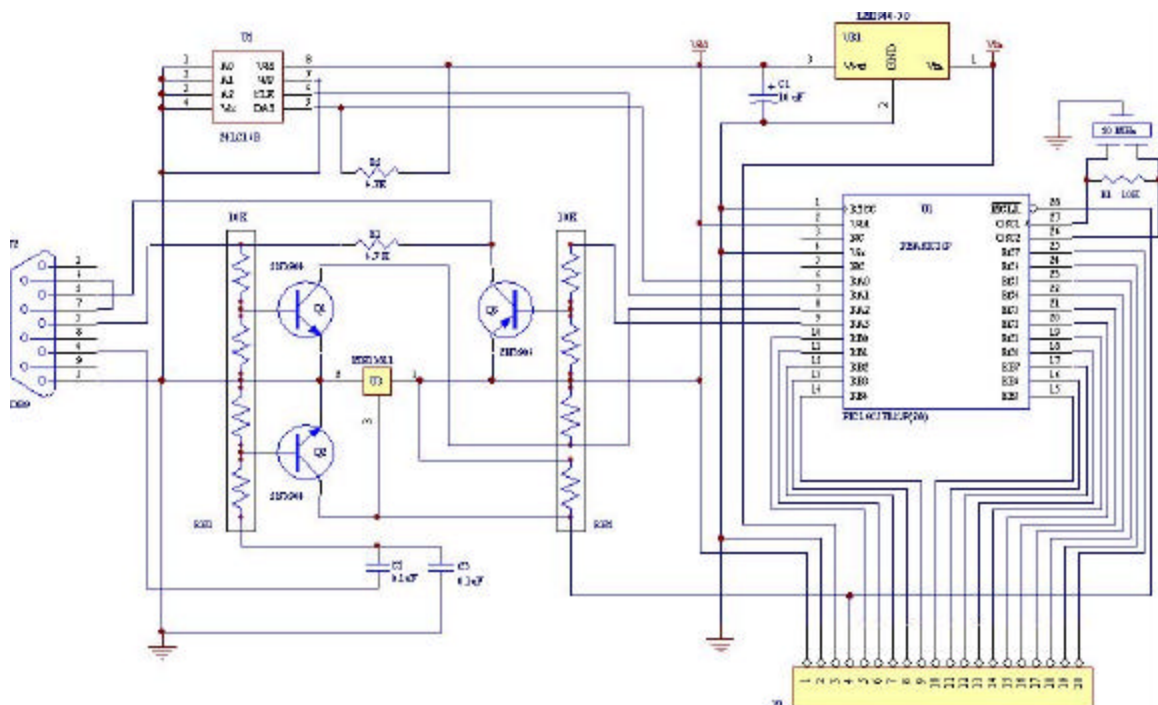
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The above images of the board layers are not to scale. Please visit [www.parallaxinc.com](http://www.parallaxinc.com) for the actual source and gerber files. This board was designed using Protel's Client '98 software. The top copper layer is the center plot, and the bottom copper layer is as 'viewed through' the board. The Rev A1 version of this board, depicted above, uses two layers. Of course, you may wish to use only one layer to reduce the cost of manufacturing. For that reason, we've provided a one-layer version of this design, Rev B, that is available from our website. The following schematic applies to both versions:



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