Cheipter 5.5

Possible buffer in L1:

L1: Write-back buffer

Possible buffer in L2

L2: Write buffer

Describle buffer in L1

L1: Write back buffer

Possible buffer in L2

L2: Write buffer

L2: Write buffer

2 for char 5.5 @ Procedure: 1. Allocate couche block for the missing data, selet a replacement victim. 2. If victim dirty, put it into the write-bed butter, which will be further forwarded into L2 nvitefutter 3. Issue write miss request to the L2 cache A. if hit in LZ, source dote into Ld cache; if miss, seus write request to memory 5. Data arrives and is installed in ki cache 6. Processorresumes execution and hilly in Ll cache, set the dirty bit (6) If LL miss, allocate cache block for the missing date select a replacement victing e) If victim dirty, put it its the write-back buffer, which will be further forwarded 3) Issue write miss request to the L2 cacle A) If hit in L2, source data into Llache, go to (8) 5) if miss, send write request to memory. 6) Deta arrives and 15 installed in LZ cocke A Dota parmires and is installed in Licacte 8) Processor resumes execution and hitsin LL cache, set the divty bit.

3 from drop. <u>5.5</u>
1) Procedure: 1) Allocate cache block for the mixing data select a replacement victing
2) if victim clean, put it into aridim buffer
between both Ld and it into the wivite-best 3) If victim dirty put it into the wivite-best into LZ
A) Issue wirite miss request to the L2 cache 5) If hit in L2, source date into 12 coche, involidate the L2 GPY.
Deta arrives and is installed in 21 coche 4) Processor resumes execution and hits in L1 coche set the dirty bit
(2) Procedore:
of richm clear put it into victim buffer, which will if richm dirty putition the write buffer, which will be richmed buffer
The furner francisco request to the L2 coche
4) If hit in L2, source detx into L1 codes inventory (5) if miss send write request to memory 2 Deta arrives and is installed in L2 code in L1 code
5) if miss send write request to memory 6) Data arrives and is installed in L1 coche in L1 coche 7) 8) Processor resures execution and hits in L1 coche Set the dirty bit