5.4

The cache line size= 2 byte offset bits
= 24
= 16 bytes

Gathe line size = 4 words

D The cache line size = 2 by te offset bits

- 25 = 32 bytes Cache line size = 8 words

4 04 5.4

Binary address 0000 100 10000 10000100	Tag	Index 0	Miss Hit	
10000	O		Hit	
10000		0		
	0			Prince Control of the
10000 100		1	Miss	
	0	1000	Muss	
MESO 111 01000	0	1110	Hus	
	0	1010	Miss	0.
1 0000000000	1	0	Missty	40
1110	0	1		110
1000 1100	0	1000		
100 110110	11	1		5
101 101 00	0	1011	Miss	
10090000 100	10	1000	-	
1 replaced	hlad	c ,with	tag	
2)	N	วา	77	2
3	ď	γ	n C	
	1000000000 11110 1000 1100 100 11010 101 101 00 1009000 100 Preplaced	10100000 0 10000000000 1 11110 0 1000 1100 0 100 110110 11 101 101 00 0 10090000 100 10	10100000 0 1010 100000000000 1 0 1010 11110 0 1 1000 100 110110 11 1 101 101 00 0 1011 10090000100 10 1000 O replaced black with	10100000 0 1010 Miss. 10000000000 1 0 Miss (v) 11110 0 1 Hit 1000 1100 0 1000 Hit 100 11010 0 10 11 Miss (s) 1009000000 10 1000 Miss (s) 1009000100 10 1000 Miss (s) 1009000100 10 1000 Miss (s)

Hit Patio

The hit Patio is equal to number of hits/number of cache access.

In this problem total hits are 3 and cache access is 12. Therefore Hit ratio = 3/12