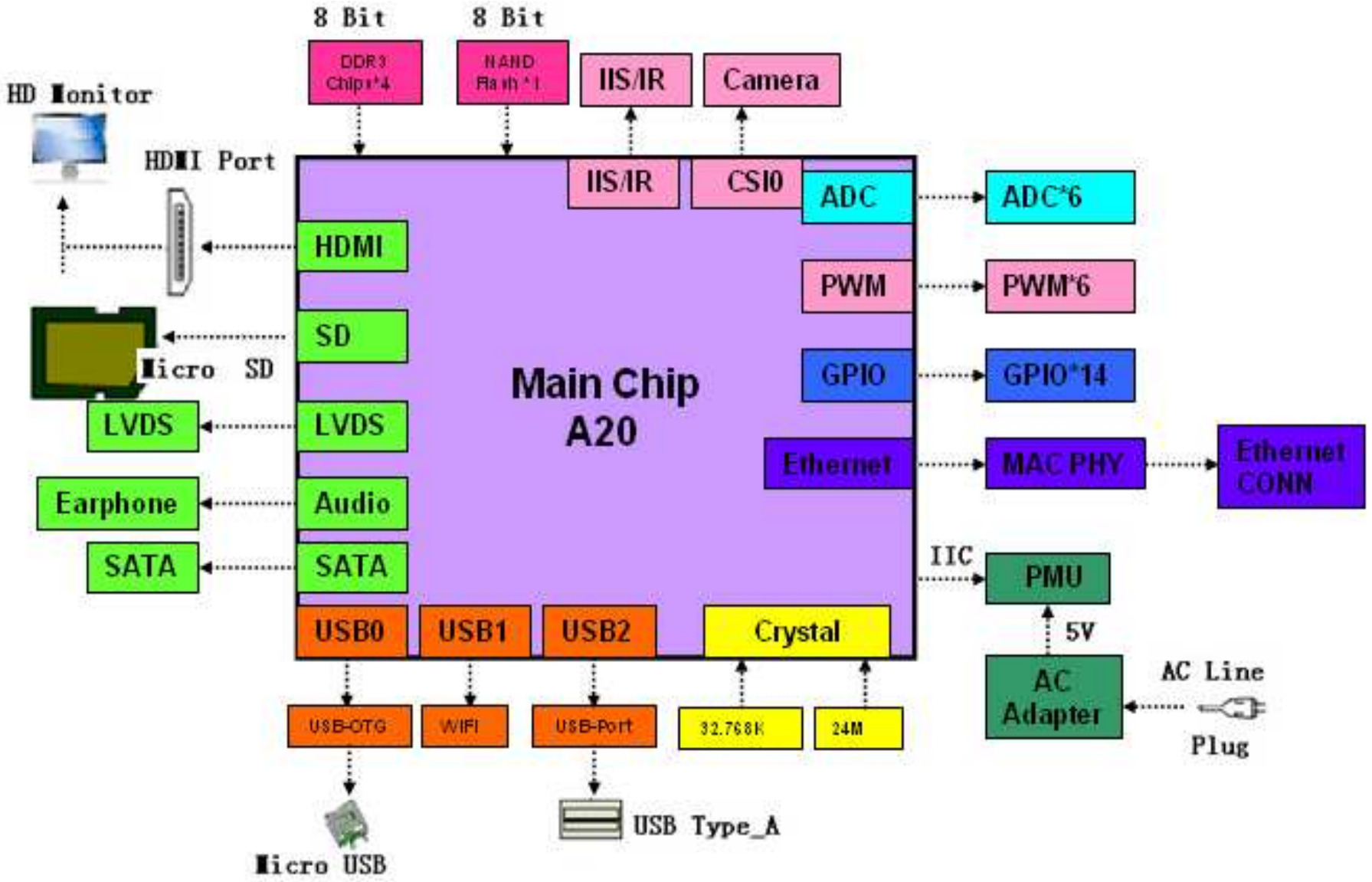
Rev	Date	Description	Drawn
V1.0	2012-11-14	First release	

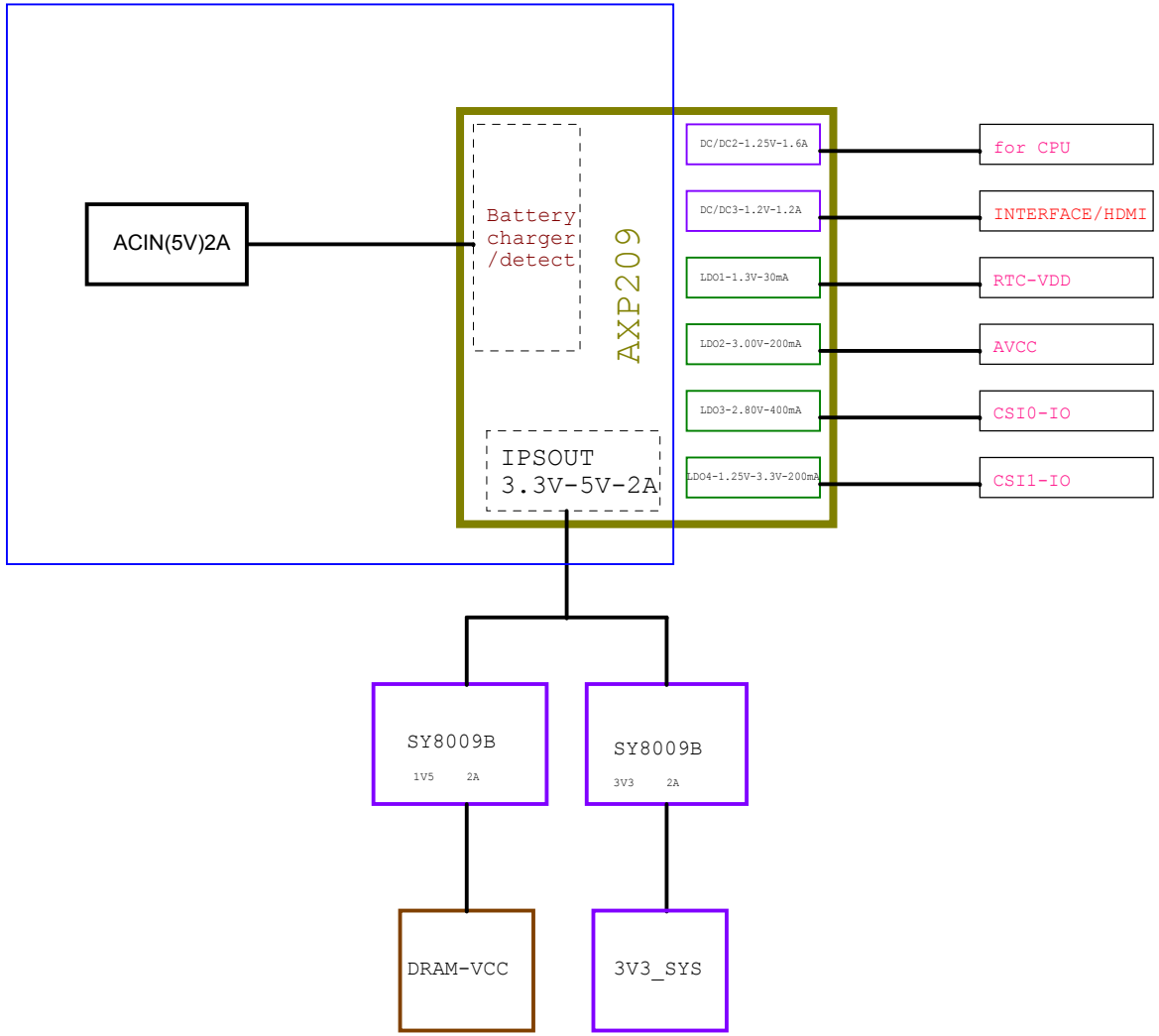
Title	pcDuino		
Size A4	Document Number COVER		Rev V3.0
Date: Tuesday, December 24, 2013	Sheet 1 of 16		



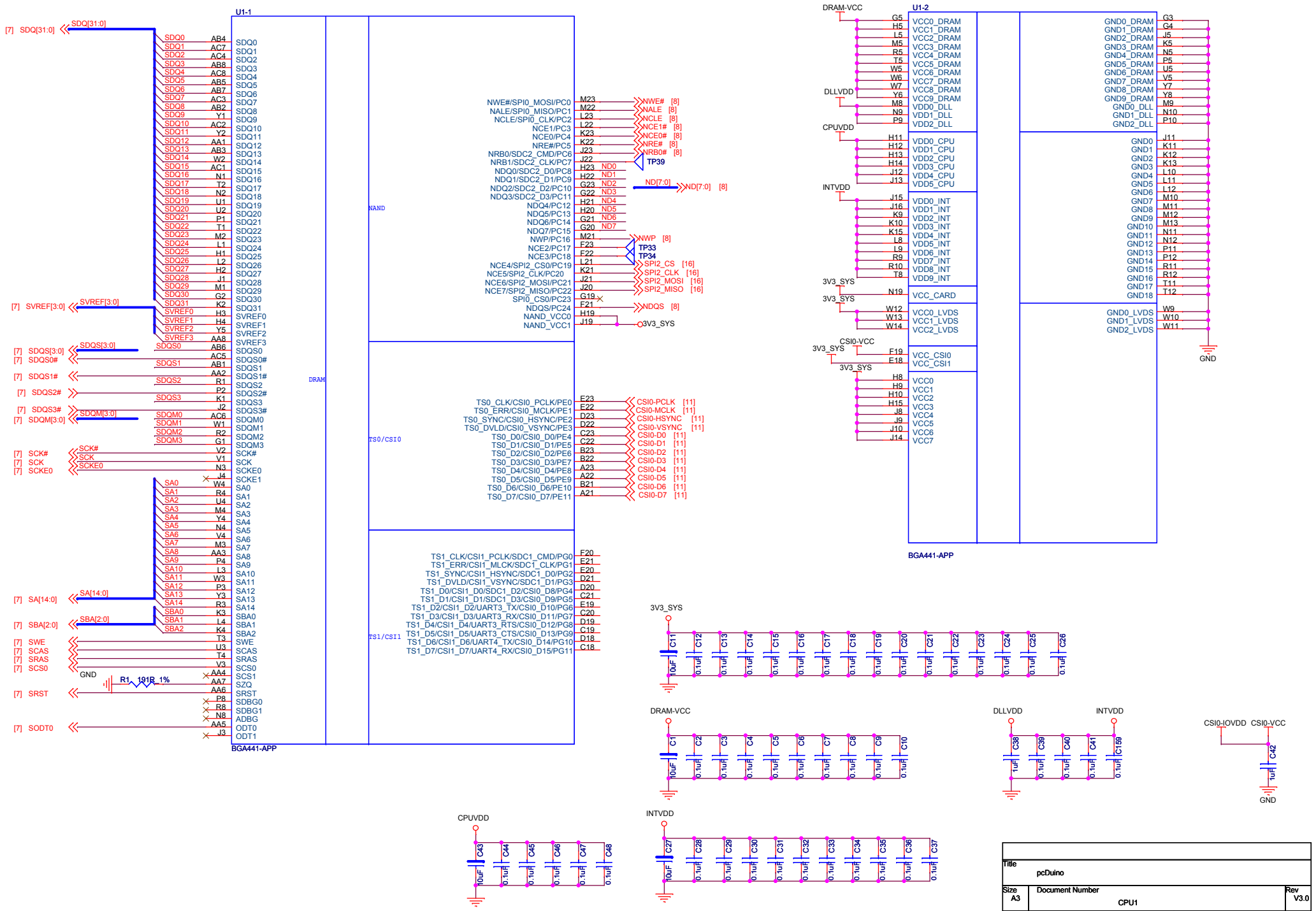
Title: pcDuino		
Size: A4	Document Number: BLOCK	Rev: V3.0
Date: Tuesday, December 24, 2013	Sheet 2 of 16	

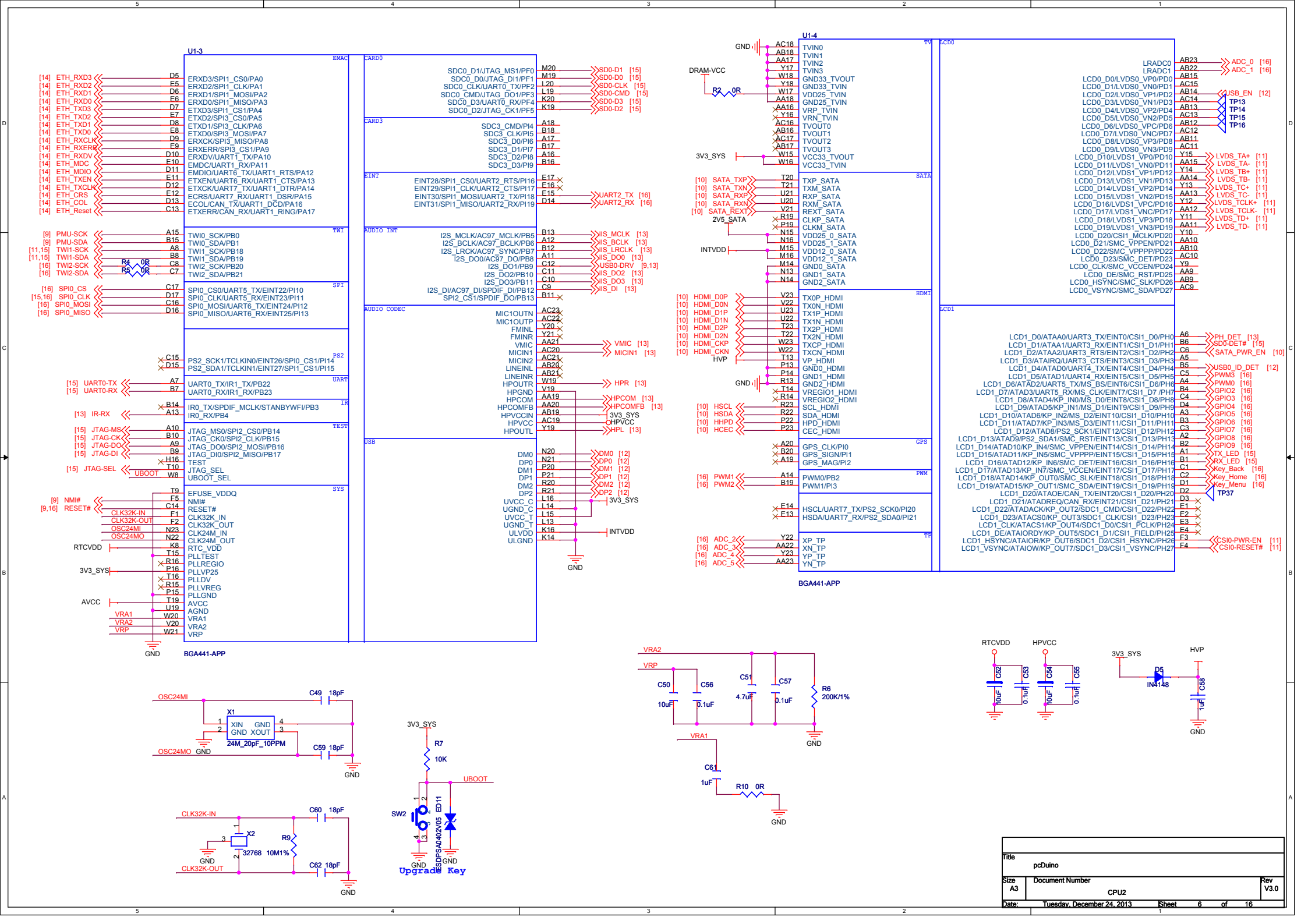
Function	GPIO List	Pin Name	Direction	Net Name	Active	Default	Description
SPI1	ERXD2/SPI1_CLK/PA1	E5	O	SPI1_CLK			SPI1_LINK
	ERXD1/SPI1_MOSI/PA2	D6	O	SPI1_MOSI			
	ERXD0/SPI1_MISO/PA3	E6	I	SPI1_MISO			
SPI0	SPI00_CS0/UART5_TX/EINT22/PI10	C17	O	SPI0_CS			SPI0_LINK/PWM
	SPI00_CLK/UART5_RX/EINT23/PI11	D17	O	SPI0_CLK			SPI01_LINK
	SPI00_MOSI/UART6_TX/EINT24/PI12	C16	O	SPI0_MOSI			SPI0_LINK/PWM
	SPI00_MISO/UART6_RX/EINT25/PI13	D16	I	SPI0_MISO			SPI0_LINK
IIC0	TWI0_SCK/PB0	A15	O	PMU-SCK			PMU_IIC_LINK
	TWI0_SDA/PB1	B15	I/O	PMU-SDA			
IIC1	TWI1_SCK/PB18	A8	O	TWI1-SCK			IIC_LINK
	TWI1_SDA/PB19	B8	I/O	TWI1-SDA			
IIC2	TWI2_SCK/PB20	C8	O	TWI2-SCK			CONN_IIC_LINK
	TWI2_SDA/PB21	C7	I/O	TWI2-SDA			
IIS	I2S_MCLK/AC97_MCLK/PB5	B13	O	IIS_MCLK			I2S_LINK
	I2S_BCLK/AC97_BCLK/PB6	A12	O	IIS_BCLK			
	I2S_LRCK/AC97_SYNC/PB7	B12	O	IIS_LRCLK			
	I2S_DO0/AC97_DO/PB8	A11	O	IIS_DO0			
	I2S_DO2/PB10	C11	O	IIS_DO2			
	I2S_DO3/PB11	C10	O	IIS_DO3			
	I2S_DI/AC97_DI/SPDIF_DI/PB12	C9	I	IIS_DI			
UART0	UART0_TX/IR1_TX/PB22	A7	O	UART0-TX			UART0_LINK For debug
	UART0_RX/IR1_RX/PB23	B7	I	UART0-RX			
JTAG	JTAG_MS0/SPI2_CS0/PB14	A10	O	JTAG-MS			JJTAG_LINK
	JTAG_CK0/SPI2_CLK/PB15	B10	O	JTAG-CK			
	JTAG_DO0/SPI2_MOSI/PB16	A9	O	JTAG-DO			
	JTAG_DI0/SPI2_MISO/PB17	B9	I	JTAG-DI			
	JTAGSEL	T10	I	JTAG-SEL			
	NMI#	F5	O	NMI#			PMU_INT
SD0	SDC0_D1/JATG_MS1/PF0	M20	I/O	SD0-D1			SD_CARD
	SDC0_D0/JATG_DI1/PF1	M19	I/O	SD0-D0			
	SDC0_CLK/UART0_TX/PF2	L20	O	SD0-CLK			
	SDC0_CMD/JTAG_DO1/PF3	L19	O	SD0-CMD			
	SDC0_D3/UART0_RX/PF4	K20	I/O	SD0-D3			
	SDC0_D2/JATG_CK1/PF5	K19	I/O	SD0-D2			
UART2	EINT30/SPI1_MOSI/UART2_TX/PI18	E15	O	UART2_TX			UART2_LINK/GPIO
	EINT30/SPI1_MISO/UART2_RX/PI19	D14	I	UART2_RX			
	I2S_DO1/PB9	C12	I	USB0-DRV	H	H	PMU_CHANEL_SEL/IIS
USB0	DM0	N20	I/O	DM0			OTG_USB
	DP0	N21	I/O	DP0			
USB1	DM1	P20	I/O	DM1			USB_WIFI
	DP1	P21	I/O	DP1			
USB2	DM2	R20	I/O	DM2			USB_Type_A
	DP2	R21	I/O	DP2			
HDMI	SCL_HDMI	R23	O	HSCL			HDMI_DDC
	SDA_HDMI	R22	I/O	HSDA			
	HPD_HDMI	P22	I	HHPD	H	L	HDMI_HPD
	CEC_HDMI	P23	I	HCEC	H	L	HDMI_CEC
PWM	PWM0/PB2	A14	O	PWM1			PWM
	PWM1/PI3	B19	O	PWM2			PWM
ADC	XP_TP	Y22	I	ADC_2			6_CHANEL_ADC
	XN_TP	AA22	I	ADC_3			
	YP_TP	Y23	I	ADC_4			
	YN_TP	AA23	I	ADC_5			
	LRADC0	AB23	I	ADC_0			
	LRADC1	AB22	I	ADC_1			
	LCD1_D1/ATAA1/UART3_RX/EINT1/CSI1_D1/PH1	B6	I	SD0-DET#	L	H	SD_CARD_DET
	LCD4_D1/ATAD0/UART4_TX/EINT4/CSI1_D4/PH4	B5	I	USB0_ID_DET	L	H	OTG_USB_DET
PWM	LCD5_D1/ATAD1/UART4_RX/EINT5/CSI1_D5/PH5	C5	I/O	PWM3			PWM
	LCD6_D1/ATAD2/UART5_TX/EINT6/CSI1_D6/PH6	A4	I/O	PWM0			PWM
Earphone	VMIC	AA21	I	VMIC			HP_LINK
	MICIN1	AC20	I	MICIN1			
	HPOUTR	W19	O	HPR			
	HPCOM	AA19	I	HPCOM			
	HPCOMFB	AA20	I	HPCOMFB			
	HPOUTL	Y19	O	HPL			
IR	IR0_RX/PB4	A13	I	IR-RX		H	IR
Camera	TS0_CLK/CSI0_PCLK/PE0	E23	I	CSI0-PCLK			CSI_LINK
	TS0_ERR/CSI0_MCLK/PE1	E22	I	CSI0-MCLK			
	TS0_SYNC/CSI0_HSYNC/PE2	D23	I	CSI0-HSYNC			
	TS0_DVLD/CSI0_VSYNC/PE3	D22	I	CSI0-VSYNC			
	TS0_D0/CSI0_D0/PE4	C23	I	CSI0-D0			
	TS0_D1/CSI0_D1/PE5	C22	I	CSI0-D1			
	TS0_D2/CSI0_D2/PE6	B23	I	CSI0-D2			
	TS0_D3/CSI0_D3/PE7	B22	I	CSI0-D3			
	TS0_D4/CSI0_D4/PE8	A23	I	CSI0-D4			
	TS0_D5/CSI0_D5/PE9	A22	I	CSI0-D5			
	TS0_D6/CSI0_D6/PE10	B21	I	CSI0-D6			
	TS0_D7/CSI0_D7/PE11	A21	I	CSI0-D7			
SATA	TXP_SATA	T20	O	SATA_TXP			SATA_LINK
	TXM_SATA	T21	O	SATA_TXN			
	RXP_SATA	U21	O	SATA_RXP			
	RXM_SATA	U20	O	SATA_RXN			
LVDS	LCD0_D10/LVDS1_VP0/PD10	Y15	O	LVDS_TA+			LVDS_LINK
	LCD0_D11/LVDS1_VN0/PD11	AA15	O	LVDS_TA-			
	LCD0_D12/LVDS1_VP1/PD12	Y14	O	LVDS_TB+			
	LCD0_D13/LVDS1_VN1/PD13	AA14	O	LVDS_TB-			
	LCD0_D14/LVDS1_VP2/PD14	Y13	O	LVDS_TC+			
	LCD0_D15/LVDS1_VN2/PD15	AA13	O	LVDS_TC-			
	LCD0_D16/LVDS1_VPC/PD16	Y12	O	LVDS_TCLK+			
	LCD0_D17/LVDS1_VNC/PD17	AA12	O	LVDS_TCLK-			
	LCD0_D18/LVDS1_VP3/PD18	Y11	O	LVDS_TD+			
	LCD0_D19/LVDS1_VN3/PD19	AA11	O	LVDS_TD-			
GPIO	LCD1_D0/ATAD0/UART3_TX/EINT0/CSI1_D0/PH0	A6	I	PH_DET	H	L	PH_DET
	LCD1_D2/ATAD2/UART3_RTS/EINT2/CSI1_D2/PH2	C6	O	SATA_PWR_EN	H	L	SATA_PWR_EN
	LCD1_D7/ATAD3/UART5_RX/EINT7/CSI1_D7/PH7	B4	I/O	GPIO2			GPIO
	LCD1_D8/ATAD4/EINT8/CSI1_D8/PH8	C4	I/O	GPIO3			GPIO
	LCD1_D9/ATAD5/EINT9/CSI1_D9/PH9	D4	I/O	GPIO4			GPIO
	LCD1_D10/ATAD6/EINT10/CSI1_D10/PH10	A3	I/O	GPIO5			GPIO
	LCD1_D11/ATAD7/EINT11/CSI1_D11/PH11	B3	I/O	GPIO6			GPIO
	LCD12_D12/ATAD8/EINT12/CSI1_D12/PH12	C3	I/O	GPIO7			GPIO
	LCD1_D13/ATAD9/EINT13/CSI1_D13/PH13	A2	I/O	GPIO8			GPIO
	LCD14_D14/ATAD10/EINT14/CSI1_D14/PH14	B2	I/O	GPIO9			GPIO
	LCD1_D15/ATAD11/EINT15/CSI1_D15/PH15	A1	O	TX_LED	L	H	TX_LED
	LCD1_D16/ATAD12/EINT16/CSI1_D16/PH16	B1	O	RX_LED	L	H	RX_LED
	LCD1_D17/ATAD13/EINT17/CSI1_D16/PH17	C1	I	Key_Back	L	H	Key_Back
	LCD1_D18/ATAD14/EINT18/CSI1_D16/PH18	C2	I	Key_Home	L	H	Key_Home
	LCD1_D19/ATAD15/EINT19/CSI1_D16/PH19	D1	I	Key_Menu	L	H	Key_Menu
	LCD1_HSYNC/ATAI0R/SDC1_D2/CSI1_HSYNC/PH26	F3	O	CSI0-PWR-EN	H	H	CSI0-PWR-EN
	LCD1_VSYNC/ATAI0W/SDC1_D3/CSI1_VSYNC/PH27	F4	O	CSI0-RESET#	L	H	CSI0-RESET#

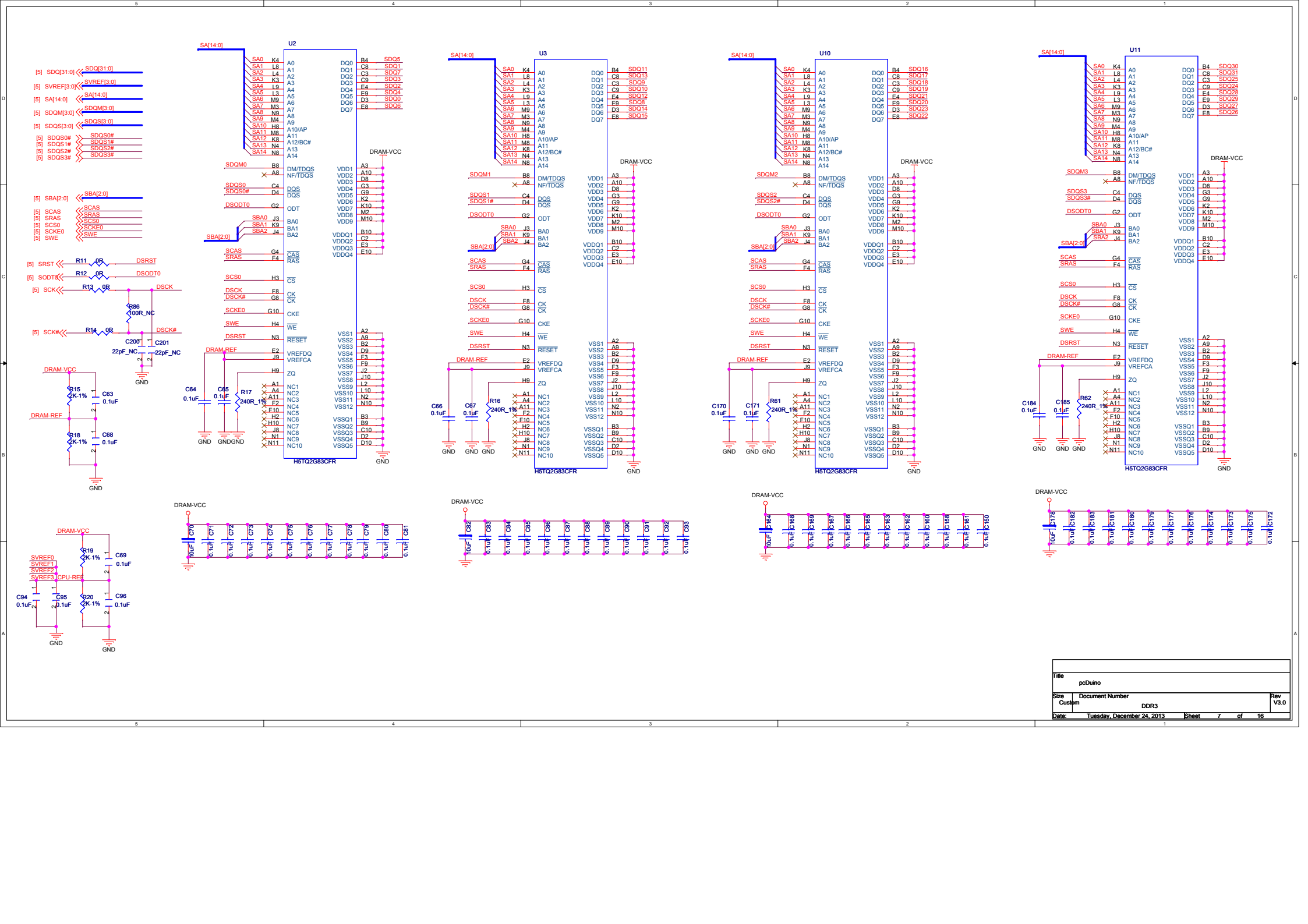
Title	pcDuino		
Size: Custom	Document Number: IO_List		Rev: V3.0
Date: Tuesday, December 24, 2013	Sheet 3 of 16		

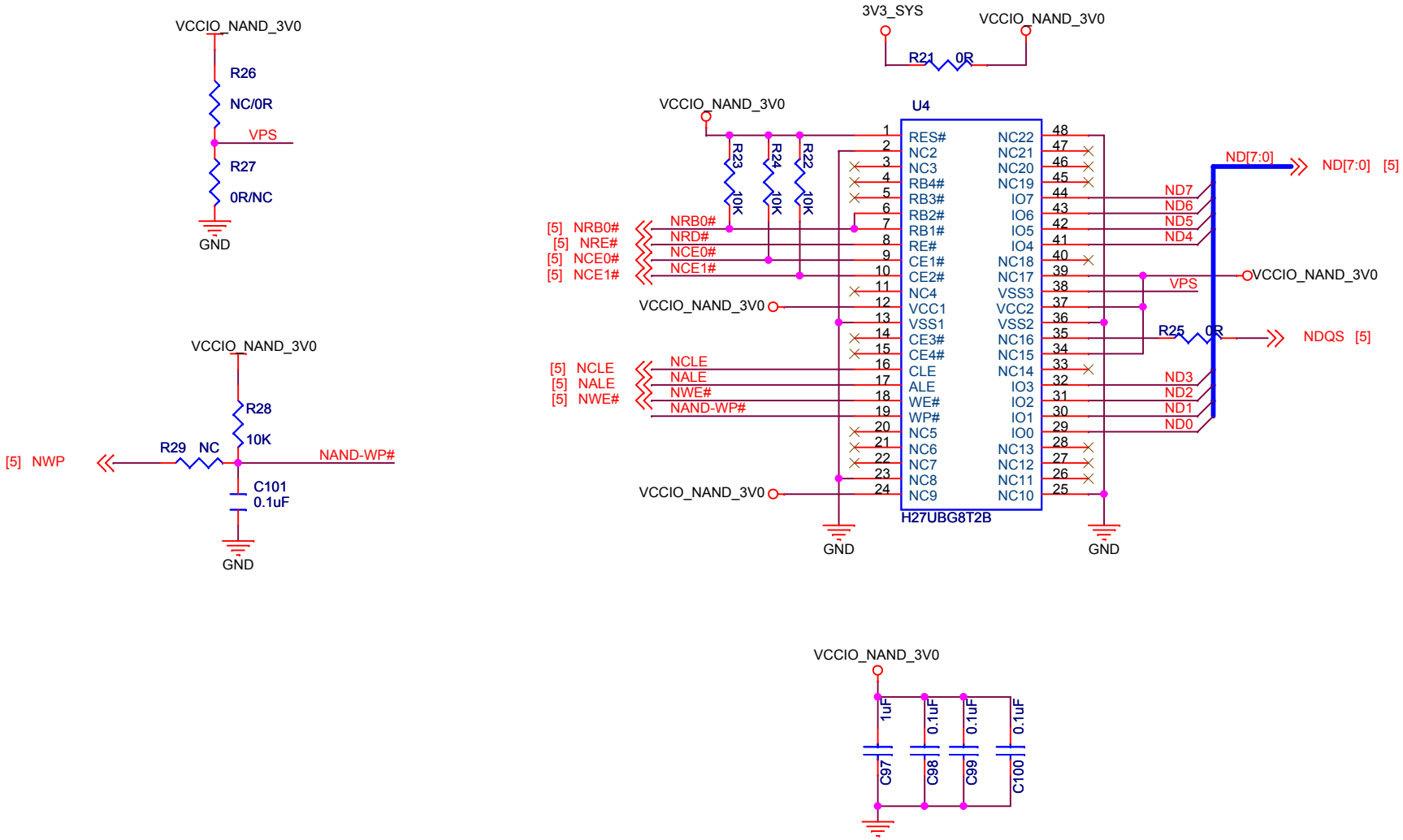


Title	pcDuino	
Size A3	Document Number Power Tree	Rev V3.0
Date: Tuesday, December 24, 2013	Sheet 4 of 16	

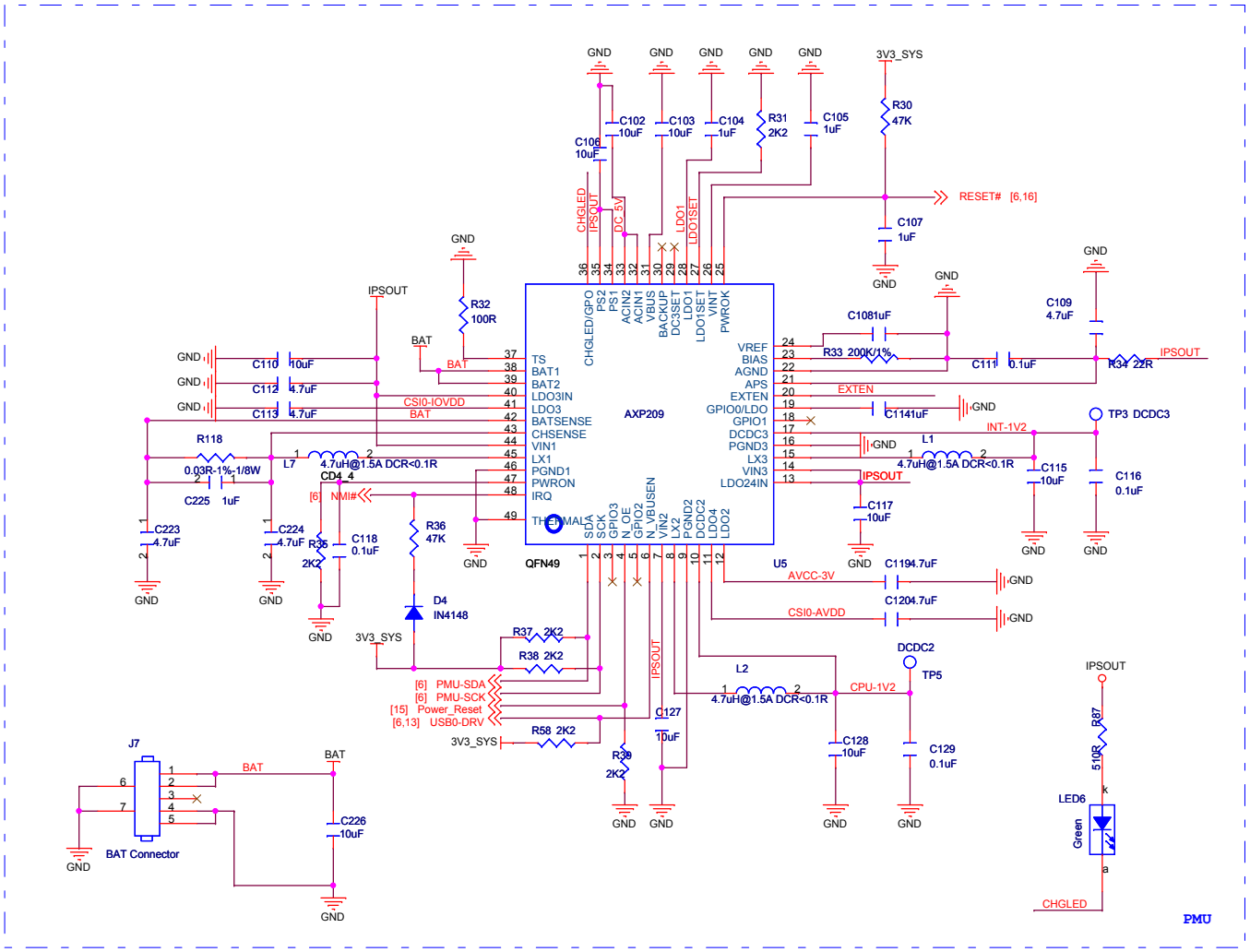

U1-1
BGA441-APP
NAND
DRAM
TS0/CSI0
TS1/CSI1
U1-2
BGA441-APP
DRAM-VCC
DLLVDD
CPUVDD
INTVDD
CSI0-VCC
3V3_SYS
GND
SDQ[31:0]
SVREF[3:0]
SDQS[3:0]
SDQS0#
SDQS1#
SDQS2#
SDQS3#
SDQM[3:0]
SCK#
SCK
SCKE0
SA[14:0]
SBA[2:0]
SWE
SCAS
SRAS
SCS0
SRST
SODT0
R1 191R 1%
ND[7:0]
TP39
TP33
TP34
SPI2_CS [16]
SPI2_CLK [16]
SPI2_MOSI [16]
SPI2_MISO [16]
NWE# [8]
NALE [8]
NCLE [8]
NCE1# [8]
NCE0# [8]
NRE# [8]
NRB0# [8]
NWP [8]
NDQS [8]
CSI0-PCLK [11]
CSI0-MCLK [11]
CSI0-HSYNC [11]
CSI0-VSYNC [11]
CSI0-D0 [11]
CSI0-D1 [11]
CSI0-D2 [11]
CSI0-D3 [11]
CSI0-D4 [11]
CSI0-D5 [11]
CSI0-D6 [11]
CSI0-D7 [11]
CSI0-IOVDD
Title
pcDuino
Size A3
Document Number CPU1
Rev V3.0
Date: Tuesday, December 24, 2013
Sheet 5 of 16

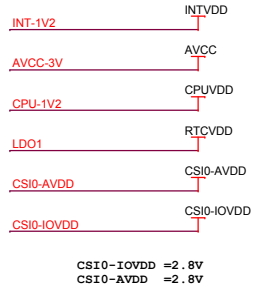

U1-3
EMAC
ERXD3/SPI1_CS0/PA0
ERXD2/SPI1_CLK/PA1
ERXD1/SPI1_MOSI/PA2
ERXD0/SPI1_MISO/PA3
ETXD3/SPI1_CS1/PA4
ETXD2/SPI3_CS0/PA5
ETXD1/SPI3_CLK/PA6
ETXD0/SPI3_MOSI/PA7
ERXCK/SPI3_MISO/PA8
ERXERR/SPI3_CS1/PA9
ERXDV/UART1_TX/PA10
EMDC/UART1_RX/PA11
EMDIO/UART6_TX/UART1_RTS/PA12
ETXEN/UART6_RX/UART1_CTS/PA13
ETXCK/UART7_TX/UART1_DTR/PA14
ECRS/UART7_RX/UART1_DSR/PA15
ECOL/CAN_TX/UART1_DCD/PA16
ETXERR/CAN_RX/UART1_RING/PA17
TWI
TWI0_SCK/PB0
TWI0_SDA/PB1
TWI1_SCK/PB18
TWI1_SDA/PB19
TWI2_SCK/PB20
TWI2_SDA/PB21
SPI
SPI0_CS0/UART5_TX/EINT22/PI10
SPI0_CLK/UART5_RX/EINT23/PI11
SPI0_MOSI/UART6_TX/EINT24/PI12
SPI0_MISO/UART6_RX/EINT25/PI13
PS2
PS2_SCK1/TCLKIN0/EINT26/SPI0_CS1/PI14
PS2_SDA1/TCLKIN1/EINT27/SPI1_CS1/PI15
UART
UART0_TX/IR1_TX/PB22
UART0_RX/IR1_RX/PB23
IR
IR0_TX/SPDIF_MCLK/STANBYWFI/PB3
IR0_RX/PB4
TEST
JTAG_MS0/SPI2_CS0/PB14
JTAG_CK0/SPI2_CLK/PB15
JTAG_DO0/SPI2_MOSI/PB16
JTAG_DI0/SPI2_MISO/PB17
TEST
JTAG_SEL
UBOOT_SEL
SYS
EFUSE_VDDQ
NMI#
RESET#
CLK32K_IN
CLK32K_OUT
CLK24M_IN
CLK24M_OUT
RTC_VDD
PLLTEST
PLLREGIO
PLLVP25
PLLDV
PLLVREG
PLLGND
AVCC
AGND
VRA1
VRA2
VRP
CARD0
SDC0_D1/JTAG_MS1/PF0
SDC0_D0/JTAG_DI1/PF1
SDC0_CLK/UART0_TX/PF2
SDC0_CMD/JTAG_DO1/PF3
SDC0_D3/UART0_RX/PF4
SDC0_D2/JTAG_CK1/PF5
CARD3
SDC3_CMD/PI4
SDC3_CLK/PI5
SDC3_D0/PI6
SDC3_D1/PI7
SDC3_D2/PI8
SDC3_D3/PI9
EINT
EINT28/SPI1_CS0/UART2_RTS/PI16
EINT29/SPI1_CLK/UART2_CTS/PI17
EINT30/SPI1_MOSI/UART2_TX/PI18
EINT31/SPI1_MISO/UART2_RX/PI19
AUDIO INT
I2S_MCLK/AC97_MCLK/PB5
I2S_BCLK/AC97_BCLK/PB6
I2S_LRCK/AC97_SYNC/PB7
I2S_DO0/AC97_DO/PB8
I2S_DO1/PB9
I2S_DO2/PB10
I2S_DO3/PB11
I2S_DI/AC97_DI/SPDIF_DI/PB12
SPI2_CS1/SPDIF_DO/PB13
AUDIO CODEC
MIC1OUTN
MIC1OUTP
FMINL
FMINR
VMIC
MICIN1
MICIN2
LINEINL
LINEINR
HPOUTR
HPGND
HPCOM
HPCOMFB
HPVCCIN
HPVCC
HPOUTL
USB
DM0
DP0
DM1
DP1
DM2
DP2
UVCC_C
UGND_C
UVCC_T
UGND_T
ULVDD
ULGND
BGA441-APP
U1-4
TV
TVIN0
TVIN1
TVIN2
TVIN3
GND33_TVOUT
GND33_TVIN
VDD25_TVIN
GND25_TVIN
VRP_TVIN
VRN_TVIN
TVOUT0
TVOUT1
TVOUT2
TVOUT3
VCC33_TVOUT
VCC33_TVIN
SATA
TXP_SATA
TXM_SATA
RXP_SATA
RXM_SATA
REXT_SATA
CLKP_SATA
CLKM_SATA
VDD25_0_SATA
VDD25_1_SATA
VDD12_0_SATA
VDD12_1_SATA
GND0_SATA
GND1_SATA
GND2_SATA
HDMI
TX0P_HDMI
TX0N_HDMI
TX1P_HDMI
TX1N_HDMI
TX2P_HDMI
TX2N_HDMI
TXCP_HDMI
TXCN_HDMI
VP_HDMI
GND0_HDMI
GND1_HDMI
GND2_HDMI
VREGIO1_HDMI
VREGIO2_HDMI
SCL_HDMI
SDA_HDMI
HPD_HDMI
CEC_HDMI
GPS
GPS_CLK/PI0
GPS_SIGN/PI1
GPS_MAG/PI2
PWM
PWM0/PB2
PWM1/PI3
HSCL/UART7_TX/PS2_SCK0/PI20
HSDA/UART7_RX/PS2_SDA0/PI21
TP
XP_TP
XN_TP
YP_TP
YN_TP
LCD0
LRADC0
LRADC1
LCD0_D0/LVDS0_VP0/PD0
LCD0_D1/LVDS0_VN0/PD1
LCD0_D2/LVDS0_VP1/PD2
LCD0_D3/LVDS0_VN1/PD3
LCD0_D4/LVDS0_VP2/PD4
LCD0_D5/LVDS0_VN2/PD5
LCD0_D6/LVDS0_VPC/PD6
LCD0_D7/LVDS0_VNC/PD7
LCD0_D8/LVDS0_VP3/PD8
LCD0_D9/LVDS0_VN3/PD9
LCD0_D10/LVDS1_VP0/PD10
LCD0_D11/LVDS1_VN0/PD11
LCD0_D12/LVDS1_VP1/PD12
LCD0_D13/LVDS1_VN1/PD13
LCD0_D14/LVDS1_VP2/PD14
LCD0_D15/LVDS1_VN2/PD15
LCD0_D16/LVDS1_VPC/PD16
LCD0_D17/LVDS1_VNC/PD17
LCD0_D18/LVDS1_VP3/PD18
LCD0_D19/LVDS1_VN3/PD19
LCD0_D20/CSI1_MCLK/PD20
LCD0_D21/SMC_VPPEN/PD21
LCD0_D22/SMC_VPPPP/PD22
LCD0_D23/SMC_DET/PD23
LCD0_CLK/SMC_VCCEN/PD24
LCD0_DE/SMC_RST/PD25
LCD0_HSYNC/SMC_SLK/PD26
LCD0_VSYNC/SMC_SDA/PD27
LCD1
LCD1_D0/ATAA0/UART3_TX/EINT0/CSI1_D0/PH0
LCD1_D1/ATAA1/UART3_RX/EINT1/CSI1_D1/PH1
LCD1_D2/ATAA2/UART3_RTS/EINT2/CSI1_D2/PH2
LCD1_D3/ATAIRQ/UART3_CTS/EINT3/CSI1_D3/PH3
LCD1_D4/ATAD0/UART4_TX/EINT4/CSI1_D4/PH4
LCD1_D5/ATAD1/UART4_RX/EINT5/CSI1_D5/PH5
LCD1_D6/ATAD2/UART5_TX/MS_BS/EINT6/CSI1_D6/PH6
LCD1_D7/ATAD3/UART5_RX/MS_CLK/EINT7/CSI1_D7 /PH7
LCD1_D8/ATAD4/KP_IN0/MS_D0/EINT8/CSI1_D8/PH8
LCD1_D9/ATAD5/KP_IN1/MS_D1/EINT9/CSI1_D9/PH9
LCD1_D10/ATAD6/KP_IN2/MS_D2/EINT10/CSI1_D10/PH10
LCD1_D11/ATAD7/KP_IN3/MS_D3/EINT11/CSI1_D11/PH11
LCD1_D12/ATAD8/PS2_SCK1/EINT12/CSI1_D12/PH12
LCD1_D13/ATAD9/PS2_SDA1/SMC_RST/EINT13/CSI1_D13/PH13
LCD1_D14/ATAD10/KP_IN4/SMC_VPPEN/EINT14/CSI1_D14/PH14
LCD1_D15/ATAD11/KP_IN5/SMC_VPPPP/EINT15/CSI1_D15/PH15
LCD1_D16/ATAD12/KP_IN6/SMC_DET/EINT16/CSI1_D16/PH16
LCD1_D17/ATAD13/KP_IN7/SMC_VCCEN/EINT17/CSI1_D17/PH17
LCD1_D18/ATAD14/KP_OUT0/SMC_SLK/EINT18/CSI1_D18/PH18
LCD1_D19/ATAD15/KP_OUT1/SMC_SDA/EINT19/CSI1_D19/PH19
LCD1_D20/ATAOE/CAN_TX/EINT20/CSI1_D20/PH20
LCD1_D21/ATADREQ/CAN_RX/EINT21/CSI1_D21/PH21
LCD1_D22/ATADACK/KP_OUT2/SDC1_CMD/CSI1_D22/PH22
LCD1_D23/ATACS0/KP_OUT3/SDC1_CLK/CSI1_D23/PH23
LCD1_CLK/ATACS1/KP_OUT4/SDC1_D0/CSI1_PCLK/PH24
LCD1_DE/ATAIORDY/KP_OUT5/SDC1_D1/CSI1_FIELD/PH25
LCD1_HSYNC/ATAIOR/KP_OUT6/SDC1_D2/CSI1_HSYNC/PH26
LCD1_VSYNC/ATAIOW/KP_OUT7/SDC1_D3/CSI1_VSYNC/PH27
BGA441-APP
OSC24MI
OSC24MO
X1
24M_20pF_10PPM
C49 18pF
C59 18pF
CLK32K-IN
CLK32K-OUT
X2
32768
10M1%
R9
C60 18pF
C62 18pF
3V3_SYS
R7
10K
UBOOT
SW2
ED11
ESDPSA0402V05
Upgrade Key
VRA2
VRP
C50 10uF
C56 0.1uF
C51 4.7uF
C57 0.1uF
R6 200K/1%
VRA1
C61 1uF
R10 0R
RTCVDD
HPVCC
C52 10uF
C53 0.1uF
C54 10uF
C55 0.1uF
HVP
D5 IN4148
C58 1uF
DRAM-VCC
R2 0R
INTVDD
2V5_SATA
R4 0R
R5 0R
Title
pcDuino
Size A3
Document Number CPU2
Rev V3.0
Date: Tuesday, December 24, 2013
Sheet 6 of 16

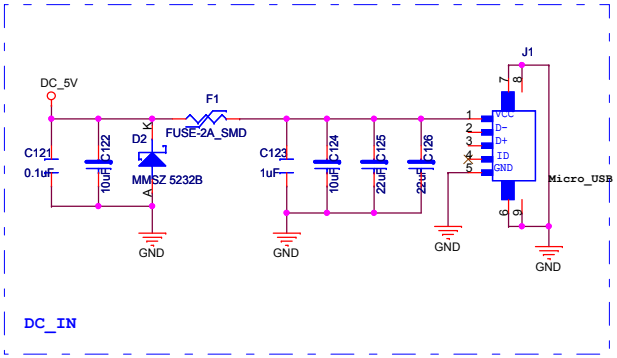

U2
U3
U10
U11
H5TQ2G83CFR
SA[14:0]
SDQ[31:0]
SVREF[3:0]
SDQM[3:0]
SDQS[3:0]
SDQS0#
SDQS1#
SDQS2#
SDQS3#
SBA[2:0]
SCAS
SRAS
SCS0
SCKE0
SWE
SRST
SODT0
SCK
SCK#
DSRST
DSODT0
DSCK
DSCK#
R11 0R
R12 0R
R13 0R
R14 0R
R86 100R_NC
C200 22pF_NC
C201 22pF_NC
DRAM-VCC
DRAM-REF
R15 2K-1%
R18 2K-1%
C63 0.1uF
C68 0.1uF
C64 0.1uF
C65 0.1uF
R17 240R_1%
C66 0.1uF
C67 0.1uF
R16 240R_1%
C170 0.1uF
C171 0.1uF
R61 240R_1%
C184 0.1uF
C185 0.1uF
R62 240R_1%
GND
SVREF0
SVREF1
SVREF2
SVREF3
CPU-REF
R19 2K-1%
R20 2K-1%
C69 0.1uF
C94 0.1uF
C95 0.1uF
C96 0.1uF
C70 10uF
C71 0.1uF
C72 0.1uF
C73 0.1uF
C74 0.1uF
C75 0.1uF
C76 0.1uF
C77 0.1uF
C78 0.1uF
C79 0.1uF
C80 0.1uF
C81 0.1uF
C82 10uF
C83 0.1uF
C84 0.1uF
C85 0.1uF
C86 0.1uF
C87 0.1uF
C88 0.1uF
C89 0.1uF
C90 0.1uF
C91 0.1uF
C92 0.1uF
C93 0.1uF
C164 10uF
C168 0.1uF
C169 0.1uF
C167 0.1uF
C166 0.1uF
C165 0.1uF
C163 0.1uF
C162 0.1uF
C160 0.1uF
C158 0.1uF
C161 0.1uF
C150 0.1uF
C178 10uF
C182 0.1uF
C183 0.1uF
C181 0.1uF
C180 0.1uF
C179 0.1uF
C177 0.1uF
C176 0.1uF
C174 0.1uF
C173 0.1uF
C175 0.1uF
C172 0.1uF
Title
pcDuino
Size
Custom
Document Number
DDR3
Rev
V3.0
Date:
Tuesday, December 24, 2013
Sheet
7
of
16

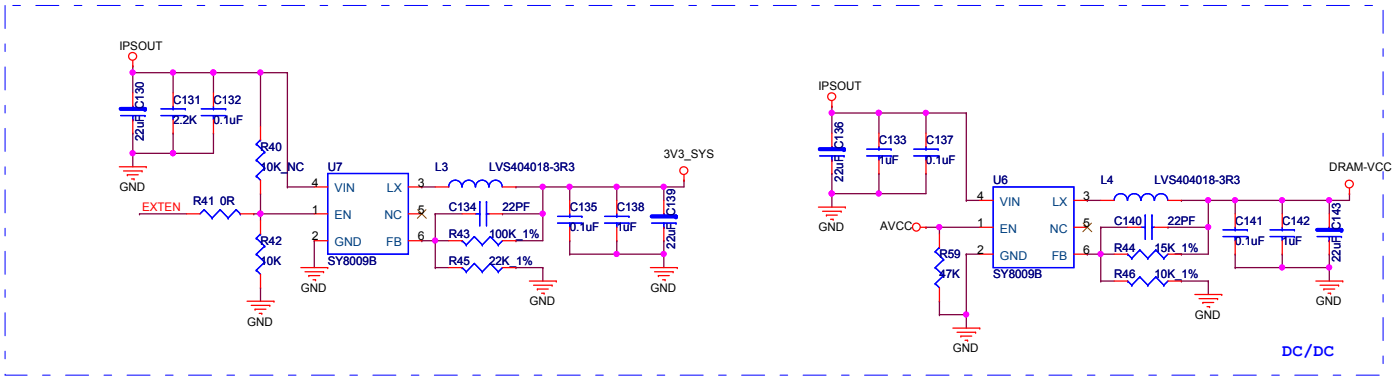


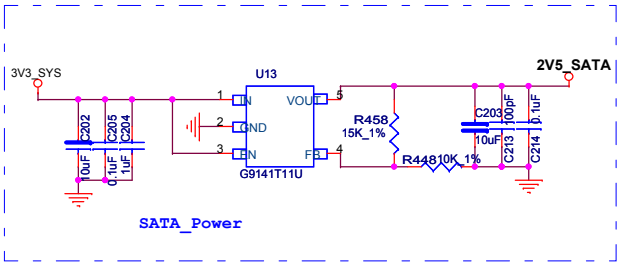
Title	pcDuino	
Size A4	Document Number NAND	Rev V3.0
Date: Tuesday, December 24, 2013	Sheet 8 of 16	



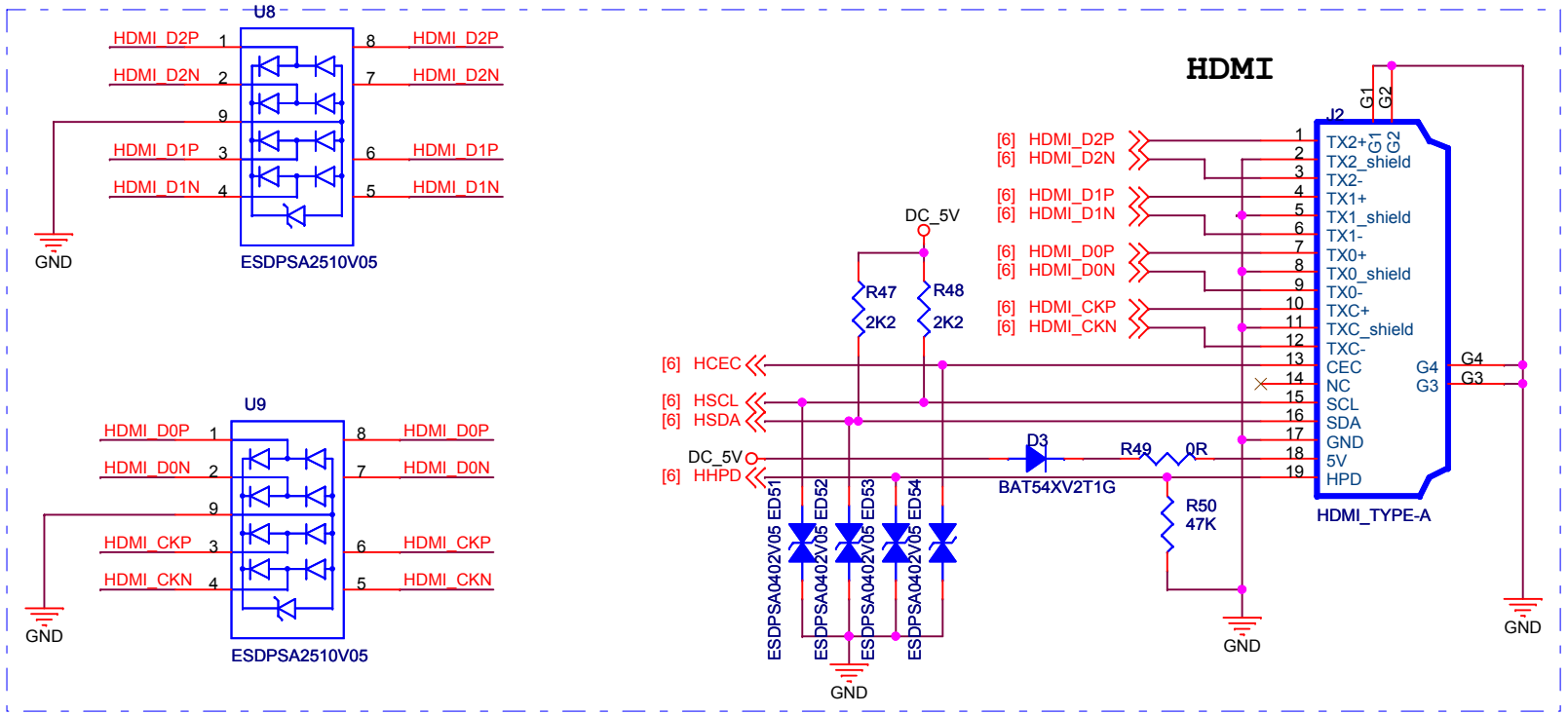


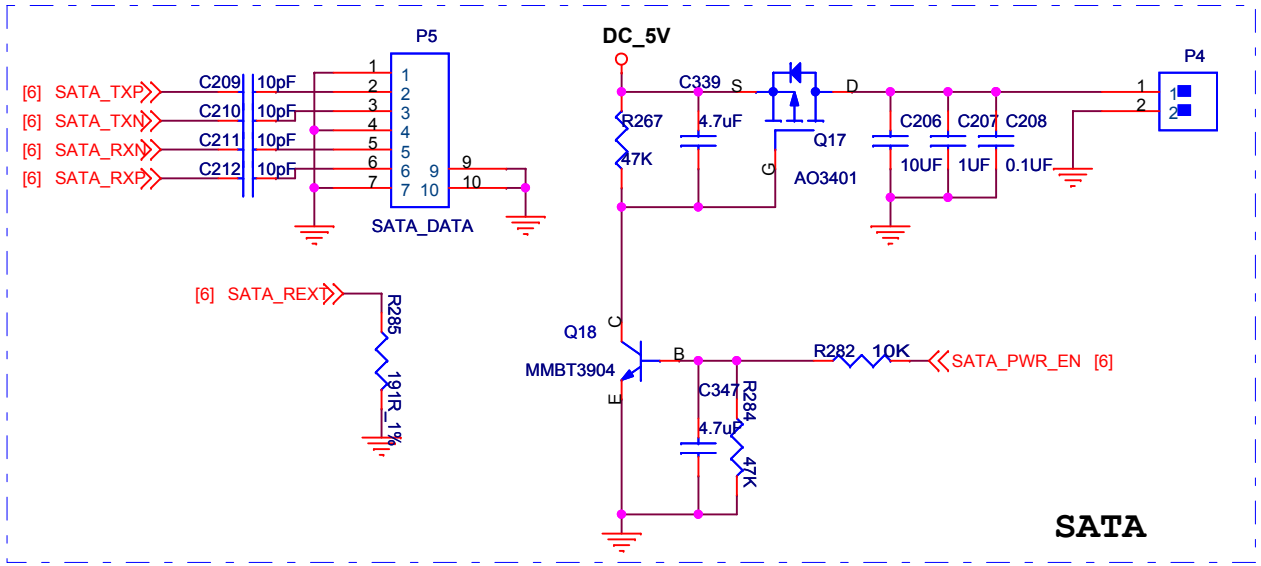




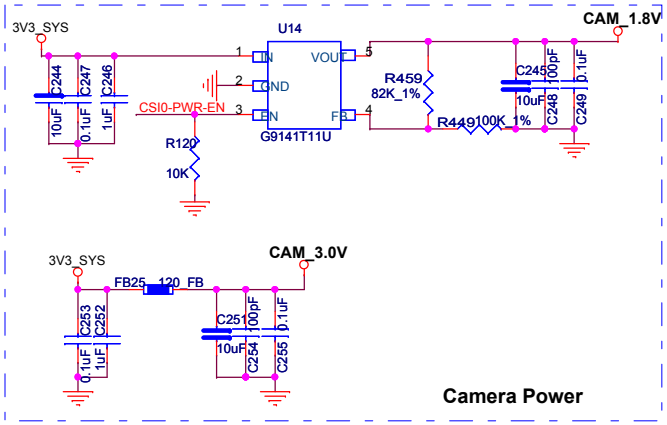


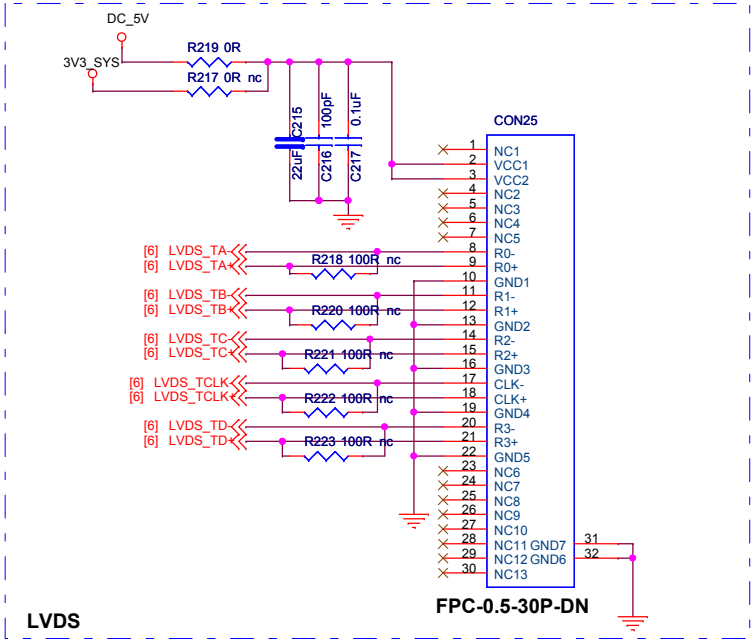
Title: pcDuino			
Size: A3	Document Number: Power		Rev: V3.0
Date: Tuesday, December 24, 2013	Sheet 9 of 16		

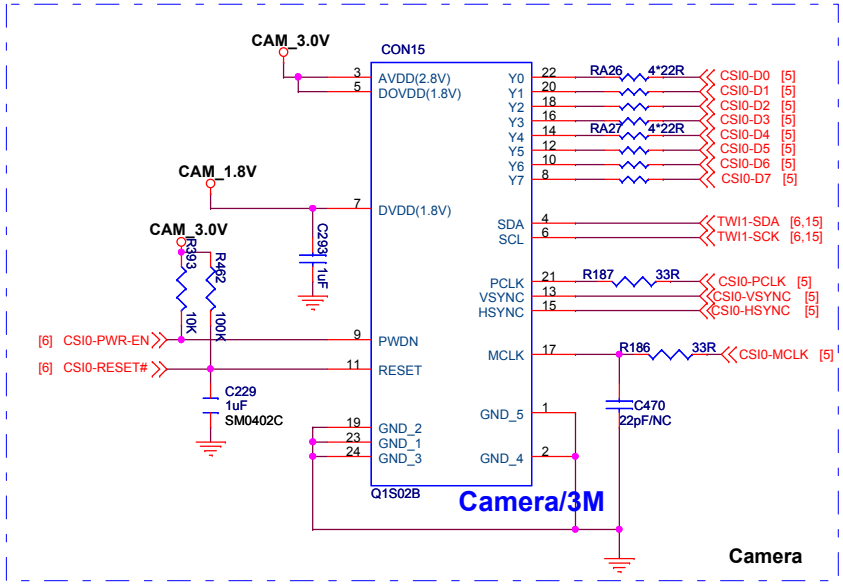


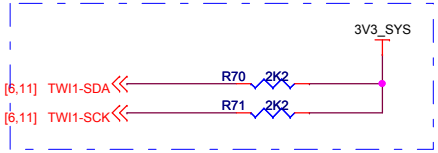


Title	pcDuino	
Size A4	Document Number HDMI	Rev V3.0
Date: Tuesday, December 24, 2013	Sheet 10 of 16	

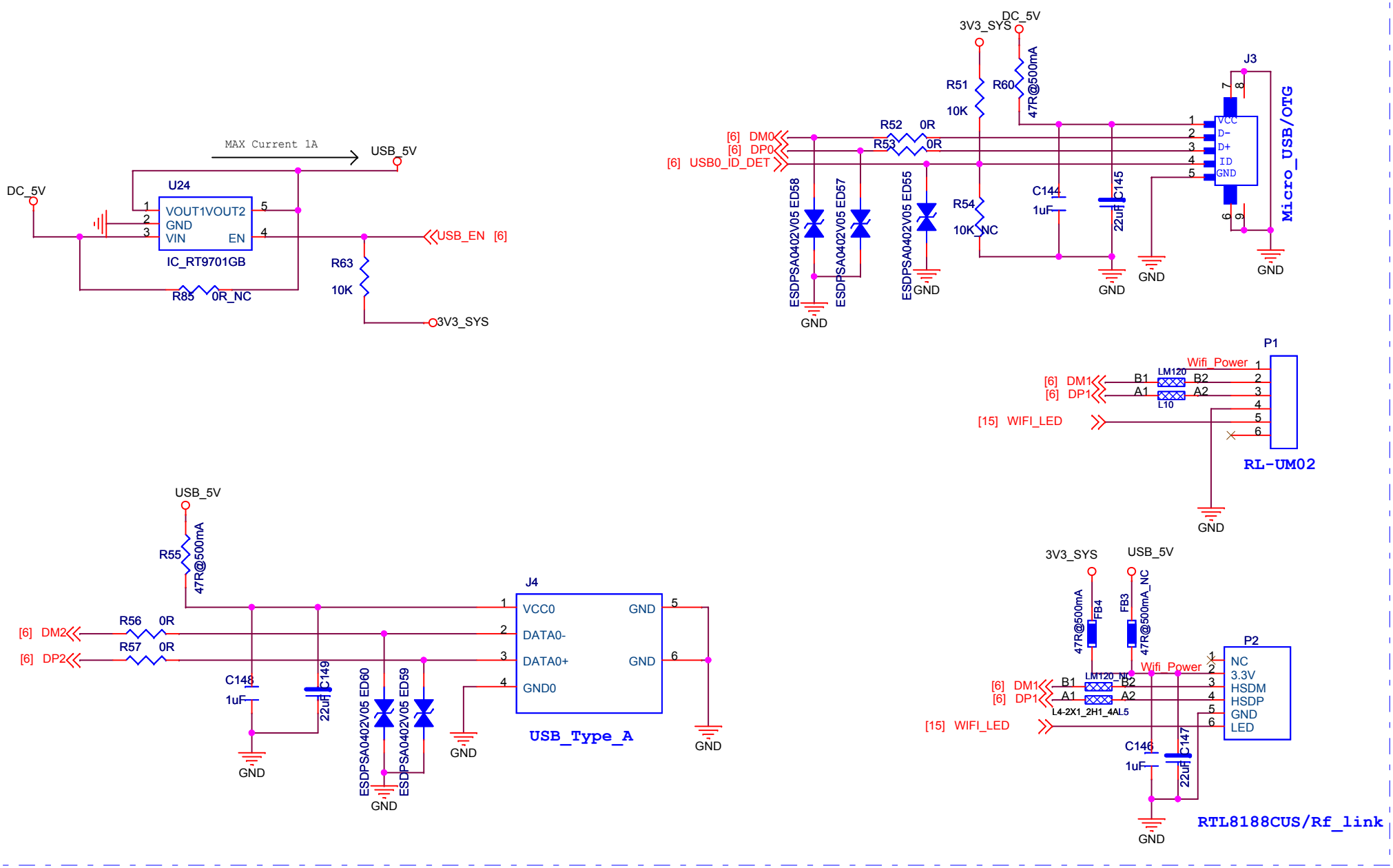




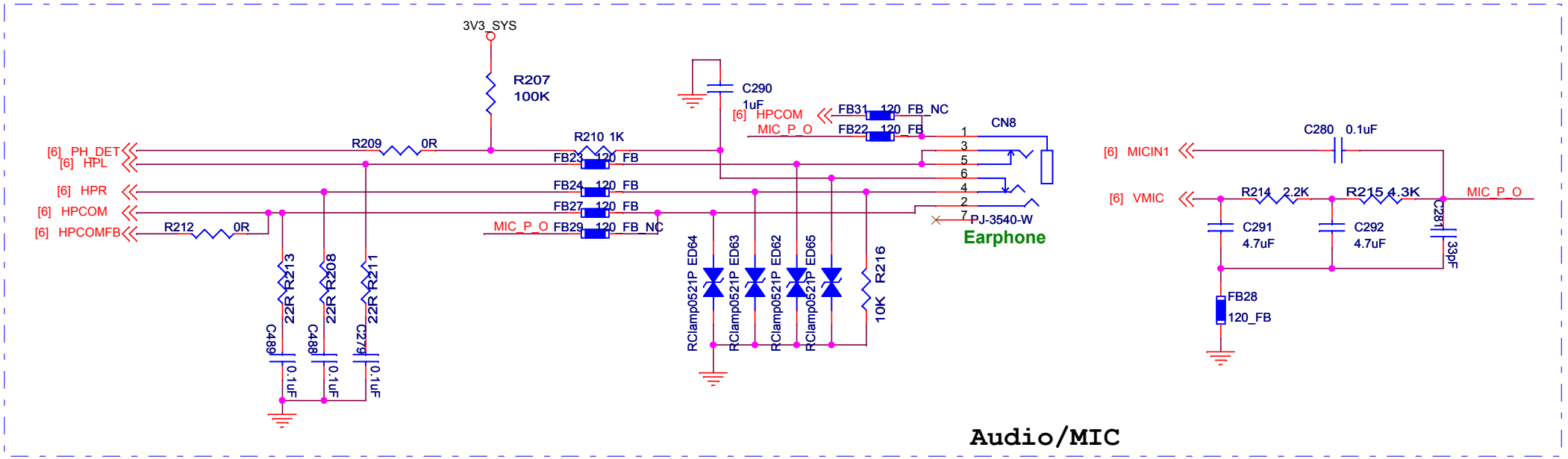


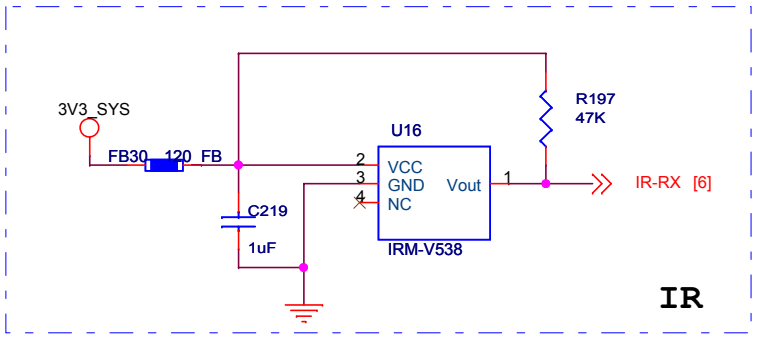


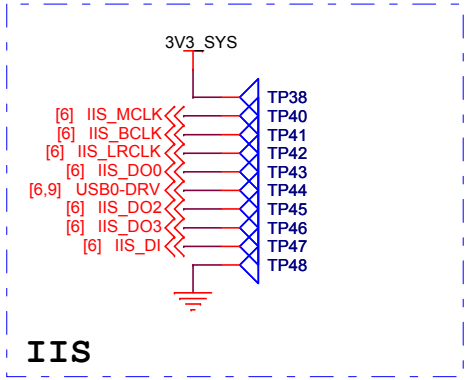
Title: pcDuino			
Size: B	Document Number: LVDS/CSI		Rev: V3.0
Date: Tuesday, December 24, 2013	Sheet 11 of 16		



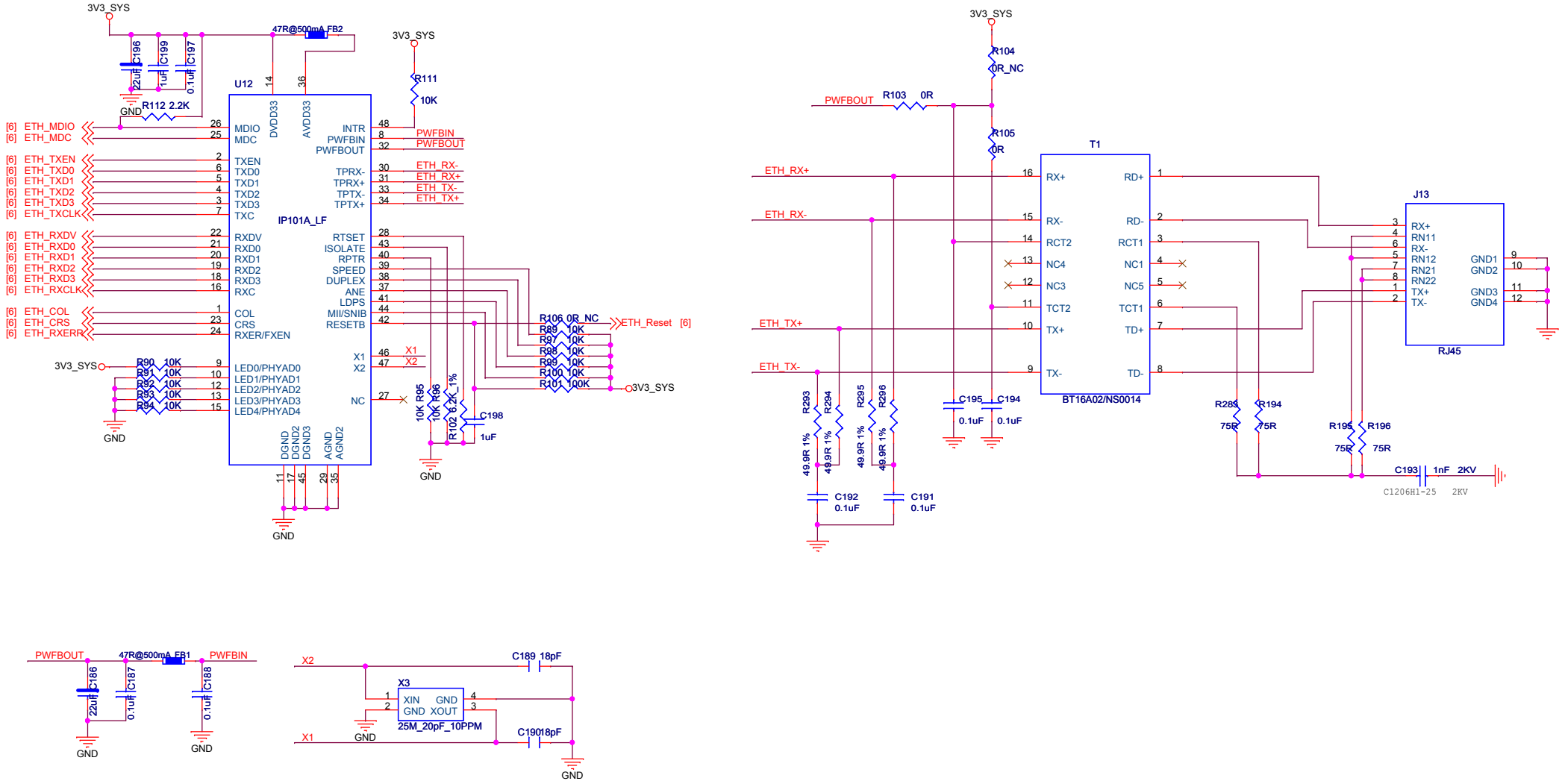
Title	pcDuino	
Size A4	Document Number WIFI/USB	Rev V3.0
Date: Tuesday, December 24, 2013	Sheet 12 of 16	



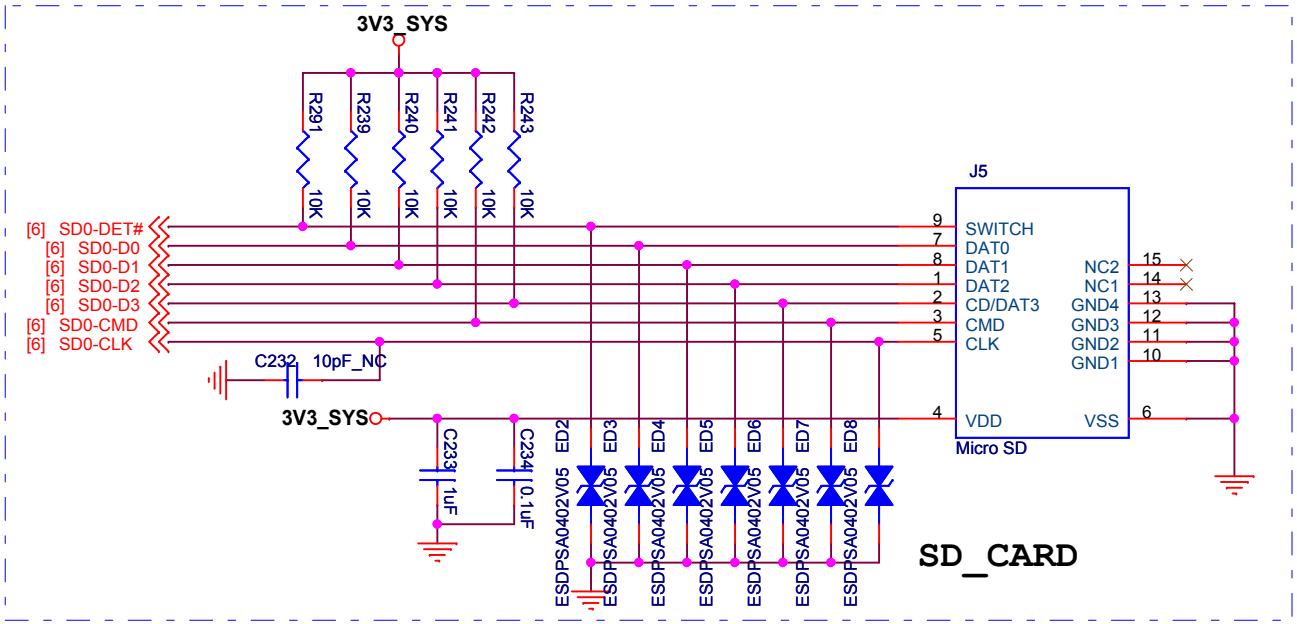


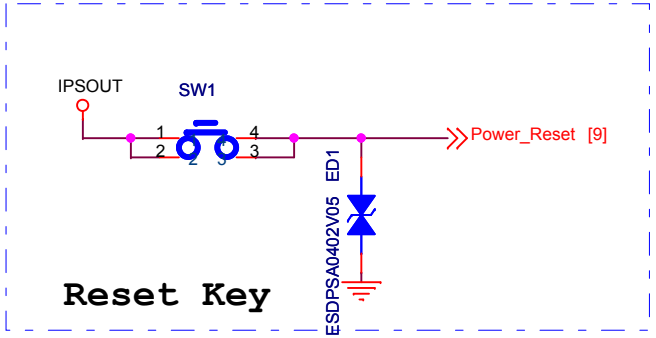


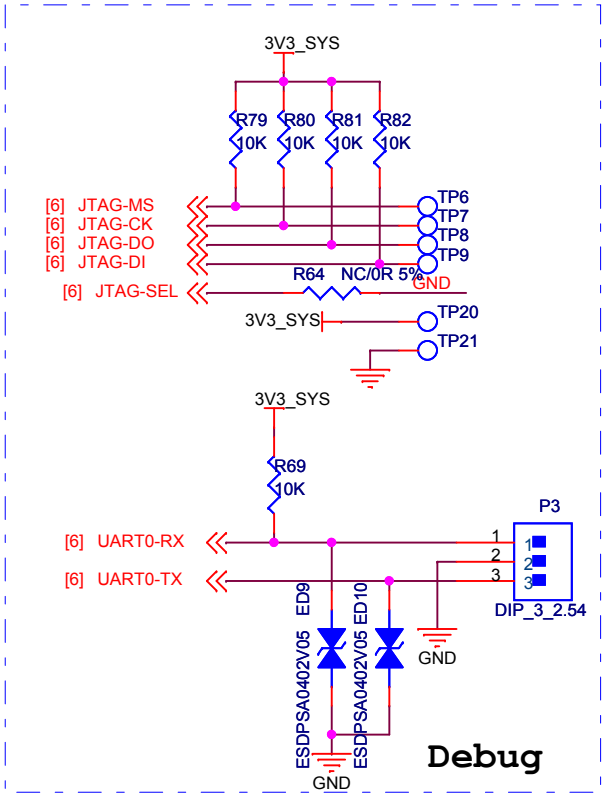
Title: pcDuino		
Size: Custom	Document Number: Audio/IIS/IR	Rev: 3.0
Date: Tuesday, December 24, 2013	Sheet 13 of 16	

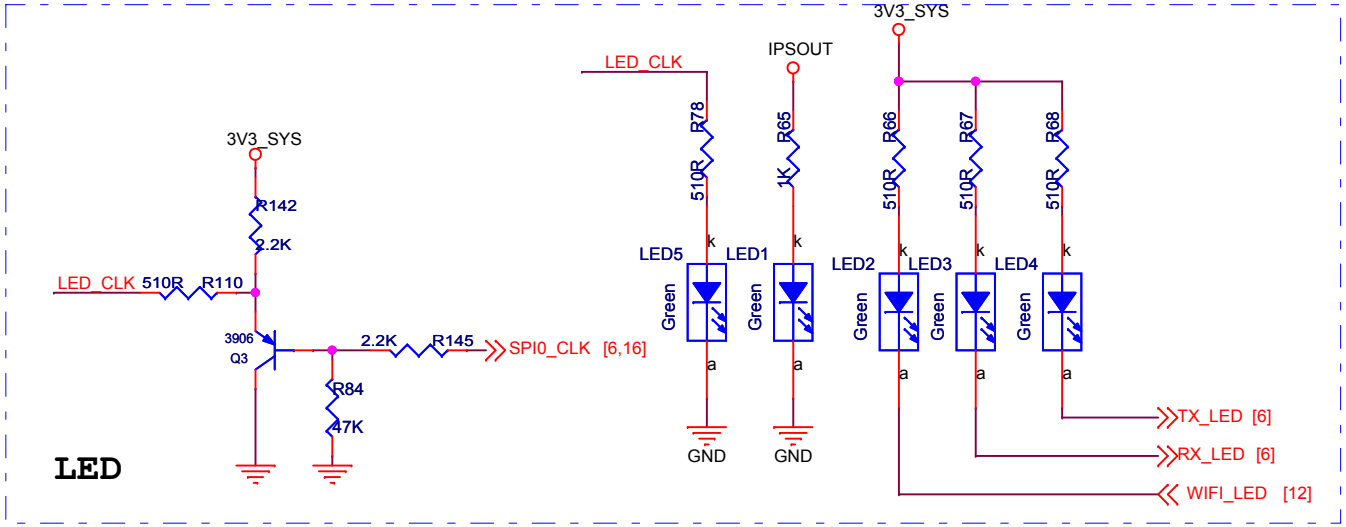


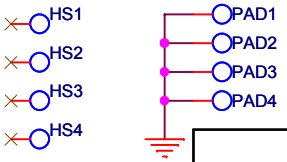
Title	pcDuino	
Size B	Document Number Ethernet	Rev V3.0
Date: Tuesday, December 24, 2013	Sheet 14 of 16	



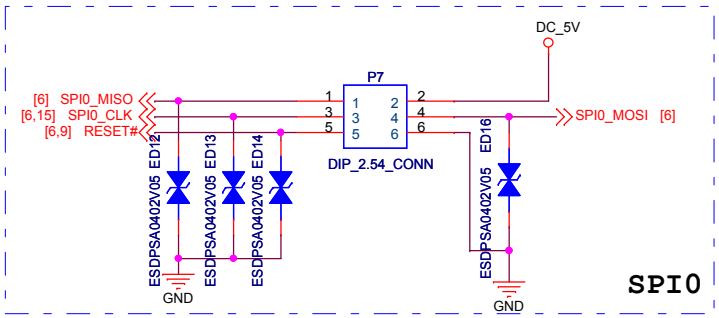


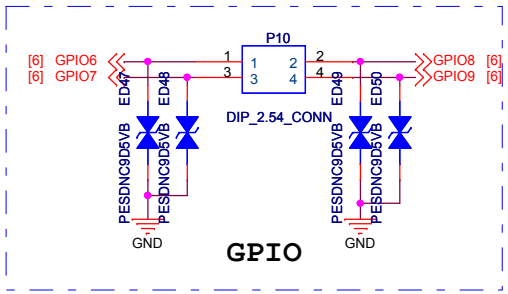


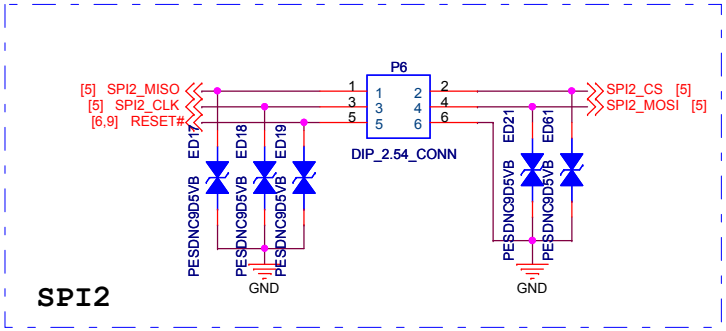


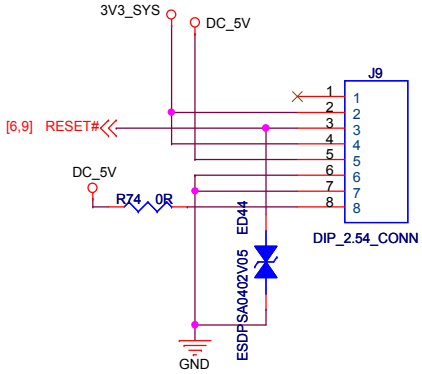


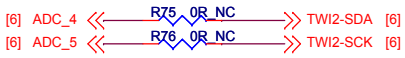
Title: pcDuino		
Size: A4	Document Number: SD/Debug	Rev: V3.0
Date: Tuesday, December 24, 2013	Sheet 15 of 16	

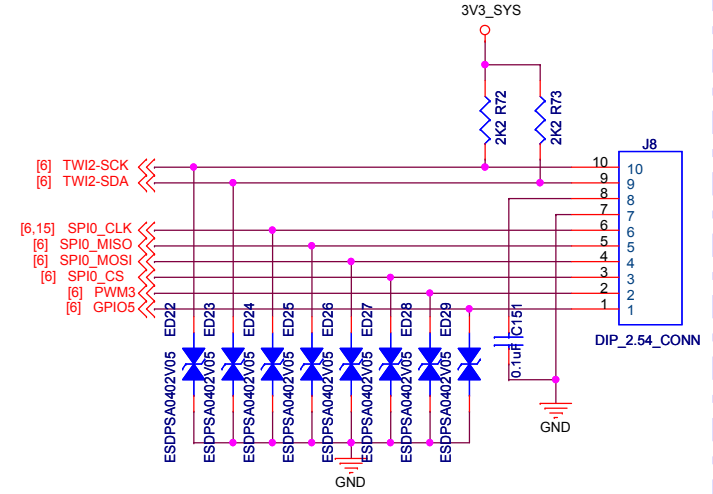


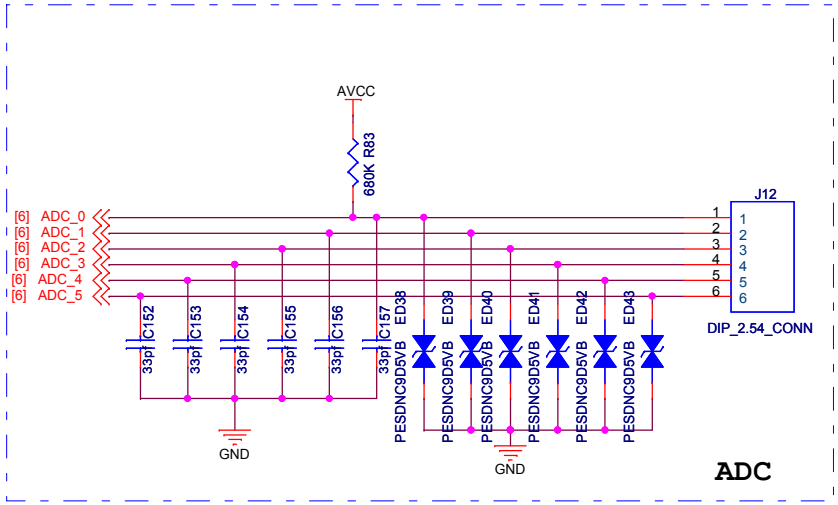


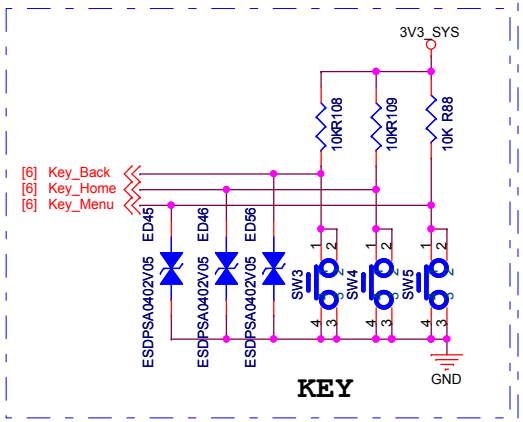


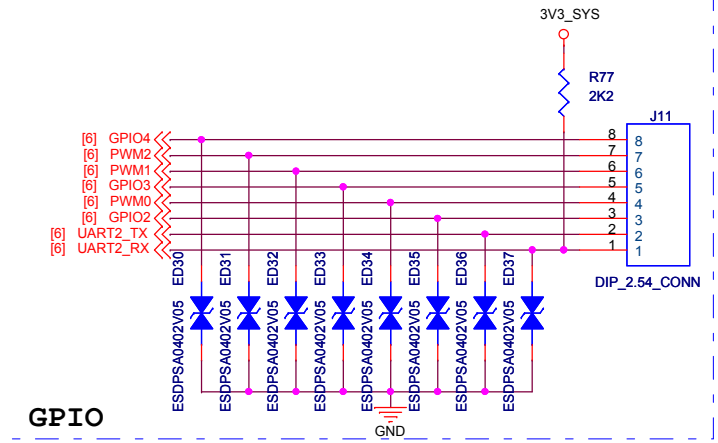












Title: pcDuino			
Size: Custom	Document Number: CONN		Rev: V3.0
Date: Tuesday, December 24, 2013	Sheet 16 of 16		