

Consider a program to record 16 y/n answers to 16 questions.

Use an unsigned integer : Answer

Answer :

63				15		0	
				1	0	---	1 0

$$1 \rightarrow y, 0 \rightarrow n$$

Note: $1 \ll j$ is a number with 1 in the j^{th} bit and zeros everywhere else

Initialize Answer to 0
if the first 4 bits have been set in Answer,
it might look like this

[illegible]

an '1' input will just increment the value of j
and leave the corresponding bit in Answer unchanged
at '0'

if the next '1' input occurs when $j=6$, the
Answer will be OR'd with $000\dots1000000$

Answer $|= 1 \ll j$; // here $j=6$

(\equiv Answer = Answer $| 1 \ll j$)

This will put a 1 in the 6^{th} bit posⁿ. and will

leave the rest of the bits in the Answer unchanged

Bit manipulation Continued.

```

#define TX_PIN 7 // transmission pin
#define BIT_RATE 100

char data = 170; //value to transmit, binary = 10101010
char mask;      // transmission mask

void setup(){
    pinMode(TX_PIN, OUTPUT);
}

void loop(){
    mask = mask << 1
    for (mask = 1; mask >0; mask <=<1){
        if (data & mask){
            digitalWrite(TX_PIN, HIGH);
        }
        else{
            digitalWrite(TX_PIN, LOW);
        }
        delayMicroseconds(BIT_RATE);
    }
}

```

Example : Transmit a byte serially on a pin.

The mask values will go from

00000001
 To 00000010
 To 00000100
 :
 To 10000000
 To 00000000

data & mask

data : 10101010
 mask : 00000001 &
 00000000 → False

data : 10101010
 mask : 00000010 &
 00000010 → True

etc

So far we have seen that

$$b \& 0 = 0$$

$$b \& 1 = b \quad - \text{use } \& \text{ to select } b \text{ from byte}$$

XOR \rightarrow

$$b \wedge 1 = \bar{b} \quad - \text{use } \wedge \text{ to toggle } b \text{ in a byte}$$

$$b | 0 = b$$

$$b | 1 = 1 \quad \text{use } | \text{ to set } b \text{ to } 1$$

To set bit 2 high in PORTD, while leaving all other bits as they are!

$$\text{PORTD} |= \text{B}00000100$$

$$\equiv \text{PORTD} |= 1 \ll 2 \quad (\text{PORTD} = \text{PORTD} | 1 \ll 2)$$

To set bit 2 low in PORTD, while leaving all other bits unchanged

$$\text{PORTD} \&= \text{B}11111011 \quad (\text{PORTD} \& \sim 1 \ll 2)$$

$$\equiv \text{PORTD} \&= \sim(1 \ll 2)$$

Not as obvious
which bit is
changing

Example : Blink on Pins

```
Void setup () {  
    DDRD |= B00100000;  
    PORTD = B00000000;  
}
```

```
Void loop () {  
    PORTD ^= B00100000;  
    delay (100);  
}
```