

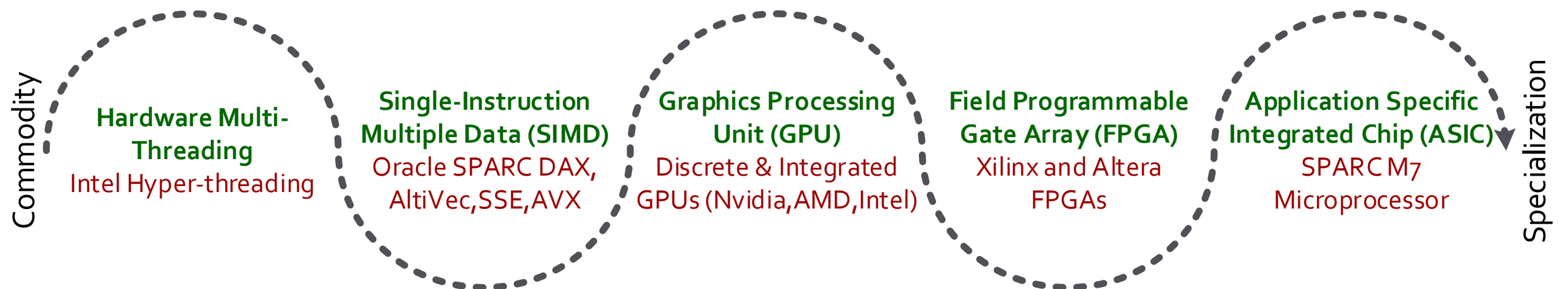
Active/HardBD Panel

Mohammad Sadoghi, Purdue University

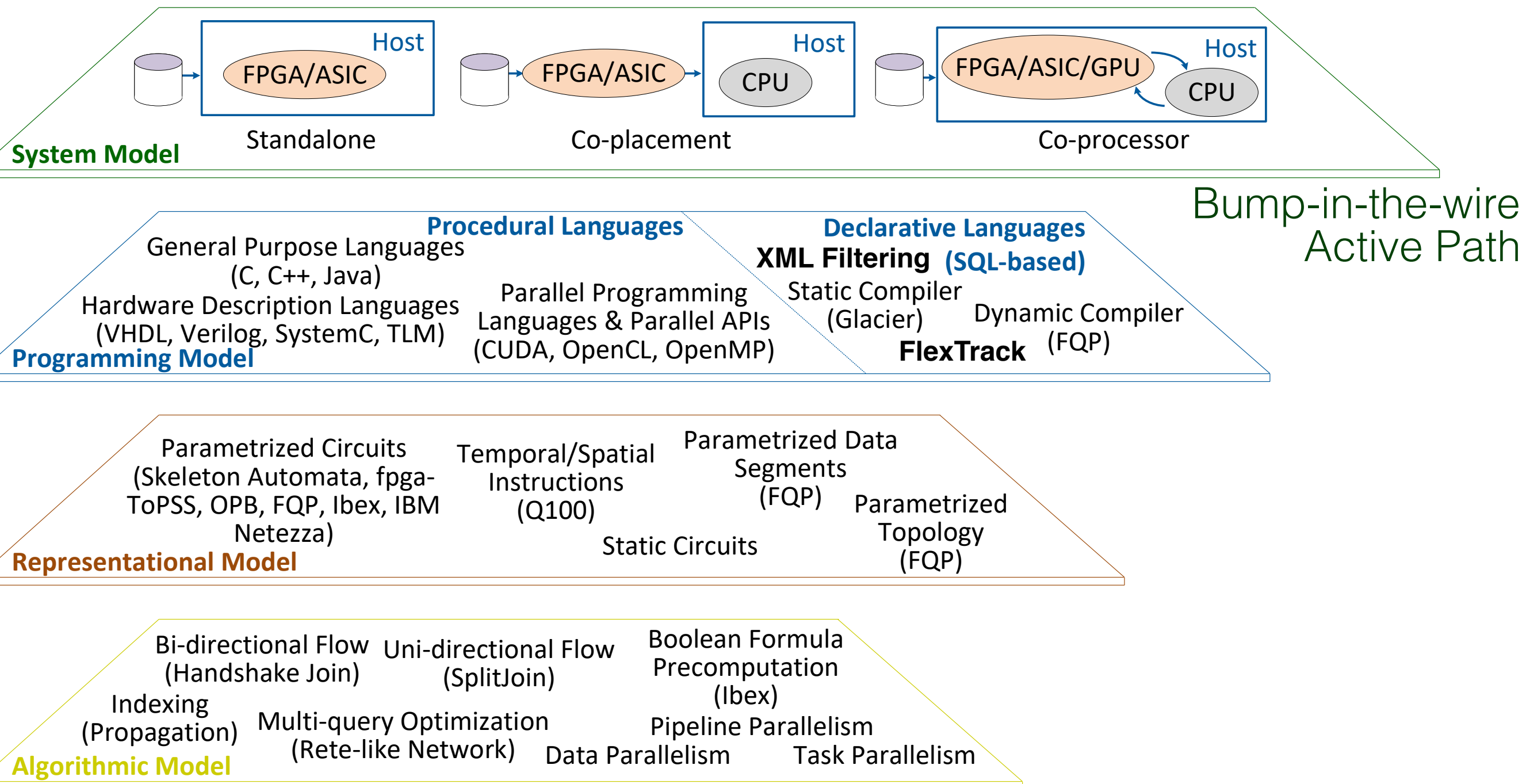
Sebastian Breß, German Research Center for Artificial Intelligence

Vassilis J. Tsotras, University of California – Riverside

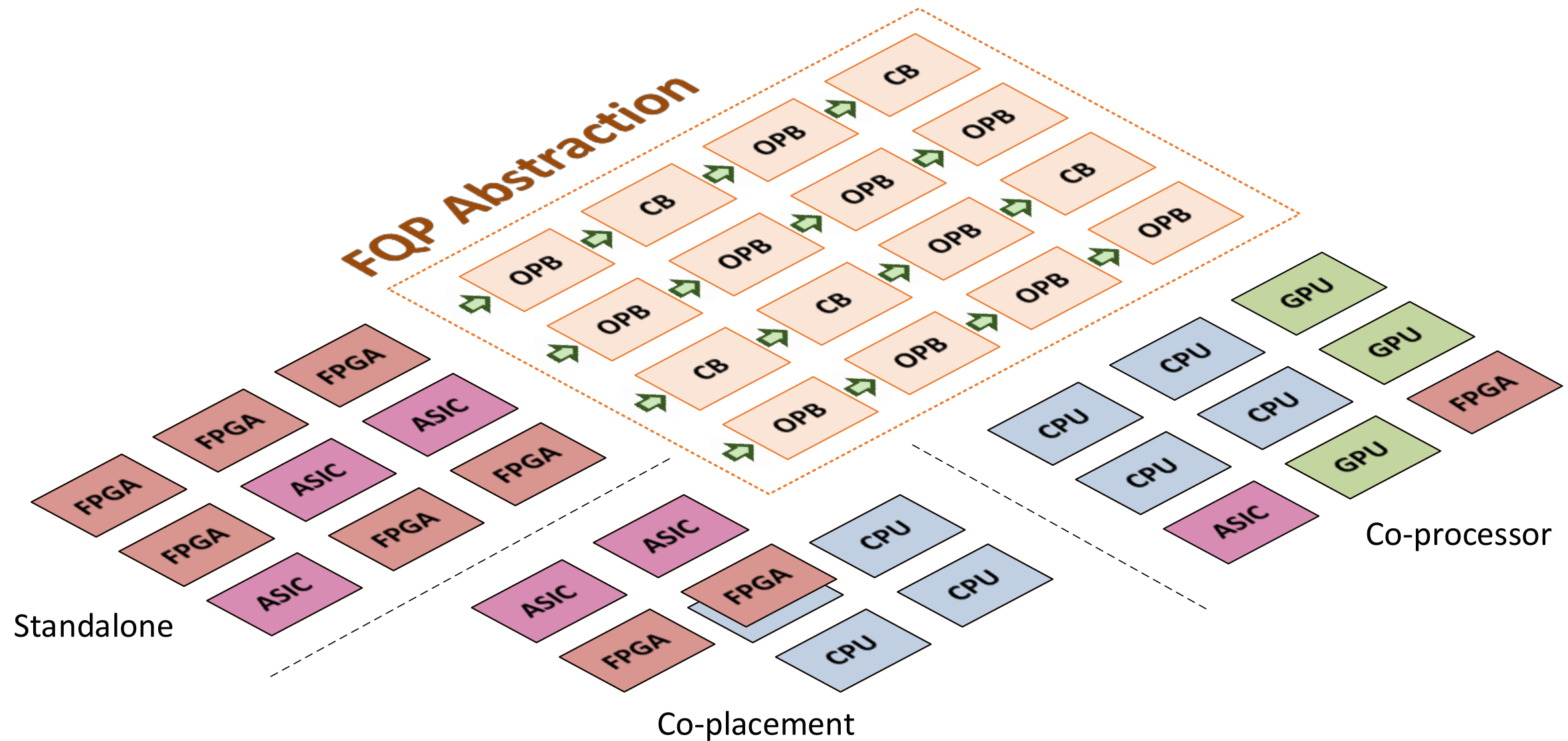
Accelerator Spectrum



Hardware Acceleration Landscape



Heterogeneous Hardware Virtualization



Software-Hardware Life-cycle for Modern Accelerators

Moore's Law Limit

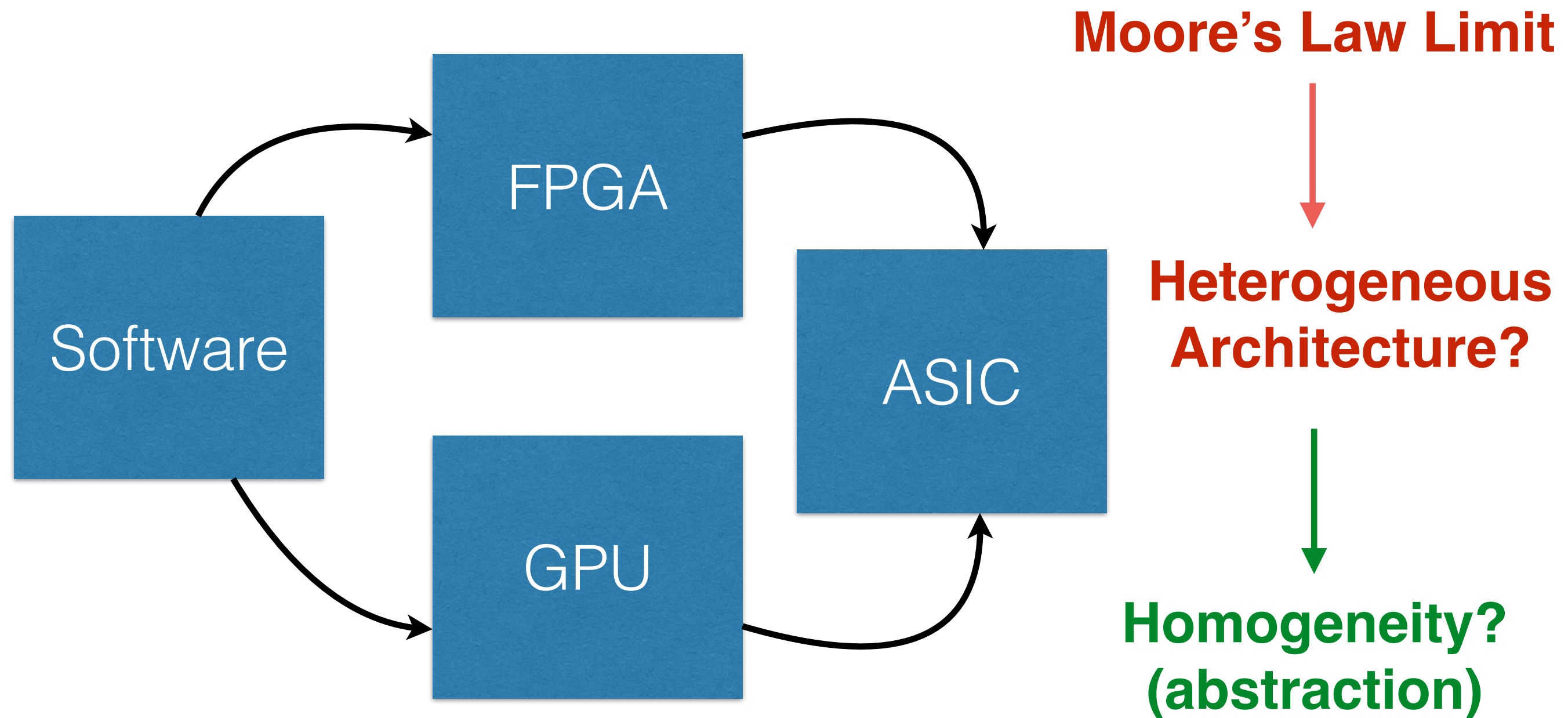


**Heterogeneous
Architecture?**

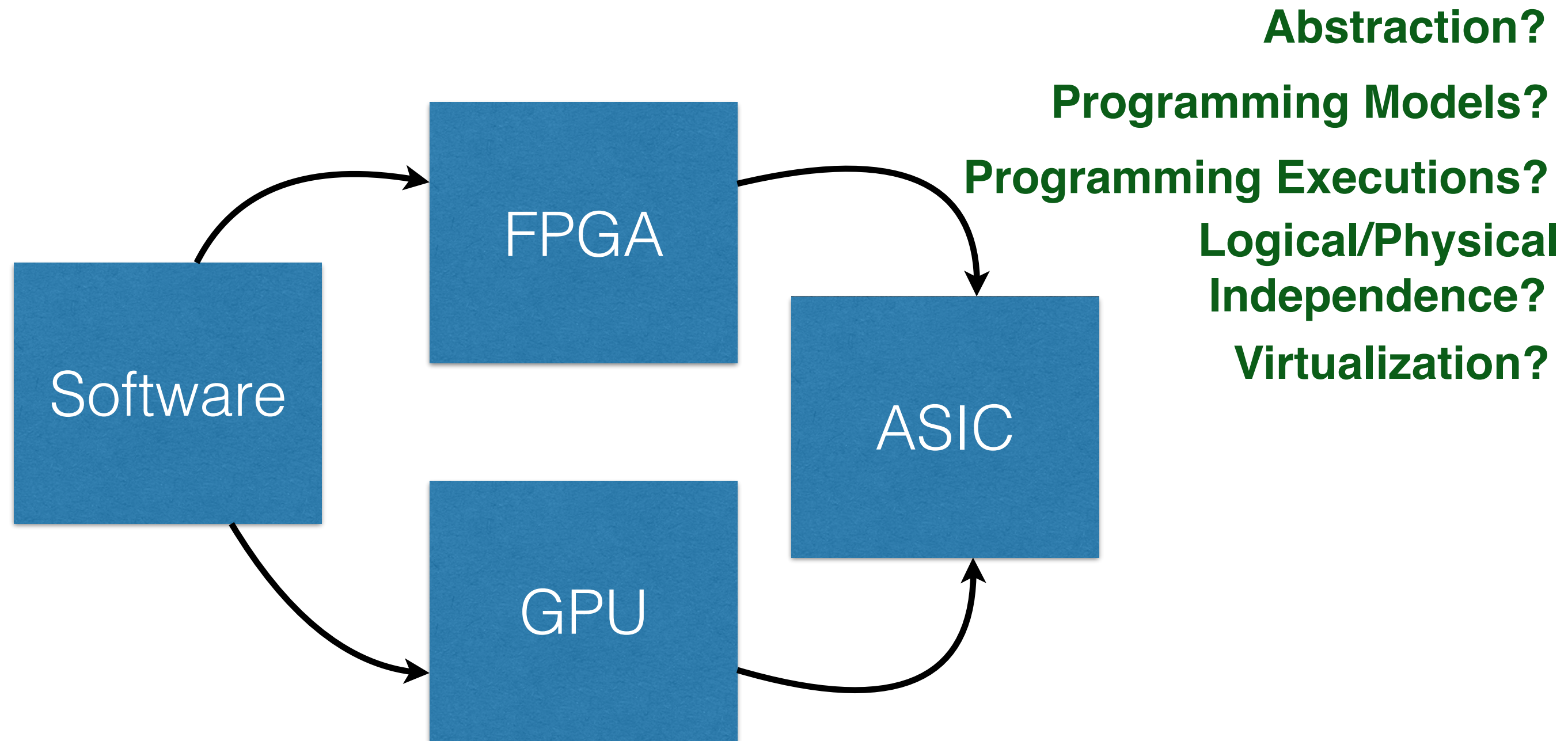


**Homogeneity?
(abstraction)**

Software-Hardware Life-cycle for Modern Accelerators



Software-Hardware Life-cycle for Modern Accelerators



Software-Hardware Life-cycle for Modern Accelerators

