J1
6502 Card Edge

Left	Right
GND	VCC
RESB	EXP1
IRQB	EXP0
NMIB	A15
RDY	A14
BE	A13
RWB	A12
SYNC	A11
PHI2	A10
EXP3	A9
EXP2	A8
D7	A7
D6	A6
D5	A5
D4	A4
D3	A3
D2	A2
D1	A1
D0	A0
VCC	GND

U1
65C02

Pin	Left	Right	Pin
1	VPB	RESB	40
2	RDY	PHI2O	39
3	PHI1O	SOB	38
4	IRQB	PHI2	37
5	MLB	BE	36
6	NMIB	NC	35
7	SYNC	RWB	34
8	VDD	D0	33
9	A0	D1	32
10	A1	D2	31
11	A2	D3	30
12	A3	D4	29
13	A4	D5	28
14	A5	D6	27
15	A6	D7	26
16	A7	A15	25
17	A8	A14	24
18	A9	A13	23
19	A10	A12	22
20	A11	VSS	21

GND

VCC

R1 1k — RESB
R2 1k — RDY
R3 1k — IRQB
R4 1k — NMIB
R5 1k — BE
R6 1k — SOB

C1 100nF

GND

J3

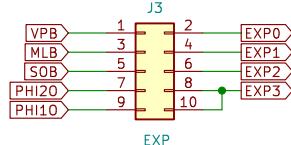|			
VPB	1	2	EXP0
MLB	3	4	EXP1
SOB	5	6	EXP2
PHI2O	7	8	EXP3
PHI1O	9	10	

EXP

J2

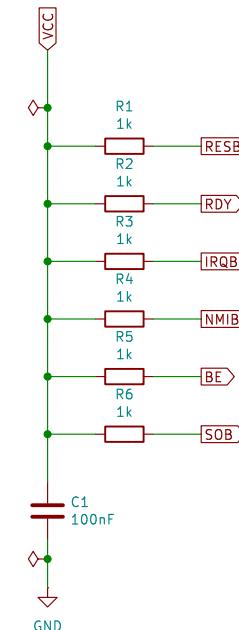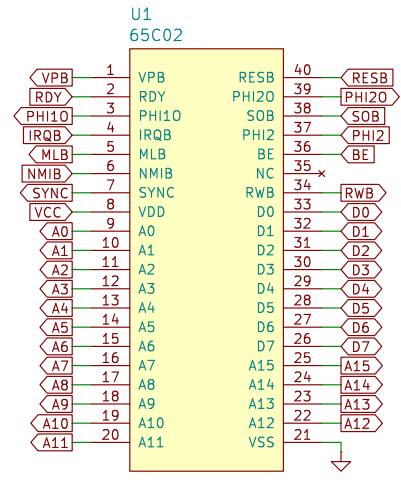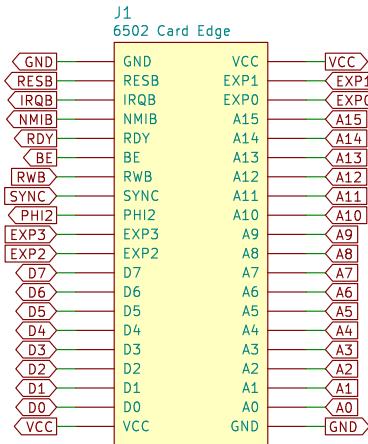Jumper J2 for orginal 6502

| 2 | | VPB |
| 1 | JP1 | |

GND

6502

A.C. Wright Design

Sheet: /
File: CPU Card.kicad_sch

Title: 6502 CPU Card

Size: USLetter | Date: 2024-07-03 | Rev: 1.0
KiCad E.D.A. 9.0.6 | Id: 1/1