## NIOS II EMULATOR

### TEST PLAN

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#### Introduction

The system will be rigorously tested with multiple assembly code files. The system will be loaded by the local host. Files will be inputted through the graphical user interface (GUI) and the output will be tested for correctness. While the file is running, the system will be started, paused, and restarted to test for functionality of the buttons.

#### System Overview

The system is designed to emulate the NIOS II processor. It is able to carry out all the instructions and processes of the regular NIOS II emulator. The graphical user interface of the system will display the registers and up to 16 user selected memory locations. While the system is running it is possible to pause, run, and restart the system.

#### **F**EATURES TO BE **T**ESTED

Testing	System	
Requirement	Requirement(s)	Short Description
1	F1, F2, F5, F8	Testing Graphical User Interface
2	F1, F2, F4, F8	Instruction handling and general execution of file
3	F1, F2, F4, F8	Instruction handling and general execution of file
4	F1, F2, F4, F8	Instruction handling and general execution of file
5	F1, F2, F4, F8	Instruction handling and general execution of file

#### TEST ENVIRONMENT

The environment for testing is done locally. Installation of Node.js is required to run the software locally. Run the server by typing "node server.js" in the terminal or command prompt. This brings up the online GUI. Files can be inputted and buttons can be pressed at this point.

# Test Cases TEST CASE NUMBER 1 COMPONENT UNDER TEST **Graphical User Interface** FEATURE(S) TO BE TESTED F1, F2, F5, F8 INITIAL CONDITIONS System is in browser being ran by local host. EXPECTED BEHAVIOR INPUT 1. Uploaded file 2. Clicked buttons 3. Picked Hexadecimal Values 4. Run file 5. Pause File 6. Restart File 7. Looked at registers Оитрит Output was as expected. All actions resulted in expected functions. TEST CASE NUMBER 2 COMPONENT UNDER TEST

Instruction handling and general execution of file

FEATURE(S) TO BE TESTED

F1, F2, F4, F8

Initial Conditions

System is in browser being ran by local host.

EXPECTED BEHAVIOR

INPUT

test Case Count To 100.txt

Оитрит

Expected: r2 = 128, r3 = 320, r4 = 70

Actual: r2 = 128, r3 = 320, r4 = 70

Test Case Number

3

COMPONENT UNDER TEST

Instruction handling and general execution of file

FEATURE(s) TO BE TESTED

F1, F2, F4, F8

INITIAL CONDITIONS

System is in browser being ran by local host.

EXPECTED BEHAVIOR

INPUT

test Case In finite Loop. txt

Оитрит

Expected: r2, r3, r4, should continue to infinity with each pause.

Actual: r2, r3, r4, continues to infinity.

TEST CASE NUMBER

4

COMPONENT UNDER TEST

Instruction handling and general execution of file

FEATURE(S) TO BE TESTED

F1, F2, F4, F8

INITIAL CONDITIONS

System is in browser being ran by local host.

EXPECTED BEHAVIOR

INPUT

test Case Invalid Register.txt

Оитрит

Expected: Error: Invalid register "r45"

Actual: Stops file execution and notifies the user of the invalid register call

TEST CASE NUMBER

5

COMPONENT UNDER TEST

Instruction handling and general execution of file

FEATURE(S) TO BE TESTED

F1.	F2.	F4.	F8

INITIAL CONDITIONS

System is in browser being ran by local host.

EXPECTED BEHAVIOR

INPUT

test Case Count To 100 And Back.txt

Оитрит

Expected: r2 = 0, r5 = 100

Actual: r2 = 0, r5 = 100

Test Case Number

6

COMPONENT UNDER TEST

Instruction handling and general execution of file

FEATURE(s) TO BE TESTED

F1, F2, F4, F8

INITIAL CONDITIONS

System is in browser being ran by local host.

EXPECTED BEHAVIOR

INPLIT

test Case Mispelled Instruction.txt

**O**UTPUT

Expected: File should not run. Return error.

Actual: System returns error. File still runs.

Test Case Number

7

COMPONENT UNDER TEST

Instruction handling and general execution of file

FEATURE(S) TO BE TESTED

F1, F2, F4, F8

INITIAL CONDITIONS

System is in browser being ran by local host.

EXPECTED BEHAVIOR

INPUT

test Case With Comments.txt

Оитрит

Expected: File uploads with no errors. Displays correct registers.

Actual: File uploads with no errors. Displays correct registers.