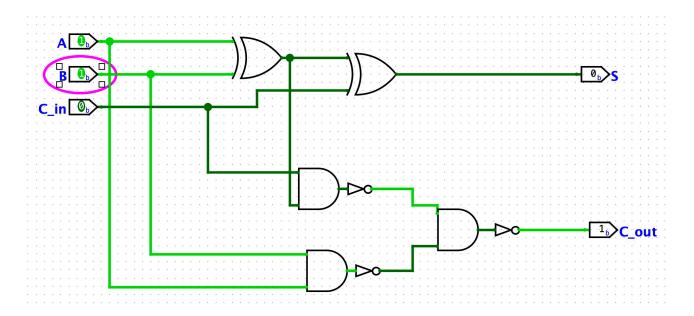
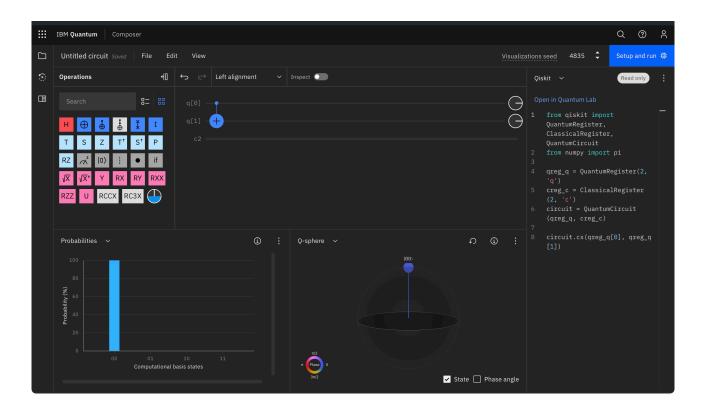
### Lab 2

# AbdelRahman Adel AbdelFattah - 17012296 Full Adder Circuit

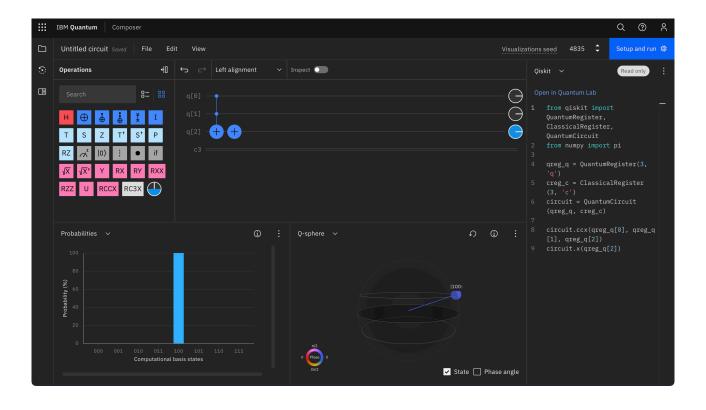


### **Full Adder Code**

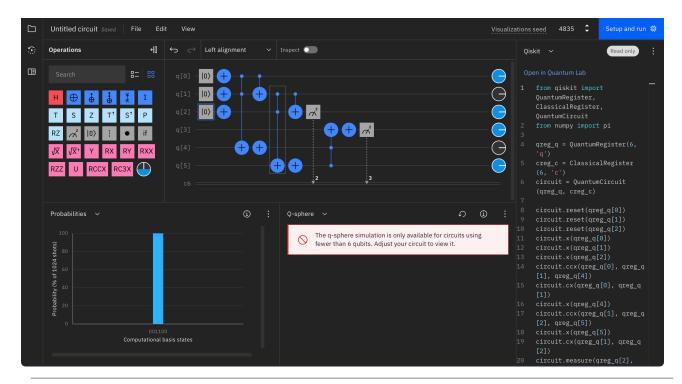
## **Quantum Connections XOR GATE**



#### **NAND Gate**



### **Full Adder**



The result bits are the observed bits. Sum on top and Carry out on the bottom