GPU Lab

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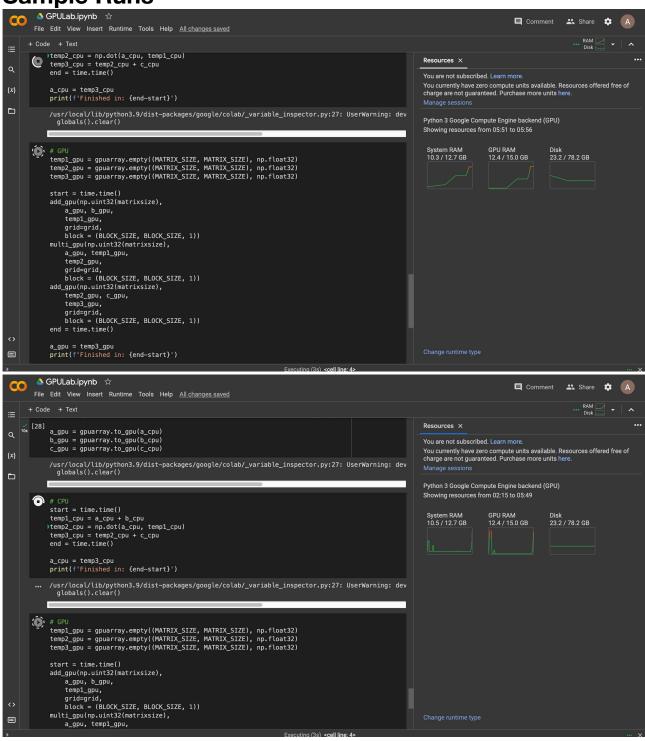
Code

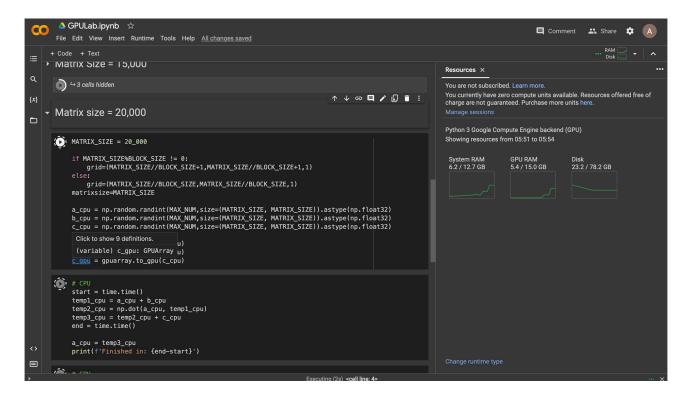
```
import numpy as np
from pycuda import compiler, gpuarray, tools
import pycuda.driver as cuda
import pycuda.autoinit
import time
kernel_code = """
 _global___ void multi_gpu(int matrixsize,float *a, float *b, float
*c)
{
    int tx = blockDim.x*blockIdx.x + threadIdx.x;
    int ty = blockDim.y*blockIdx.y + threadIdx.y;
    if((ty <matrixsize) && (tx < matrixsize)){</pre>
      float Pvalue = 0;
      for(int k=0; k<matrixsize;++k){</pre>
        float Aelement = a[ty*matrixsize +k];
        float Belement = b[k*matrixsize +tx];
        Pvalue += Aelement * Belement;
      c[ty * matrixsize + tx] = Pvalue;
}
 _global___ void add_gpu(int matrixsize,float *a, float *b, float
*c)
{
    int tx = blockDim.x*blockIdx.x + threadIdx.x;
    int ty = blockDim.y*blockIdx.y + threadIdx.y;
    if((ty <matrixsize) && (tx < matrixsize)){</pre>
      float Aelement = a[ty * matrixsize + tx];
      float Belement = b[ty * matrixsize + tx];
      c[ty * matrixsize + tx] = Aelement + Belement;
}
def add_cpu(matrix_a, matrix_b):
  result = np.zeros((matrix a.shape[0], matrix a.shape[1]))
  for i in range(matrix a.shape[0]):
    for j in range(matrix_a.shape[1]):
      result[i][i] = matrix a[i][i] + matrix b[i][i]
  return result
```

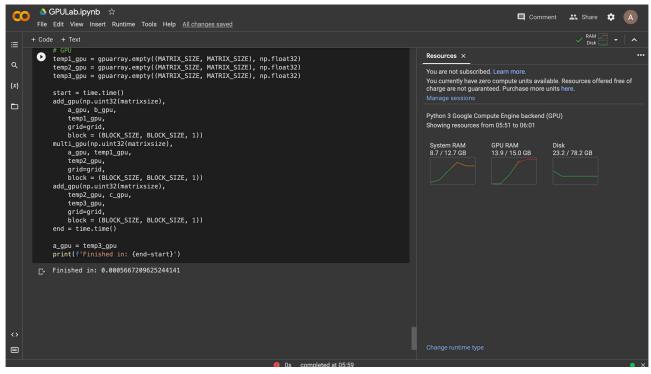
```
def multi cpu(matrix a, matrix b):
  result = np.zeros((matrix a.shape[0], matrix a.shape[1]))
  for i in range(matrix a.shape[0]):
    for j in range(matrix b.shape[1]):
      for k in range(matrix b.shape[0]):
        result[i][j] += matrix a[i][k] * matrix b[k][j];
  return result
mod = compiler.SourceModule(kernel code)
multi gpu = mod.get function("multi gpu")
add gpu = mod.get function("add gpu")
BLOCK SIZE = 32
MAX NUM = 1024
MATRIX SIZE = 3
if MATRIX SIZE%BLOCK SIZE != 0:
    grid=(MATRIX SIZE//BLOCK SIZE+1, MATRIX SIZE//BLOCK SIZE+1,1)
else:
    grid=(MATRIX SIZE//BLOCK SIZE,MATRIX SIZE//BLOCK SIZE,1)
matrixsize=MATRIX_SIZE
a cpu = np.random.randint(MAX_NUM, size=(MATRIX_SIZE,
MATRIX SIZE)).astype(np.float32)
b cpu = np.random.randint(MAX NUM.size=(MATRIX SIZE,
MATRIX SIZE)).astype(np.float32)
c cpu = np.random.randint(MAX NUM, size=(MATRIX SIZE,
MATRIX_SIZE)).astype(np.float32)
a_gpu = gpuarray.to_gpu(a_cpu)
b_gpu = gpuarray.to_gpu(b_cpu)
c gpu = gpuarray.to gpu(c cpu)
MATRIX SIZE = 3
if MATRIX SIZE%BLOCK SIZE != 0:
    grid=(MATRIX SIZE//BLOCK SIZE+1, MATRIX SIZE//BLOCK SIZE+1,1)
else:
    grid=(MATRIX SIZE//BLOCK SIZE,MATRIX SIZE//BLOCK SIZE,1)
matrixsize=MATRIX SIZE
a cpu = np.random.randint(MAX NUM, size=(MATRIX SIZE,
MATRIX_SIZE)).astype(np.float32)
b cpu = np.random.randint(MAX NUM, size=(MATRIX SIZE,
MATRIX_SIZE)).astype(np.float32)
c_cpu = np.random.randint(MAX_NUM, size=(MATRIX_SIZE,
MATRIX_SIZE)).astype(np.float32)
a_gpu = gpuarray.to_gpu(a_cpu)
b_gpu = gpuarray.to_gpu(b_cpu)
c gpu = gpuarray.to gpu(c cpu)
# CPU
```

```
start = time.time()
temp1_cpu = add_cpu(a_cpu, b_cpu)
temp2 cpu = multi cpu(a cpu, temp1 cpu)
temp3 cpu = add cpu(temp2 cpu, c cpu)
end = time.time()
a cpu = temp3 cpu
print(f'Finished in: {end-start}')
# GPU
temp1 gpu = gpuarray.empty((MATRIX SIZE, MATRIX SIZE), np.float32)
temp2_gpu = gpuarray.empty((MATRIX_SIZE, MATRIX_SIZE), np.float32)
temp3 gpu = gpuarray.empty((MATRIX SIZE, MATRIX SIZE), np.float32)
start = time.time()
add gpu(np.uint32(matrixsize),
    a_gpu, b_gpu,
    temp1 qpu,
    grid=grid,
    block = (BLOCK SIZE, BLOCK SIZE, 1))
multi_gpu(np.uint32(matrixsize),
    a gpu, temp1 gpu,
    temp2 gpu,
    grid=grid,
    block = (BLOCK SIZE, BLOCK SIZE, 1))
add gpu(np.uint32(matrixsize),
    temp2_gpu, c_gpu,
    temp3_gpu,
    grid=grid.
    block = (BLOCK_SIZE, BLOCK_SIZE, 1))
end = time.time()
a qpu = temp3 qpu
print(f'Finished in: {end-start}')
```

Sample Runs







Speed Comparisons

in seconds

Table 1

Matrix Size	CPU	GPU
3	0.00377678871154785	0.0012199878692627
10	0.000234603881835938	0.000370502471923828
100	0.00583577156066895	0.000510215759277344
1000	0.0613725185394287	0.000532388687133789
5000	3.59896087646484	0.000474929809570313
10000	30.3431296348572	0.000492095947265625
15000	91.3707048892975	0.000566720962524414

Code explanation

- Kernel code: creates two functions that calculates the multiply and addition of matrix parallelized depending on block size and grid size
- · Two functions to use for cpu calculation
- · Compile the kernel of CUDA
- · Referencing the two kernel functions
- Setting the grid size.
- · Initialize the matrices
- Calculate CPU and GPU and see the time of each.

References

- http://homepages.math.uic.edu/~jan/mcs507/gpuacceleration.pdf
- https://documen.tician.de/pycuda/array.html#module-pycuda.gpuarray
- https://ecatue.gitlab.io/gpu2018/pages/Cookbook/matrix_multiplication_cuda.html
- https://shephexd.github.io/development/2017/02/19/pycuda.html
- https://vitalitylearning.medium.com/a-short-notice-on-performing-matrix-multiplications-in-pycuda-cbfb00cf1450