/ESG Group/FDD Module Requirements/EA4 Specific ES005A_TmplMonr

Besilened v2.0 and Released

Version: 2.0

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ES005A _8	1.1 Definitions
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ES005A _92	PwrOutpEnaFb : A physical feedback input signal to verify that temporal monitor function is properly working.
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ES005A _106	StrtUpSt: Startup State enumeration input is used to decide when Temporal Monitor function should start.
ES005A _13	1.1.2 Outputs
ES005A _96	PwrOutpEna : This physical output when driven high will enable power to the Gate Drive(s).
ES005A _97	TmplMonrIninTestCmpl: An output flag to notify Temporal Monitor Initialization test completed or not-completed.
ES005A _100	1.1.3 Internally Defined Terms
ES005A _101	TmplMonrWdg: Physical square wave output used for Temporal Monitor verification.
ES005A _102	SysFlt2A: An output signal generated to control the power pass of Gate Drive A.
ES005A _103	SysFlt2B: An output signal generated to control the power pass of Gate Drive B.

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ES005A _144 ES005A _144 ES005A _169 The Temporal Monitor function shall detect an error of ±11% in the Primary Processor clock within 200ms. ES005A _169 The Temporal Monitor function shall provide a mechanism to store its sequence number (TmplMonrIninCntr) into the per-instance menory. ES005A _126 ES005A _22 ES005A _23 ES005A _23 ES005A _24 ES005A _25 ES005A _25 ES005A _26 ES005A _27 ES005A _27 ES005A _28 ES005A _29 The Temporal Monitor function shall generate TmplMonrWdg square wave signal with the following characteristics. Period = 2ms ± 0.12ms Duty Cycle = 50 ± 30% 2V < Vmax < 5 V -0.1 V > Vmin < 0.5 V ES005A _104 ES005A _31 ES005A _31 ES005A _31 ES005A _31 ES005A _31 ES005A _31 ES005A _32 The Temporal Monitor function shall generate TmplMonrWdg signal by toggling a GPIO pin from Temporal Monitor Software Function. ES005A _31 ES005A _31 ES005A _31 ES005A _32 The Temporal Monitor function shall perform initialization test at Warm Init state once per ignition cycle. ES005A _108 ES005A _108 ES005A _31 The Temporal Monitor function shall start Temporal Monitor Initialization when StrtUpSt = ELECGI.BPRM_STRTUPSTTMPLMONININTESTSTRT_CNT_U08 & TmplMonrIninTestCmplFlg = 1. ES005A The Temporal Monitor Function shall generate TmplMonrWdg for 8 periodic execution followed by a		2 Requirements
ES005A _169 The Temporal Monitor function shall provide a mechanism to store its sequence number (TmplMonrIninCntr) into the per-instance menory. ES005A _212 ES005A _126 ES005A _127 ES005A _23 ES005A _24 2.3 Software Requirements ES005A _25 ES005A _26 ES005A _27 ES005A _27 The Temporal Monitor Temporal Monitor Signal Generation _29 ES005A _29 ES005A _29 ES005A _29 ES005A _10 ES005A _10 ES005A _10 The Temporal Monitor function shall generate TmplMonrWdg signal by toggling a GPIO pin from Temporal Monitor Software Function. ES005A _31 ES005A _31 ES005A _31 ES005A _31 ES005A _31 The Temporal Monitor function shall generate TmplMonrWdg signal by toggling a GPIO pin from Temporal Monitor Software Function. ES005A _31 ES005A _31 The Temporal Monitor function shall perform initialization test at Warm Init state once per ignition cycle. ES005A _108 The Temporal Monitor function shall start Temporal Monitor Initialization when StrtUpSt = ELECGLBPRM_STRTUPSTTMPLMONININTESTSTRT_CNT_U08 & TmplMonrIninTestCmplFlg = 1. ES005A The Temporal Monitor Function shall generate TmplMonrWdg for 8 periodic execution followed by a		2.1 Primary Functional Requirements
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ES005A		
		2.3.1.2 Sub Function: Temporal Monitor Initialization
		ELECGLBPRM_STRTUPSTTMPLMONININTESTSTRT_CNT_U08 & TmplMonrIninTestCmplFlg

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ES005A _167	The Temporal Monitor Function shall verify that it has control over PwrOutpEna signal by forcing a fault and monitoring the feedback signal.
ES005A _110	The Temporal Monitor function shall issue a FLASH_MODE command through SPI if re-flash is requested.
ES005A _111	The Temporal Monitor function shall issue a WD_RESTART command through SPI to get out of flash mode once re-programming is done.
ES005A _122	2.3.1.3 Sub Function: Temporal Monitor Run
ES005A _168	The Temporal Monitor function shall increment the internal valid counter value by 1 if 10 subsequent rising edges of 2 ms \pm 0.12 ms square wave pulses are present over a 20 ms moving window after the Temporal Monitor Initialization.
ES005A _125	The Temporal Monitor function shall qualify TmplMonrWdg signal when the internal valid counter value reaches to a predefined SPI configured value.
ES005A _124	The Temporal Monitor function shall continue monitoring and qualifying the TmplMonrWdg signal during the rest of the ignition cycle.
ES005A _145	2.4 Diagnostic Requirements
ES005A _146	2.4.1 Temporal Monitor Init Test Fault (NTC0x040)
ES005A _147	2.4.1.1 Required Debounce Strategy
ES005A _157	The Temporal Monitor function use the Immediate fault strategy for NTC0x040.
ES005A _149	2.4.1.2 Requirements to Perform Diagnostic Test Conditions
ES005A _158	The Temporal Monitor function shall perform the test condition for NTC0x040 during the Temporal Monitor Initialization and only once per ignition cycle.
ES005A _170	The Temporal Monitor function shall perform the test condition for NTC0x040 during the sequence number (TmplMonrIninCntr) 8 to 50.
ES005A _150	2.4.1.3 Test Condition Negative Requirements
ES005A _163	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 8-10 and PwrOutpEna is not High.

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ES005A _171	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 12 and PwrOutpEna is not Low.
ES005A _172	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 13-15 and PwrOutpEna is not High.
ES005A _173	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 16 and PwrOutpEna is not Low.
ES005A _177	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 17 and Watchdog State = Idle or Flash or Test Hunt or Watchdog.
ES005A _176	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 19 and Edge and Valid Counter value is not written properly.
ES005A _175	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 50 and PwrOutpEna is LOW and Watchdog State = Watchdog.
ES005A _178	The Temporal Monitor function shall provide a negative result for NTC0x040, when the sequence number is 50 and PwrOutpEna is LOW and Watchdog State is not Watchdog.
ES005A _151	2.4.1.4 Test Condition Positive Requirements
ES005A _164	The Temporal Monitor function shall provide a positive result to the test condition for NTC $0x040$ when none of the negative result requirements are satisfied.
ES005A _152	2.4.2 Temporal Monitor Run Fault (NTC0x041)
ES005A _153	2.4.2.1 Required Debounce Strategy
ES005A _159	The Temporal Monitor function use the Immediate fault strategy for NTC0x041.
ES005A _154	2.4.2.2 Requirements to Perform Diagnostic Test Conditions
ES005A _160	The Temporal Monitor function shall perform the test condition for NTC0x041 in ENABLE
ES005A _174	The Temporal Monitor function shall perform the test condition for NTC0x041 when the sequesnce number is greater than 50.
ES005A _155	2.4.2.3 Test Condition Negative Requirements

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ES005A _165	The Temporal Monitor function shall provide a negative result for NTC0x041, when the sequence number is 51 PwrOutpEna is LOW and Watchdog State is Watchdog.
ES005A _179	The Temporal Monitor function shall provide a negative result for NTC0x041, when the sequence number is 51 PwrOutpEna is LOW and Watchdog State is not Watchdog.
ES005A _156	2.4.2.4 Test Condition Positive Requirements
ES005A _166	The Temporal Monitor function shall provide a positive result to the test condition for NTC 0x041 when none of the negative result requirements are satisfied.
ES005A _161	2.5 Manufacturing Requirements
ES005A _162	None.