LPCOpen V2-V3 Porting Guide

Overview:

In an effort to simplify code structure and decrease path lengths, LPCOpen has been reorganized to a flatter structure. Additionally, embedded ROM drivers are being deprecated. Deprecated ROM driver functionality is now provided by LPCOpen style drivers. These changes have been made in an effort to improve usability and maintainability of LPCOpen software.



File Organization changes

- Chip Directory
- o Includes have been consolidated and moved from
- o software\lpc_core\lpc_chip\chip_xxxx, software\lpc_core\lpc_chip\chip_xxxx\config, and software\lpc_core\lpc_chip\chip_common to chip_xxxx\inc
- Source has been moved from software\lpc_core\lpc_chip\chip_xxxx and Software\lpc_core\lpc_chip\chip_common to chip_xxxx\src
- o Board Directory
- o Includes have been moved from
- o software\lpc_core\lpc_board\board_common and software\lpc_core\lpc_board\boards_xxxx, to board_name\inc (i.e. lpcxpresso_54102\inc).
- Source has been moved from
- software\lpc_core\lpc_board\board_common and software\lpc_core\lpc_board\boards_xxxx to board_name\src (i.e. lpcxpresso_54102\src)
- Libraries have been moved from software\yyyy to chip_xxxx\libs\yyyy
- Startup files have been moved from applications\lpcxxxxxx\startup to chip_xxxx\startup

Example / Project changes:

- Example source code has been moved from applications\lpcxxxx\examples to examples_xxxx\example_name where example name represents the example project. (i.e. periph_blinky etc).
- Project files have moved from applications\lpcxxxx\tool_vendor\board_name\zzzz to prj_board_name\tool_vendor where board_name represents the board, tool_vendor represents the tool vendor (Keil, IAR, LPCXPresso) and zzzz represents the workspace.
- Keil and IAR projects must define CHIP_LPCxxxx in the project settings.

Power Library API changes:

The ROM based power API's have been deprecated and are replaced by the power library (library specific file name depends on the tool chain vendor). The power library interface is defined in power_lib_xxxx.h. The power library contains optimizations / enhancements that were not available at chip design time and should be used in place of the ROM calls.

How to Reach Us

Home Page: www.nxp.com

Web Support: www.nxp.com/support

USA/Europe or Locations Not Listed:

NXP Semiconductor

Technical Information Center, EL516

2100 East Elliot Road

Tempe, Arizona 85284

+1-800-521-6274 or +1-480-768-2130

www.nxp.com/support

Europe, Middle East, and Africa:

NXPHalbleiter Deutschland GmbH

Technical Information Center

Schatzbogen 7

81829 Muenchen, Germany

+44 1296 380 456 (English)

+46 8 52200080 (English)

+49 89 92103 559 (German)

+33 1 69 35 48 48 (French)

www.nxp.com/support

Japan:

NXP Semiconductor

ARCO Tower 15F

1-8-1, Shimo-Meguro, Meguro-ku,

Tokyo 153-0064, Japan

0120 191014 or +81 3 5437 9125

support.japan@nxp.com

Asia/Pacific:

NXP Semiconductor Hong Kong Ltd.

Technical Information Center

2 Dai King Street

Tai Po Industrial Estate

Tai Po, N.T., Hong Kong

+800 2666 8080

support.asia@nxp.com