



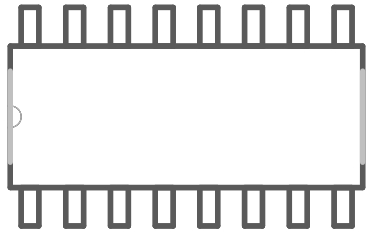
P2

V<sub>-IN</sub>

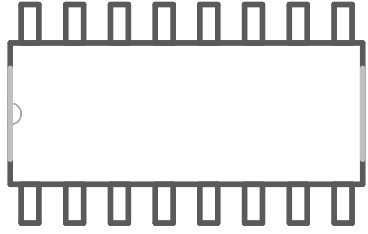
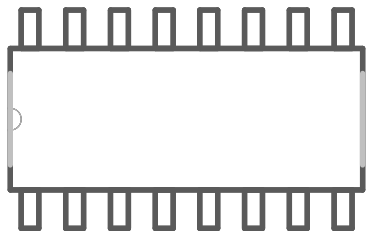
U2

VCC

U3



U1



P3

LP

BP

C1



C2



C3



C4



R1



R2



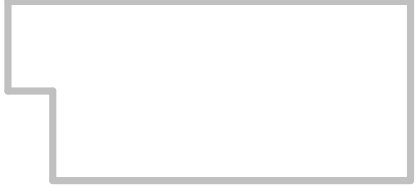
R3



R4



R5

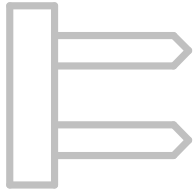


R6



VCC

GND



P1

