

# **CRYSTAL OSCILLATOR (SPXO)**

**OUTPUT: LV-PECL, LVDS** 

# SG3225EAN, VAN SG5032EAN / VAN SG7050EAN, VAN

Achieved wide frequency range by PLL technology and

Fundamental AT crystal units

 Frequency range : 73.5 MHz to 700 MHz Supply voltage 2.5 V to 3.3 V Function Output enable (OE) LV-PECL or LVDS Output





**Product Number** 

SG3225EAN: X1G004251xxxx00 SG3225VAN: X1G004241xxxx00 SG5032EAN: X1G004271xxxx00 SG5032VAN: X1G004261xxxx00 SG7050EAN: X1G004291xxxx00 SG7050VAN: X1G004281xxxx00





SG3225EAN/VAN  $(3.2 \times 2.5 \times 1.05 \text{ mm})$ 

SG5032EAN/VAN

SG7050EAN/VAN  $(5.0 \times 3.2 \times 1.0 \text{ mm})$   $(7.0 \times 5.0 \times 1.4 \text{ mm})$ 

### Specifications (characteristics)

	Symbol	Specifications				
Item		LV-PECL	LVDS	Conditions / Remarks		
item		SG3225EAN / SG5032EAN /	SG3225VAN / SG5032VAN /	Conditions / R	emarks	
		SG7050EAN	SG7050VAN			
Output frequency range	fo	73.5 MHz t	o 700 MHz	Please contact us about available	Please contact us about available frequencies.	
Supply voltage	Vcc	K: 2.5 V - 10 %	to 3.3 V + 10 %			
Storage temperature	T_stg	-40 °C to	+125 °C	Storage as single product.		
Operating temperature	T_use	B: -20 °C to +70 °C,	G: -40 °C to +85 °C			
Frequency tolerance	f_tol	J: ± 50 × 10 <sup>-6</sup> , E: ± 30	0 × 10 <sup>-6</sup> , C: ± 20 × 10 <sup>-6</sup>			
Current consumption	Icc	65 mA Max.	30 mA Max.	OE = Vcc, L_ECL = 50 $\Omega$ or L_LVDS = 100 $\Omega$		
Disable current	I_dis	20 m <i>A</i>		OE = GND		
Symmetry	SYM		p 55 %	At outputs crossing point		
Output voltage (LV-PECL)	Vон	Vcc - 1.0 V to Vcc - 0.8 V	_	DC characteristics		
Output Voltage (EV-1 EOE)	Vol	Vcc - 1.78 V to Vcc - 1.62 V	<u>-</u>			
	Vod	_	250 mV to 450 mV	Vod1, Vod2	DC characteristics	
Output voltage (LVDS)	dVod	_	50 mV Max.	dVod =   Vod1-Vod2		
Cutput voltage (EVDC)	Vos	_	1.15 V to 1.35 V	Vos1, Vos2		
	dVos	_	150 mV Max.	dVos =   Vos1-Vos2		
Output load condition	L_ECL	50 Ω	_	Terminated to Vcc -2.0 V		
(ECL) / (LVDS)	L_LVDS		100 Ω	Connected between OUT to OUT		
Input voltage	VIH	70 % Vcc Min.		OE terminal		
put romago	VIL	30 % Vcc Max.				
Rise time / Fall time	tr / tf	350 ps Max.	300 ps Max.	LV-PECL: Between 20 % and 80 LVDS: Between 20 % and 80		
Start up time	t str	2 ma May		peak to peak voltage		
Start-up time		3 ms Max.		Time at minimum supply voltage to be 0 s		
Phase Jitter	tpJ	0.6 ps Max.*1		Offset frequency: 12 kHz to 20 MHz		
Frequency aging	f_age	$\pm$ 5 $ imes$ 10 <sup>-6</sup> / year Max.		+25 °C, First year, Vcc = 2.5 V, 3.3 V		

\*1 0.9 ps Max. (fo = 243 MHz ~ 250 MHz, 486 MHz ~ 500 MHz)

**Product Name** (Standard form) SG3225 E AN 156.250000MHz K J G A

(56: CG is not available)

(4)(5)(6)(7)②Output (E: LV-PECL, V: LVDS)

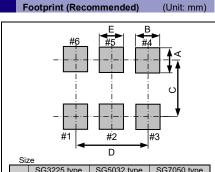
③Frequency ④Supply voltage ⑤Frequency tolerance ⑥Operating temperature ⑦Internal identification code ("A" is default)

Supply voltage		
K	2.5 V ~ 3.3 V	

⑤Frequency tolerance		
J	±50 × 10 <sup>-6</sup>	
Е	±30 × 10 <sup>-6</sup>	
С	±20 × 10 <sup>-6</sup>	

⑥Operating temperature		
В	-20 °C ~ +70 °C	
G	-40 °C ~ +85 °C	

#### External dimensions



_ {	Size				
	S	G3225 type	SG5032 type	SG7050 type	
Α		1.05	1.60	2.00	
В		0.86	0.89	1.80	
С		1.85	2.60	4.20	
D		2.58	2.54	5.08	
Е		0.82	0.89	1.80	
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To maintain stable operation, provide a 0.01  $\,\mu F$ to 0.1 µF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

External difficults		(Unit: mm)
7.0±0.2 #6 #5 #4 SG7050 #1 #2 #3	3.0±0.2 5.0±0.2 1.0±0.2 #6 #5 #4 SG5032 #1 #2 #3	70 #6 #5 #4 SG3225 #1 #2 #3
5.08 September 1.4 September 1	Note. OE pin = "H" or "open" : Specified frequency output. OE pin = "L" : Output is high impedance.	Pin Name Pin Name #1 OE #4 OUT #2 N.C. #5 OUT #3 GND #6 VCC

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



► Complies with EU RoHS directive.

\*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.

(Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc.).

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