## \\ecfile1.uwaterloo.ca\e2adam\My Documents\GitHub\MTE241 RTOS\src\ kernelCore.h

```
KERNELCORE H
2
3
    #include <stdint.h>
4
    #include <stdio.h>
5
    #include <LPC17xx.h>
6
    #include <stdbool.h>
    #define SHPR3 *(uint32_t*)0xE000ED20 //location of the PendSV priority register
8
     #define SHPR2 *(uint32 t*)0xE000ED1C // location of SVC priority register
9
10
    #define ICSR *(uint32_t*)0xE000ED04 //location of the ISCR
11
12
    void kernelInit(void); //initialize memory structures and interrupts necessary to run the kernel
13
14
    bool osKernelStart(); //start running the kernel, i.e. the OS
15
    void osLoadFirst(); //called by the kernel to start running the very first thread, before getting into
16
    context switching
17
18
    void osYield(void); //called by the kernel to signal for the next thread to be scheduled
19
20
    void osSleep(int sleepTime); //called by kernel to set the non-periodic thread to sleep and signal for
    the next thread to be scheduled
21
22
    void SysTick Handler(void); //interrupt called every ~1ms to decrement timers and potentially pre-empt
     the running thread
23
24
    void scheduler(void); //decides which thread to run next, based off EDF logic
25
    void SVC Handler Main(uint32 t *svc args); //interrupt containing yield code
26
27
28
    int task switch(void); //called by the PendSV interrupt to set PSP to the next thread scheduled to run
29
30
31
32
    #endif
```