Test 2 • Graded

Student

Adam Fenjiro

Total Points

70 / 100 pts

Question 1

Semaphore Basic Operation

18 / 18 pts

- → + 3 pts Line 2: SemA -> -1; Waiting Set semA -> {Pc}; no other changes
- → + 3 pts Line 3: SemA -> 0; Waiting Set SemA -> {}; Resumed Thread -> {Pc}; no other changes
- → + 3 pts Line 4: SemB-> -1; Waiting Set SemB -> {Pc}; no other changes
- → + 3 pts Line 5: SemB-> -2; Waiting Set SemB -> {Pc,Pa}; no other changes
- → + 3 pts Line 6: SemB ->-1; Waiting Set SemB -> {Pc} | | {Pa}; Resumed thread = {Pa,Pc}
 - + 0 pts Entirely incorrect or no answer

Question 2

Semaphore Atomic

12 / 12 pts

- - + 0 pts Anything else

Question 3

Unprotected Update

Resolved 0 / 12 pts

- + 4 pts Gives a complete and valid execution of the system that runs until all processes have either completed or are blocked
- + 4 pts Recognizes that atomic instructions must be interleaved to produce the identified execution
- + 4 pts Gives a valid execution that produces the output AA



Thread does not loop

C Regrade Request

Submitted on: Mar 20

50% of the execution is correct. Starting by the wait(gob), then moving to the other process to print('A'). I understand that the other half is wrong, but 0/12 is like someone that did not answer or had it completely off. Could you please check this one out? Thanks!

The point of this question is to recognize that sometimes you need to go down to the instruction level to encounter a race condition. The execution is not only incorrect (even though it is technically correct for the first part), in its incorrectness you miss the point of the question, and instead answer a much more trivial question. You're answering the wrong question, if that makes sense, and I can't really give you points for that.

Reviewed on: Mar 21

Ouestion 4

Fast Runner

3 / 12 pts

- + 12 pts Entirely correct
- + 3 pts Indicates the solution is incorrect
 - + 3 pts Gives a valid execution
 - + 6 pts Execution shows a process completing phase i+1 before all processes have complete phase i
 - + 0 pts Wrong answer and no valid execution or no answer
- What is the problem? This does not show a violation of the ordering. mutex has to be signaled by P1 before P2 or P3 can go.

Question 5

Bad Wait in a Mutex

9 / 12 pts

- - + 3 pts Identifies deadlock from wait in the mutex
- - + 0 pts Indicates solution is correct



Missing a counter example

- - + 3 pts AABB not possible
 - + 3 pts BBAA not possible
 - + 3 pts BAAB possible
 - + 3 pts Deadlock not possible
 - + 0 pts All answers incorrect or no answers

Question 7

Create Simple Synchronization

14 / 14 pts

- - + 0 pts No solution or solution updates shared variables non-exclusively

Question 8

Modification of Existing Solution

2 / 8 pts

- +8 pts Entirely correct
- + 2 pts Gives a well specified solution
- + 6 pts Gives a valid efficient solution
- + 1 pt Disallows inserter and deleter to run at the same time, but not currently with searchers
- + 0 pts No solution, poorly specified solution, incorrect solution.
- + 3 pts Valid solution but concurrency significantly diminished,
- Since deleter does wait on mutex, this process can run concurrently with a deleter.

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CS 3331 Exam 2 Spring 2024 SOLUTIONS

MAKE SURE THAT YOUR COPY OF THE SOLUTION SHEET HAS 8 PAGES. You may add additional pages using the supplied paper (only). Be sure to put your name and a page number on each sheet that you add. Please only use a landscape layout for your answers. Otherwise, it increases the grading time unnecessarily.

1. [18 points]

P_{A}	P _B	P_C	SemA	Sem B	Waiting Set SemA	Waiting Set Sem B	Resumed Thread
		inger er (1957)	1	-0	{}	{} :	
	Wait(SemA)		0		33 -	33	Ber (9 10 10 10 10 10 10 10 10 10 10 10 10 10
		Wait(SemA)	-1		3PC3	33	
Signal(SemA)			0		33	33	PC
		Wait(SemB)		-1	33	3963	
Wait(SemB)		1.5 11:4:		-2	33	3PC/PAS	
	Signal(SemB)			-1	33	3PC3 or 3PA3	PC oc PA

2. [12 points]

Thread A instructions	Register	Thread B inhrustions	Register	S.count	S waiting set
	The state of the s	10000000000000000000000000000000000000		-1	{W}
LOAD regisky, s.count	-1				
ADD regisku, #1	0		7		T
STORE regislar, s. count				0	
		LOAD regiska, s. wunt	0	4-1	
		ADD (egislar, #1			
		STORE (egisla, s. count		1	
if (S. Lount 6=0) 13					3W3
11 resumes					
	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	if (s.count<=0) 33		1	1W3
		// resumes			
					3W3
	4.7 1 1 1 1 1 1				
	L				

3. [12 points]

Thread A_1	Thread A ₂	Thread B ₁	count	output
			Ð	
Marie 1 3 1 1 1 1 2		wait (gob)		
count ++				
count ++ if(count == 2)				1
6/26				THE
Write ("A")				1A1
	if (camt = -2)	it are entre entre	2	
	if(count = = 2)			
The section	Signal(gob)			59 Y - 59
	wait (bdone)			Esa
	Count =0	2 Mary Learning	0	41.
comt tt				
comb tt $iF(conb == 2)$				
else				1.44
else White ("A")				AA
1.52 T 1.20 M			747.7	
A THE PLANT A.			HY A.D	
M WIN SEE !!			41-71-1	

No, the solution is not correct. the problem here is the mutex that is signaled on line 07.

Py instructions	P2 instructions	P3 instructions	count
rigit/Anuley			10
CONC ++:			
wait (mulex) cont ++: if (count <3) 43			
	wait (mutex)		
	Wait (mutex) count ++ if (count < 3) 13		2
		wait (mukx	
		Whit (mutex count ++ if (count <3) else 23	3
•			
		/problem line 7	

No, the solution is not correct. the mutex is misplaced here. Here is a proposed solution:

Increment:

wait (inverment);
wait (mukex);
count ++;
signal (decrement);
Signal (mutex);

decrement:

wait (decrement);
wait (mutex);
count --;
Signal (increment);
signal (mutex);

The thing with the original solution and why it was incorrect is that they don't and won't have a matching pace on decrement and increment, where one of them is conciderably Pasks. My solution, thus, is correct.

6. [12 points]

Type A Processes

wait(A)
wait(A)
write("A")
signal(A)
signal(B)

Semaphore	Initial Value		
A	2		
В	1		

Possible	Impossible
	X
	X
X	
	X
	Possible

Type B Processes

wait(B)
write("B")
signal(A)

```
7. [14 points] 'AABB'
       semaphone A=1, B=0, mulex=1
        shared count = 0;
       Thread A
                                                       Thread B
                                                while (1=1) {
while (1==1) }
                                                      wait (B)
      wait (A);
                                                       wait ( mulex)
       weit (mmex);
                                                        count tti
        count ++;
       write("A");
                                                      if (count == 2) 3
     if(caunt == 2)?
       Girofod (SE))
                                                          count =0;
          count = 0,
                                                          signal (mulex);
          signal(muter);
                                                           Signal (A);
          Signal (B);
                                                        else 3 signal (mulea);
signal (B);
       else?
signal (mulex);
signal (A);
                                                Page 7 of 8
                                                         3
```

insorters:

while (1) 2 newtoods // we muse to reach the end of the while (1) 2 newtood // we muse to reach the end of the singly unked-list and make sure only and make sure only one insorteon can proceed.

if (count ==0) rection)

righal (musex);

// add huo nodes

Insorters add new modes at the end of the list while mutually eatherive to prelide two invertes from insorting new modes at the same time, and one invertes from insorting new modes at the same time, and one invertes from panallel with any number of severches.

This solution should be able proposed in panallel with any number of severches.